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Acceleration, Heterogeneity and Programming for Accelerators - Lecture 1

Friday, 20 June 2014 09:00 (1 hour)

Affordable and efficient accelerators are becoming a standard data-center offering. However, programming these devices requires a shift to a heterogeneous approach, discussed here, where data access between nodes can be expensive and floating point answers obtained on different hardware can diverge. In particular, we will cover the up- and down-sides of direct, offload and symmetric acceleration. GPU and Many Integrated Core devices will be used as examples.

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Session Classification: Acceleration