

ACES 2014 - Fourth Common ATLAS CMS Electronics Workshop for LHC Upgrades

Wednesday 19 March 2014

Posters: exposed during 3 days - 222/R-001 (18:10 - 18:42)

time	[id] title	presenter
18:10	[60] A New High-Speed Optical Transceiver For Data Transmission at the LHC Experiments	PARAMONOV, Alexander
18:12	[61] The AMC13XG: A New Generation Clock/Timing/DAQ Module for (CMS) MicroTCA	HAZEN, Eric Shearer
18:14	[62] BCM1F Backend Electronics for Beam Conditions and Luminosity measurement	ZAGOZDZINSKA, Agnieszka Anna
18:15	[63] Testing Integrated COTS DC/DC Converters in Hostile Environment	LANZA, Agostino
18:16	[64] First Test Beam measurements of BCM1F back-end phase 1 upgrade, using the FMC125 Fast Digitizer	BURTOWY, Piotr Rafal
18:17	[65] Electronics Challenges for HL-LHC pileup Mitigation with HyperFast Timing	WHITE, Sebastian
18:18	[66] Design of a Front-end ASIC for Single Crystal Diamond Sensors	PRZYBOROWSKI, Dominik Wladyslaw
18:19	[67] The GBT-FPGA project	BARROS MARIN, Manoel
18:20	[68] Radiation-Hard Power Electronics for the ATLAS New Small Wheel	EDGAR, Ryan Christopher
18:21	[69] Proton irradiation test of an SRAM FPGA for the possible usage in the readout electronics of the LHCb experiment	FAERBER, Christian
18:22	[70] Poster 11	