



Contribution ID: 28

Type: **Oral presentation**

Design and Assembly Studies for Track Trigger Modules

Thursday 15 May 2014 11:30 (30 minutes)

High luminosity upgrade of the LHC will require that tracking detectors participate in the lowest levels of trigger decisions. The implementation of such track-trigger logic systems will necessitate local processing of information and sparsification of data transmitted to global processors. Further, such a system will require dense interconnections between various sensors, readout electronics and local trigger logic. We will describe R&D efforts aimed at optimizing module concepts and mechanical designs that realize this functionality. We will also describe interconnect technologies that will be employed in such assemblies. Progress in prototyping of modules will be presented.

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