



Contribution ID: 107

Type: Oral

FSBB-M and FSBB-A: Two Large Scale CMOS Pixel Sensors Building Blocks Developed for the Upgrade of the Inner Tracking System of the ALICE Experiment

Tuesday 23 September 2014 11:10 (25 minutes)

Two CMOS Pixel Sensors (CPS) flavours: MISTRAL and ASTRAL, dedicated to the upgrade of the Inner Tracking System (ITS) of the ALICE experiment are being designed at IPHC in Strasbourg. Each of two sensors is composed of three identical units called FSBB (Full Scale Building Block), multiplexed towards the external world. This paper will show the design and the laboratory test results of FSBB-M (for MISTRAL) and FSBB-A (for ASTRAL).

Summary

The ALICE-ITS upgrade requires $50\ \mu\text{m}$ thin CPS covering about $10\ \text{m}^2$ detection areas. These CPS should have higher read-out speeds and radiation tolerance than those already achieved in the Pixel Detector (PXL) equipping the STAR experiment at RHIC/BNL. These requirements are the main motivation to migrate from a $0.35\ \mu\text{m}$ process used for the development of MIMOSA28 dedicated to the STAR-PXL detector to a smaller feature size process. A $0.18\ \mu\text{m}$ process, based on a high resistivity epitaxial layer of sizeable thickness, has therefore been chosen for these new devices. [Ref. TWEPP 2013, Id: 62]

After validation of the process, two sensor architectures, called MISTRAL and ASTRAL, are being developed at IPHC. MISTRAL is derived from MIMOSA28 sensor. It is based on a column parallel read-out with amplification and correlated double sampling (CDS) inside each pixel. Each column is terminated with two high precision discriminators in order to read out 2 rows simultaneously. The matrix is read out in a rolling shutter mode ($200\text{ns}/2\text{-rows}$). The discriminator outputs are processed through an integrated zero suppression logic (SUZE02). With this mature architecture, MISTRAL, with its $\sim 1.3 \times 3\ \text{cm}^2$ sensitive area, is currently developed to suit both the inner and outer layers of the ITS. The MISTRAL sensor for the inner layers will provide a single point resolution of $\sim 5\ \mu\text{m}$ with a power consumption of $\sim 185\ \text{mW}/\text{cm}^2$ and a readout frequency of $\sim 25\ \text{k}\ \text{frame/s}$. In order to reduce the power density for the outer layers, the MISTRAL sensor for the outer layers will rely on larger pixels to provide a single point resolution of $\sim 10\ \mu\text{m}$. The power consumption diminishes to $\sim 100\ \text{mW}/\text{cm}^2$ while the readout frequency increases to $40\text{-}50\ \text{k}\ \text{frame/s}$.

ASTRAL provides a further improvement in terms of power consumption and readout speed. It integrates signal discrimination inside each pixel. As a consequence, the analogue signals driving over centimetre long traces are replaced by the digital signals. The ASTRAL sensor is proposed for the innermost layers with a single point resolution of $\sim 5\ \mu\text{m}$. Its expected power consumption is $\sim 85\ \text{mW}/\text{cm}^2$ with a read-out frequency of $\sim 50\ \text{k}\ \text{frames/s}$.

The first prototypes of FSBB-M and FSBB-A were designed and fabricated. Their tests with X-ray and beta sources will start in June 2014.

This contribution will discuss in details the design of both sensors and summarise their laboratory test results.

Authors: Dr HU-GUO, Christine (IPHC-IN2P3, UDS); Dr MOREL, Frederic (IPHC-IN2P3, UDS); Dr PHAM, Yhanh Hung (IPHC-IN2P2, UDS)

Presenter: Dr MOREL, Frederic (IPHC-IN2P3, UDS)

Session Classification: ASICs

Track Classification: ASICs