

Session Program

22-26 Sept 2014



TWEPP 2014 - Topical Workshop on Electronics for Particle Physics

ASICs

Centre des Congrès - Aix en Provence, France
14 boulevard Carnot 13100

Tuesday 23 September

09:50

ASICs: A1a

Session | **Location:** Centre des Congrès - Aix en Provence, France, Amphi Cezanne, 14 boulevard Carnot 13100 | **Convener:** Christophe De La Taille

09:50-10:15

TDCPix: Tracking for the NA62 GigaTracker

Speaker

Matthew Noy

10:15-10:40

The ToPiX v4 Prototype for the Triggerless Readout of the PANDA Silicon Pixel Detector

Speaker

Giovanni Mazza

10:40

11:10

ASICs: A1b

Session | **Location:** Centre des Congrès - Aix en Provence, France, Amphi Cezanne, 14 boulevard Carnot 13100 | **Convener:** Christophe De La Taille

11:10-11:35

FSBB-M and FSBB-A: Two Large Scale CMOS Pixel Sensors Building Blocks Developed for the Upgrade of the Inner Tracking System of the ALICE Experiment

Speaker

Dr Frederic MOREL

11:35-12:00

Front End Electronics for SOI Monolithic Pixel Sensor

Speaker

Toshinobu Miyoshi

12:00

14:50

ASICs: A2

Session | **Location:** Centre des Congrès - Aix en Provence, France, 14 boulevard Carnot 13100 | **Convener:** Mr Marcus Julian French

14:50-15:15

Characterization of Depleted Monolithic Active Pixel Sensor (DMAPS) Prototypes

Speaker

Theresa Obermann

15:15-15:40

The ABC130 ASIC Design for the ATLAS Silicon Strip Upgrade

Speaker

Francis Anghinolfi

15:40-16:05

A Dedicated Front-end for Readout of Strip Detectors in the LHCb Upgrade Experiment

Speaker

Marek Idzik

16:05

Wednesday 24 September

09:50

ASICs: A3a

Session | **Location:** Centre des Congrès - Aix en Provence, France, 14 boulevard Carnot 13100 | **Convener:** Alessandro Marchioro

09:50-10:15

PACIFIC: A 128 Ch ASIC for Scintillating Fiber Tracking in LHCb Upgrade

Speaker

David Gascon

10:15-10:40

CLARO-CMOS: a Fast, Low Power and Radiation-hard Front-end ASIC for Single-photon Counting in 0.35 Micron CMOS Technology

Speaker

Paolo Carniti

10:40

11:10

ASICs: A3b

Session | **Location:** Centre des Congrès - Aix en Provence, France, 14 boulevard Carnot 13100 | **Convener:** Alessandro Marchioro

11:10-11:35

SPIROC and TRIROC, Design and Performance of Dedicated Very Front-End for SiPM

Speaker

Ludovic Raux

11:35-12:00

Low Noise 4-channel Front End ASIC with On-Chip DLL for the Upgrade of the LHCb Calorimeter

Speaker

Eduardo Picatoste Olloqui

12:00

14:00

ASICs: A4

Session | **Location:** Centre des Congrès - Aix en Provence, France, 14 boulevard Carnot 13100 | **Convener:** Christophe De La Taille

14:00-14:25

Development of Front-end Electronics for LumiCal Detector in CMOS 130 nm Technology

Speaker

Jakub Moron

14:25-14:50

High-speed, High-resolution, Radiation-tolerant SAR ADC for Particle Physics Experiments

Speaker

Prof. Yun Chiu

14:50-15:15

VMM2 - An ASIC for the New Small Wheel

Speaker

Gianluigi De Geronimo

15:15-15:40

A Radiation Hardened TDC with < 10 ps Resolution and Improved Recovery Time from Single Events in 40 nm CMOS

Speaker

Mr Jeffrey Prinzie

15:40-16:05

Design of Bandgap Reference Circuits in a 65 nm CMOS Technology for HL-LHC Applications

Speaker

Gianluca Traversi

16:05

Thursday 25 September

09:49

ASICs: A5a

Session | **Location:** Centre des Congrès - Aix en Provence, France, 14 boulevard Carnot 13100 | **Convener:** Mr Marcus Julian French

09:49-10:14

FEAST2: a Production Grade 10W Radiation Tolerant DC/DC Converter

Speaker

Federico Faccio

10:14-10:39

Development of a Lower Power 5.12 Gbps Data Serializer and Wireline Transmitter Circuit for VeloPix Chip

Speaker

Mr Vladimir Gromov

10:40

11:10

ASICs: A5b

Session | **Location:** Centre des Congrès - Aix en Provence, France, 14 boulevard Carnot 13100 | **Convener:** Mr Marcus Julian French

11:10-11:35

8-Gbps-per-channel Radiation-tolerant VCSEL Drivers for the LHC Detector Upgrade

Speaker

Ms Xiaoting Li

11:35-12:00

A 10 Gb/s Laser Driver in 130 nm CMOS Technology for High-energy Physics Applications

Speaker

Ping Gui

12:00