

TWEPP 2014 - Topical Workshop on Electronics for Particle Physics

Tuesday, September 23, 2014

First Poster Session (4:30 PM - 6:30 PM)

-Conveners: Ken Wyllie

time	[id] title	presenter
4:30 P	M138] ABC130 Test System and Wafer Probe Results	PHILLIPS, Peter
4:33 P	M140] Dream: a 64-channel Front-end Chip with Analogue Trigger Latency Buffer for the Micromégas Tracker of the CLAS12 Experiment.	Dr FLOUZAT, Christophe
4:34 P	M138] The Front-end Electronics for the 1.8-kchannel SiPM Tracking Plane in the NEW Detector	Mr RODRIGUEZ SAMANIEGO, Javier
4:35 P	M137] A Muon Trigger Upgrade with High Transverse Momentum Resolution for the ATLAS Detector at the High-Luminosity LHC	HORII, Yasuyuki
4:36 P	M136] Development of a Multi-points Wireless Temperature Monitoring System for Optohybrid for CMS GEM Project	YANG, yifan
4:37 P	M140] The ATLAS FTK Auxiliary Card: A Highly Functional VME Rear Transition Module for a Hardware Track Finding Processing Unit	KRIZKA, Karol
4:38 P	M138] 6 bit, Low Power SAR ADC for CBM-MUCH with Maximum Sampling Rate 50 MS/s	OSIPOV, Dmitry
4:39 P	M139] A Simulation Framework for the Track Trigger System for the CMS Upgrade	AMSTUTZ, Christian
4:42 P	M144] NUCLEON ASIC and Ladder Electronics for Cosmic Ray Experiment	ATKIN, Eduard
4:43 P	M170] The eCDR-PLL IC, a Radiation-Tolerant ASIC for Clock and Data Recovery and Deterministic Phase Clock Synthesis	VICENTE LEITAO, Pedro Miguel
4:45 P	M177] High Reliability DC/DC Converter Module for Electronic Boards Equipped with FPGAs	SIRVENT BLASCO, Jose Luis
4:46 P	M128] A CMOS Pixel Sensor Prototype for the Outer Layers of Linear Collider Vertex Detector	Dr ZHANG, Liang
4:47 P	M143] Development of the Readout System for Triple-GEM Detectors for the CMS Forward Muon Upgrade	DE LENTDECKER, Gilles
4:48 P	M140] Clock and Timing Distribution in the LHCb Upgraded Detector and Readout System	ALESSIO, Federico
4:49 P	M145] The CMS Hadron Calorimeter Detector Control System Upgrade	SAHIN, Mehmet Ozgur
4:50 P	M141] A New Generation of Charge Integrating ADC (QIE) for the CMS HCAL Upgrade	HIRSCHAUER, Jim
4:51 P	M149] A Software Package for the full GBT Chipset Lifecycle	FEGER, Sebastian Stefan
4:53 P	M146] Low power Analog Digital Converter for a Silicon Photomultiplier Readout ASIC	BRIGGL, Konrad
4:54 P	M147] Flexible Front-End Hybrids for the CMS Outer Tracker Upgrade	KOVACS, Mark Istvan
4:55 P	M149] A Low-latency and Low Overhead Encoder ASIC for the Serial Data Transmission in ATLAS LAr Calorimeter Readout Upgrade	GONG, Datao

4:56 P	[M5] Versatile ASIC for L0 Triggering in Cherenkov Telescopes	GASCON, David
4:57 P	[M24] FlashCam: A Novel Cherenkov Telescope Camera with Continuous Signal Digitization	Dr GADOLA, Arno
4:58 P	[M1] Proposed FPGA Based Tracking for a Level-1 Track Trigger at CMS for the HL-LHC	POZZOBON, Nicola
4:59 P	[M02] Design of Low-Power, Low-Voltage, Differential I/O Links for High Energy Physics Applications	TRAVERSI, Gianluca
5:00 P	[M29] The GBT-SCA, a Radiation Tolerant ASIC for Detector Control and Monitoring Applications in HEP Experiments	CARATELLI, Alessandro
5:01 P	[M3] Building Blocks X-fab SOI 0.18 μ m	Mr CIZEL, Jean-Baptiste
5:02 P	[M01] First Results of the Belle II Silicon Vertex Detector Readout System	Dr FRIEDL, Markus
5:03 P	[M9] 100Gbps PCI-express Readout for the LHCb Upgrade	DURANTE, Paolo
5:04 P	[M05] Failure Analysis Results and Lessons Learned on LHC Experiments Crate and Power Supply Equipment	MICO, Sylvain
5:05 P	[M9] A Generic Firmware Core to Drive the Front-End GBT-SCAs for the LHCb Upgrade	CAPLAN, Cairo
5:06 P	[M5] The development of a general purpose ARM-based Processing Unit for the ATLAS TileCal sROD	COX, Mitchell A.
5:07 P	[M7] CLIC-ACM: Generic Modular Rad-Hard Data Acquisition System Based on CERN GBT Versatile Link	MAGNONI, Stefano
5:08 P	[M3] The MuPix Monolithic Active Pixel Sensor for the Mu3e Experiment	Dr WIEDNER, Dirk
5:09 P	[M8] Applications of Cascaded Phase Lock Loop (PLL) Blocks inside Field Programmable Gate Array (FPGA)	Dr WU, Jinyuan
5:10 P	[M0] Extensions for the Rear Side of MicroTCA.4 Systems	ROSNER, Annika
5:11 P	[M17] Radiation Testing Techniques and Results For the ATLAS TileCal Upgrade	DRAKE, Gary
5:12 P	[M4] DDL, the ALICE Data Transmission Protocol and its Evolution from 2 to 6 Gb/s.	COSTA, Filippo
5:13 P	[M9] The Synchronization of CMS ECAL Trigger Primitives via Optical Links	RASTEIRO DA SILVA, Jose Carlos
5:14 P	[M4] Progress Towards the First Prototype of a Silicon Tracker Using an 'Artificial Retina' for Fast Track Finding	ABBA, Andrea
5:15 P	[M03] Global Trigger Upgrade Firmware Architecture for the Level-1 Trigger of the CMS Experiment	Dr RAHBARAN, Babak
5:16 P	[M11] MMC Implementation for the MTCA Devices Used in X-FEL	Dr SZEWINSKI, Jaroslaw
5:17 P	[M72] Development of a Five Channel, 12 bit, 800 MSPS μ TCA-Based Digitizer For the Muon g-2 Experiment at Fermilab	RIDER, Nate
5:18 P	[M2] The CMS Level-1 Calorimeter Trigger Electronics System for the LHC Run II	ROMANTEAU, Thierry