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RD-53 Progress on High Rate Pixel Readout chip

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This talk will introduce the RD-53 collaboration and focus on on-going work in defining the next generation pixel readout chip for the ATLAS and CMS experiments. Will focus on particular on the issue on high data hit rate with MHz trigger rate readout. Will cover issues and possible solutions for internal data flow within the chip, which impacts layout, and options for data compression.

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