

International Workshop on Semiconductor Pixel Detectors for Particles and Imaging (PIXEL2014)

Wednesday, 3 September 2014

Pixel Front End Electronics Development (09:00 - 13:00)

-Conveners: Rainer Wallny

time	[id] title	presenter
09:00	[210] Participant Information	TAYLOR, Wendy
09:10	[175] RD-53 Progress on High Rate Pixel Readout chip	GARCIA-SCIVERES, Maurice
09:40	[192] The CMS Pixel Readout Chip for the Phase 1 Upgrade	HITS, Dmitry
10:10	[165] VeloPix: The Pixel ASIC for the LHCb VELO Upgrade	POIKELA, Tuomas
10:40	Coffee	
11:00	[141] TDCpix - Pixel Read-out ASIC with 100 ps Time-tagging Capability for the NA62 Gigatracker Experiment	NOY, Matthew
11:30	[137] Low power, high resolution MAPS for particle tracking and imaging	GIUBILATO, Piero
12:00	[156] Evaluation of SOI Pixel Detector with Charge Sensitive Amplifier Circuit for Event-Driven X-ray Readout	TAKEDA, Ayaki
12:30	[136] Novel Active Signal Compression in Low-noise Analog Readout at Future XFEL Facilities	MANGHISONI, Massimo