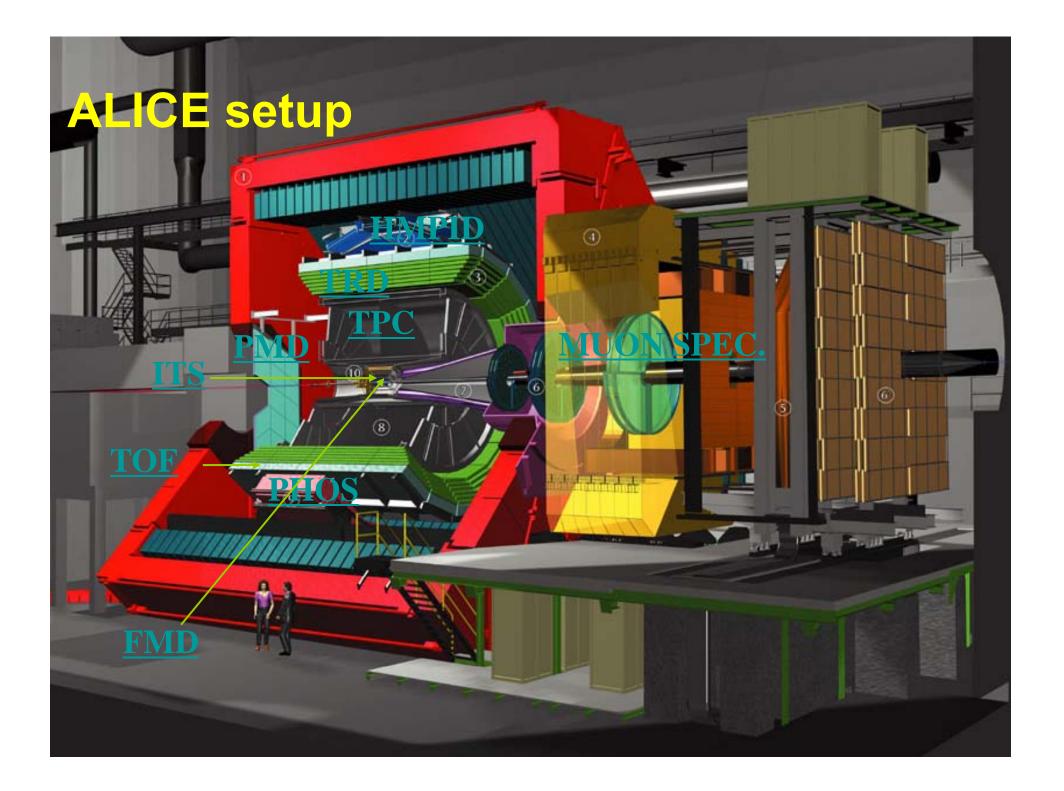
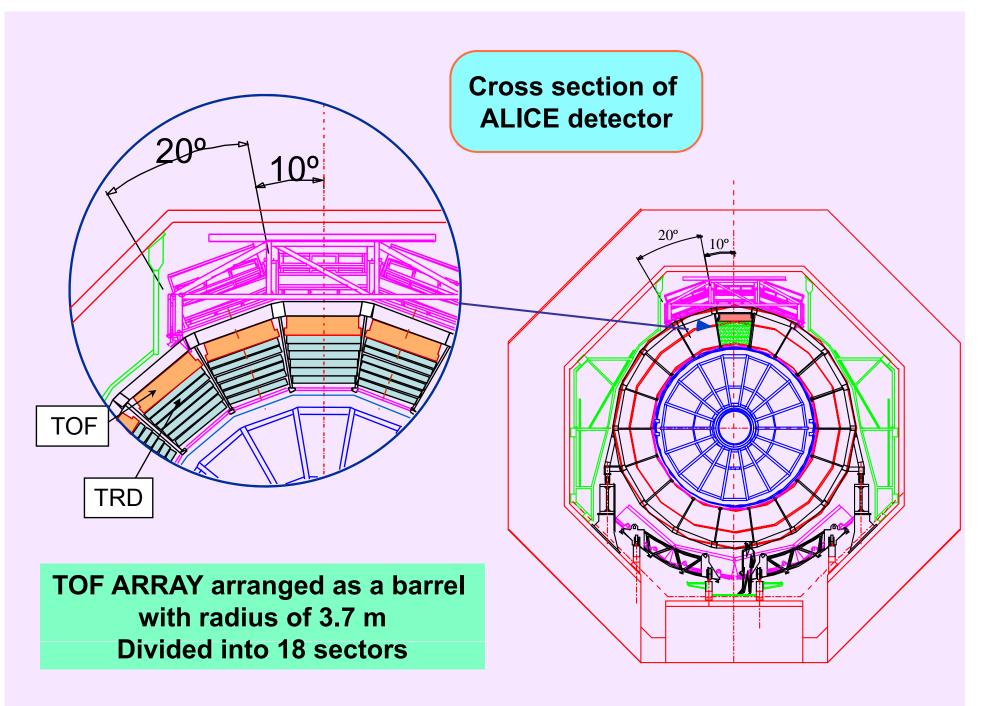
THE ALICE TIME OF FLIGHT AND THE MULTIGAP RESISTIVE PLATE CHAMBER

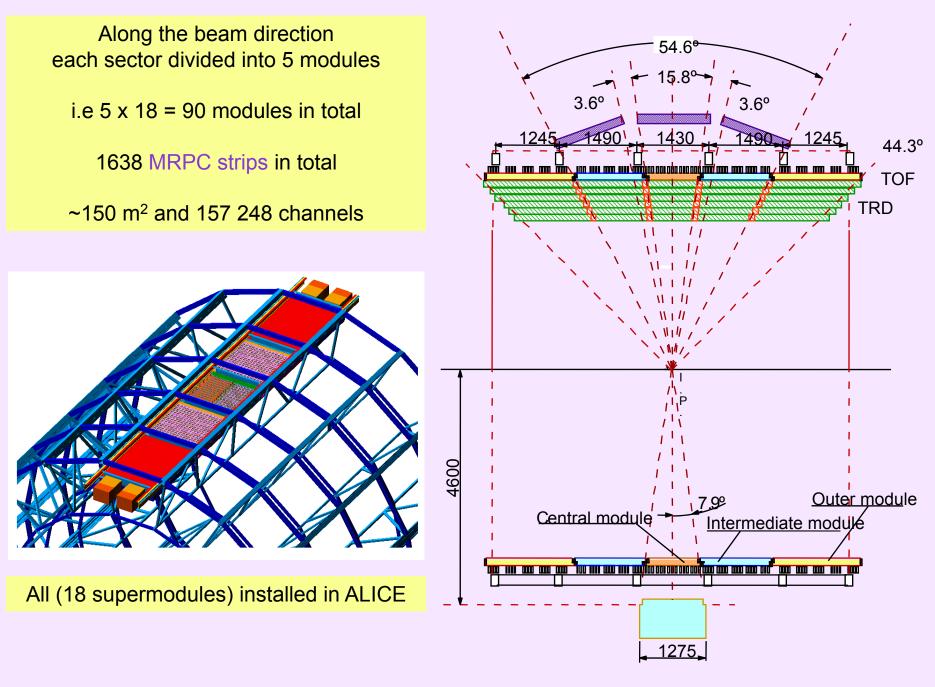
Despina Hatzifotiadou INFN Bologna

- •The ALICE TOF system
- •The Multigap Resistive Plate Chamber strip (detector element)
- Performance
- Electronics
- The TOF as a trigger device
- Further R&D : the 24 gap MRPC (20 ps time resolution)
- Implementation of MRPCs in DHCAL

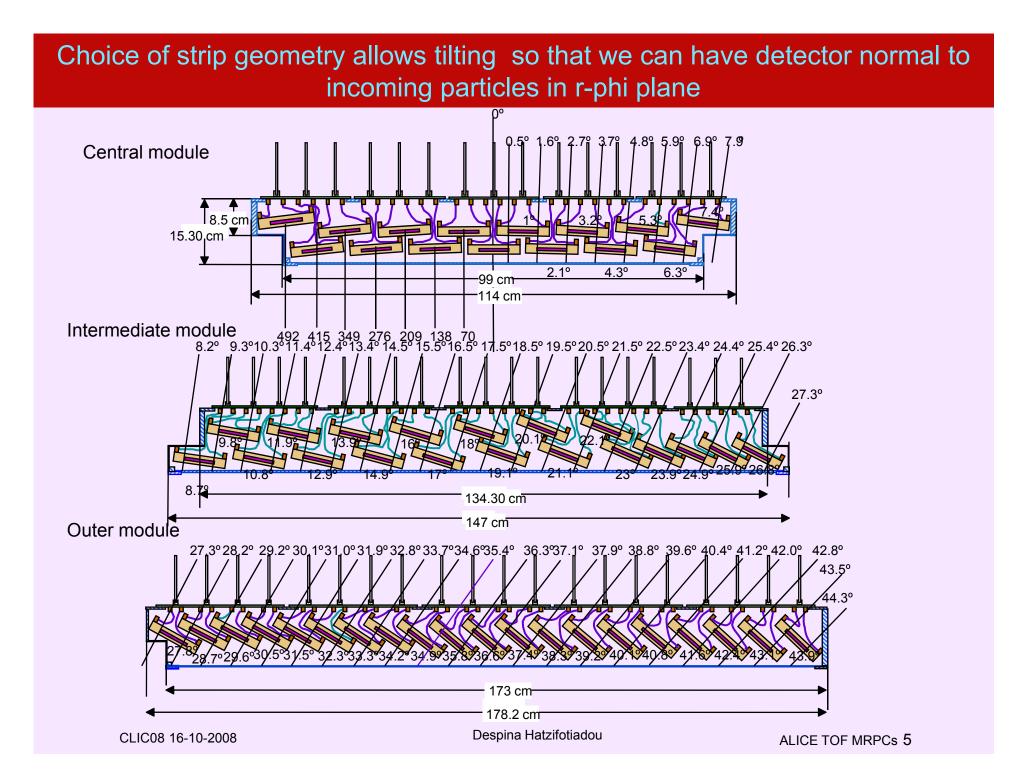


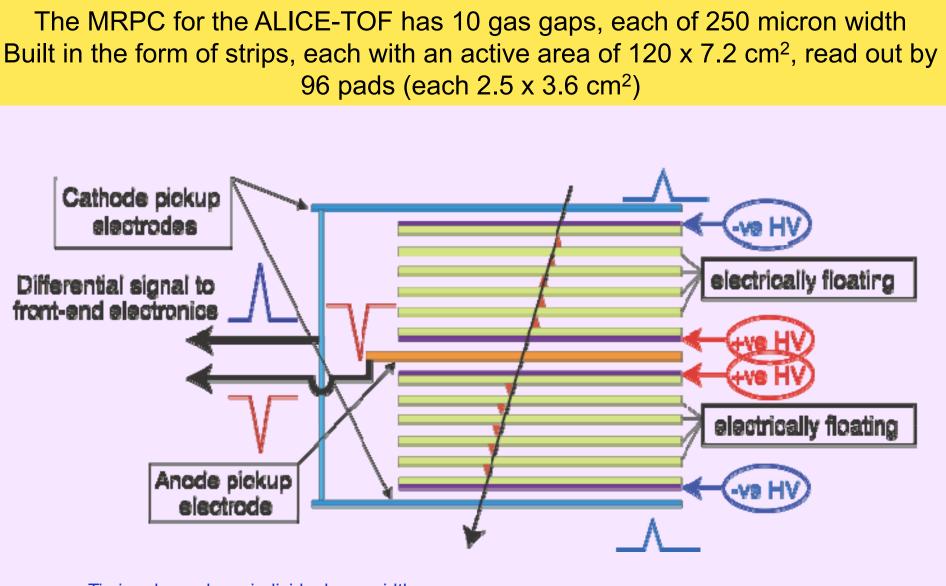


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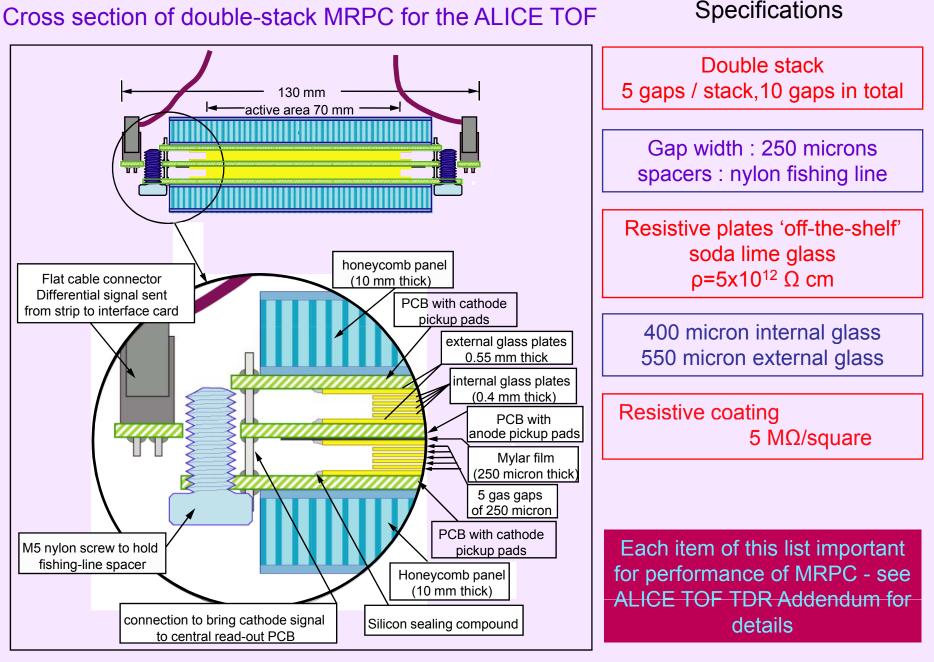


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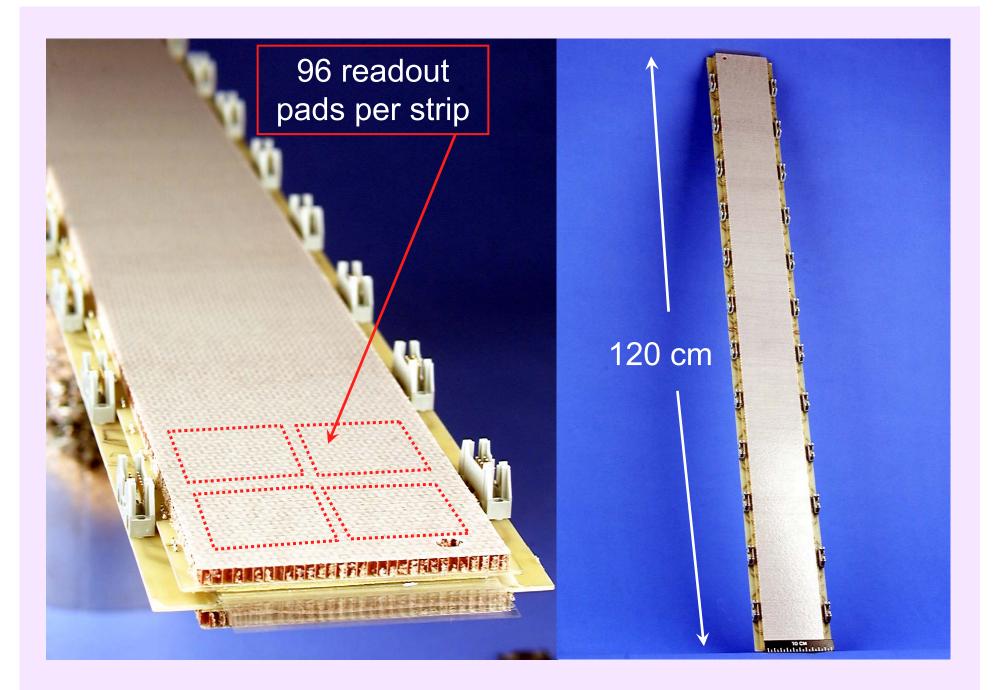


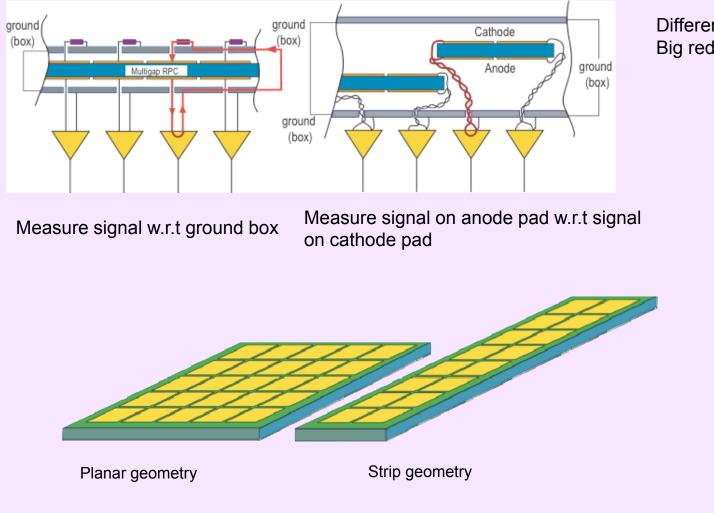
Timing depends on individual gap width Efficiency depends on total gas gap (10x250 mm)



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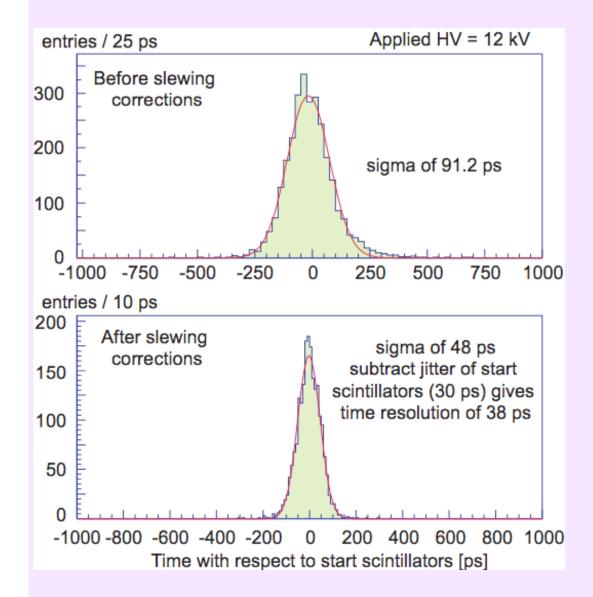
Strip geometry allows easy access to both anode and cathode signals

Differential readout Big reduction in noise

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TIME RESOLUTION



quoted time resolution σ = 38 ps $\sigma^2 = (\sigma \text{ of gaussian})^2$ - (jitter of start)²

start : average of 4 time measurements from 2 crossed fast scintillator bars equipped with 2 photomultipliers each

(jitter σ = 30 ps)

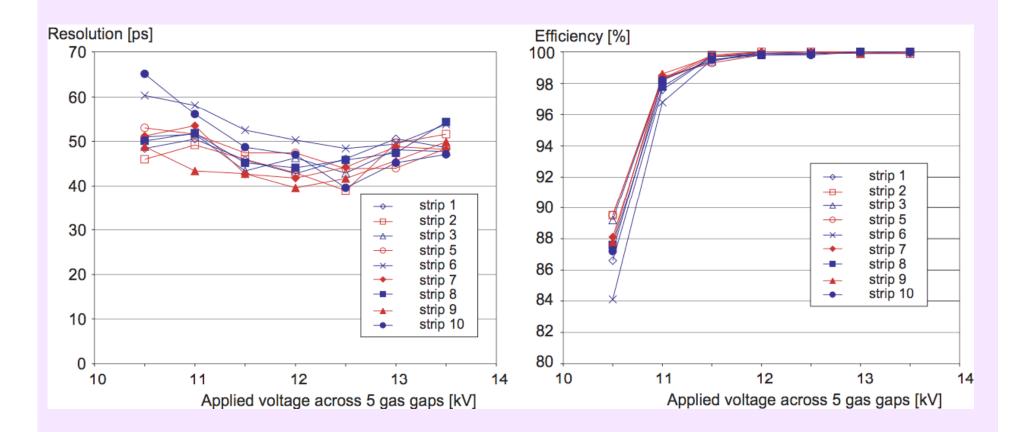
Gas mixture : 93% C₂F₄H₂ 7% SF₆

7 GeV/c beam of π^- , μ^- (T10/PS East Hall)

1 cm² beam spot selected by trigger Particle flux ~100 Hz/cm²

Pulse amplitude (ADC) initially and now TOT (time width=trailing edge-leading edge) used for slewing correction

Uniformity of performance



Online results from 9 MRPC (preproduction) strips tested at T10 during October 2003 All tests of samples of production strips with beam (2004, 2006) gave full efficiency and time resolution 40-50 ps

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Electronics

Front End : 24 channel cards (made by Digitec) with 3 NINO ASICs

The NINO ASIC : developed at CERN by Anghinolfi, Jarron etc 0.25 micron CMOS technology Fast preamplifier + discriminator Peaking time 1 ns Differential input (signals from anode / cathode of MRPC); Differential architecture throughout LVDS output Power consumption : 45 mW / channel Adjustable threshold : 10-100 fC

QuickTime™ and a decompressor are needed to see this picture.

Output width depends on input charge (no ADC needed for slewing correction)

Readout : custom made TRM (made by CAEN) with 30 HPTDC chips - 240 channels

HPTDC developed by microelectronics CERN group 8 channels/chip in high resolution mode (25 ps bin) Leading edge and trailing edge time --> time width 40 MHz clock / time stamp

QuickTime™ and a decompressor are needed to see this picture.

NOTE ON THE QUOTED TIME RESOLUTION

Factors contributing to the quoted resolution			
 Front End Electronics (NINO ASIC) and cables 		20 ps	
• Readout electronics 20 ps HPTDC 25 ps bin The time shown : difference between two time measurements t = t _{MRPC} -t _{REF.SCINT.}			
•Beam spot 1 cm² (50 ps/√12)		14.4 ps	
Intrinsic MRPC resolution		25 ps	
add in quadrature:	$20^2 + 20^2 + 14.4^2 + 25^2 = 40.4^2$		
CLIC08 16-10-2008	Despina Hatzifotiadou	ALICE TOF MRPCs 13	

The TOF as a trigger

Right now ALICE is enjoying the additional benefit of MRPC : very quiet (low noise) detector

Typical value of noise : ~0.2 Hz/cm²

- MRPC \rightarrow fast detector (L0, pre-trigger)
- Low noise \rightarrow can trigger on low multiplicity
- The large area (~150 m2) \rightarrow high geometrical efficiency
- <u>48 pads OR</u> = 1 channel to the Local Trigger Module
- ightarrow Used during commissioning with Cosmics

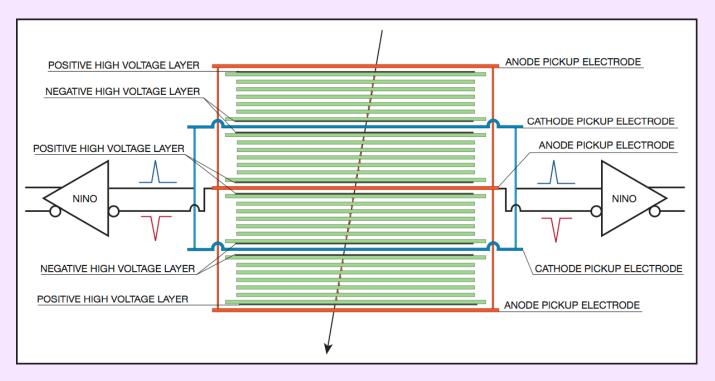
Event display of events triggered by TOF with and without magnetic field (Annalisa de Caro)

QuickTime™ and a decompressor are needed to see this picture. QuickTime™ and a decompressor are needed to see this picture.

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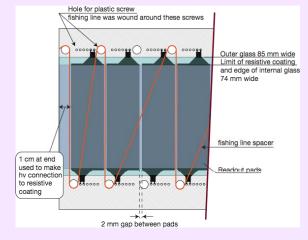
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Pushing the limits of time resolution : the 24 gap MRPC



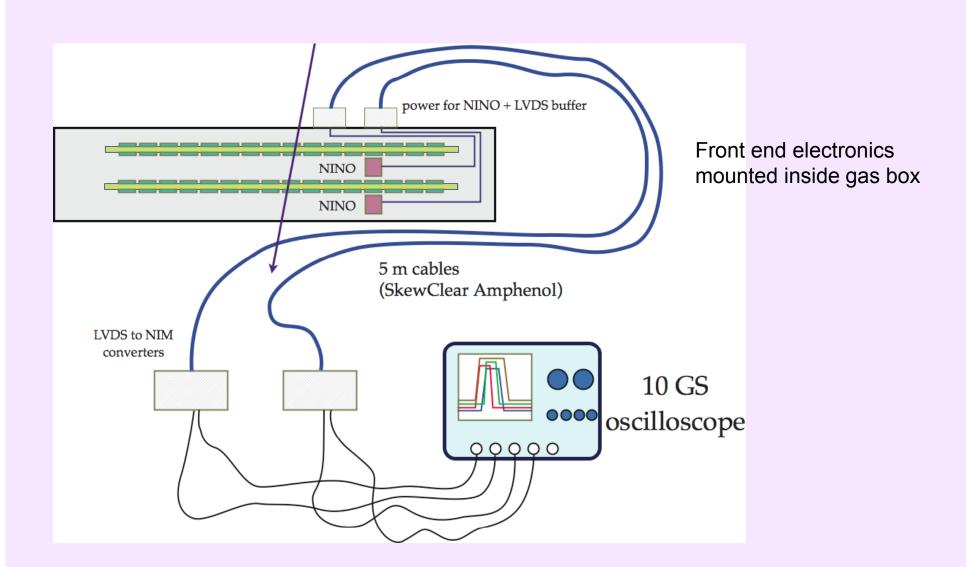
24 gaps of 160 microns each arranged in 4 stacks Glass plates 400 μm / 550 μm pickup pads : 2.5 x 7.4 cm² read out at both ends

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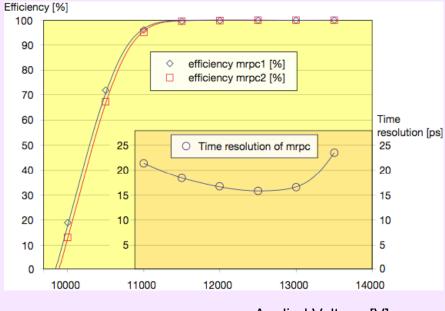


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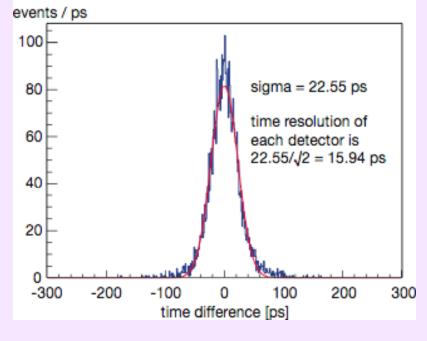
Despina Hatzifotiadou



Data Acquisition using LeCroy WM8300 + labview program



Applied Voltage [V] Time difference between MRPC1 and MRPC2



Improvement in time resolution due to

•Reduction in gap width

•Read out of both ends eliminates position dependence

•Front end electronics directly attached to readout pads

•TDC resolution (oscilloscope) : some ps

Time of each MRPC : 1/2(time_{end1}+time_{end2})

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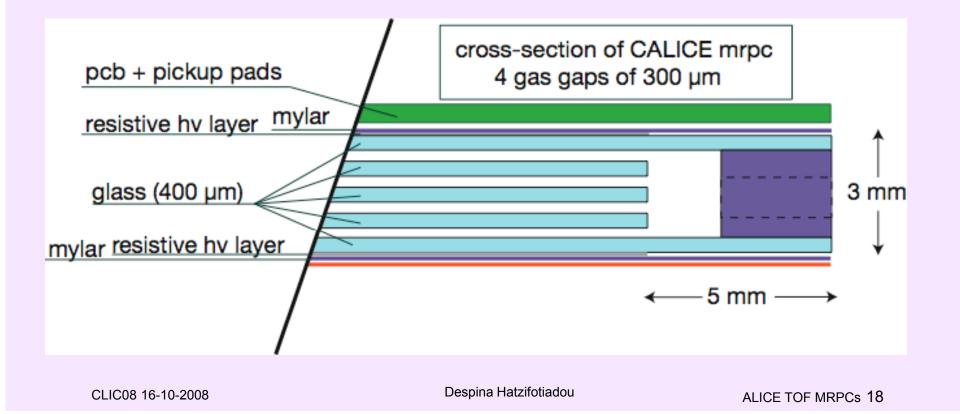
Despina Hatzifotiadou

MRPCs for the DHCAL of CALICE

Detectors under study for the Digital Hadron Calorimeter : Glass RPCs with 1 gap, MRPCs, Micromegas

Limitation : space available between iron plates

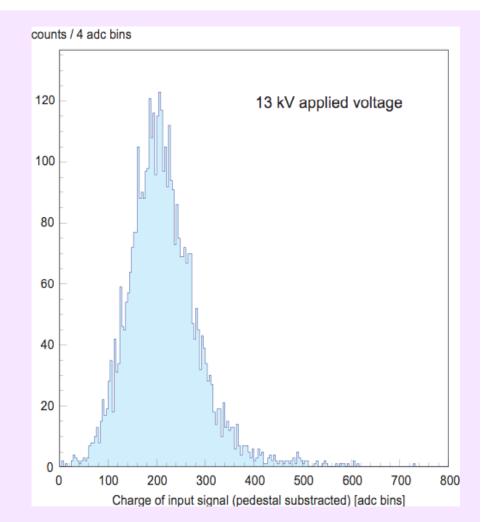
Build Multigap RPC with 1 m² surface and read out with 1x1 cm² pads



Reason for MRPC in the DHCAL

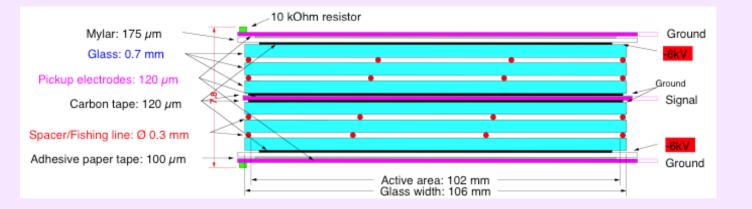
Shape of charge distribution :

- better (narrower) when many gaps, easier to set the threshold
- may be able to separate single particle from multiparticles on 1 cm² pickup pad
- Can provide comfortably 150 ps timing or better (see HARP)
 - can be used to assign particles to correct interaction (CLIC bunch crossing 0.670 ns)
 - this allows to untangle overlapping hadron showers in calorimeters



THE HARP MRPC

- 4-gap glass RPC
 - Active volume:1.92m x 106mm x 7.6mm
 - Glass plates: 0.7 mm
 - Gap size: 0.3 mm (fishing line)
 - HV: -6 kV (over two gaps)
 - Central readout electrode for all four gas gaps



Lucie Linssen RPC2003

Preamplifier connected to 8 strips (strip = $3 \times 10.4 \text{ cm}^2$) Time resolution in the range: $\sigma = 140 - 180 \text{ ps}$

Conclusions

- The multigap RPC provides precise timing
- The design of the ALICE TOF used for PID is expected to have a system time resolution of the order of 70 ps
- MRPCs can certainly be used at CLIC for providing a time stamp of the event
- Electronic development : integration of front end NINO and HPTDC ?

reminder

- Time resolution depends on the gap width (intrinsic detector)
- Time resolution depends on the size/geometry of readout pads/strips (time walk 50 ps/cm)
- Efficiency depends on the amount of gas traversed by through-going particle