

2nd Summer School on INtelligent signal processing for FrontIER Research and Industry

Wednesday, 16 July 2014

Second Morning Session (09:00 - 13:00)

time	[id] title	presenter
09:00	[7] Introduction to Very Deep Sub Micron CMOS technologies: the 65nm CMOS process for mixed mode analog-digital circuits	Prof. RE, Valerio
10:00	[8] Introduction to Very Deep SubMicron CMOS Technologies: FD-SOI technology for digital systems	Dr FENOUILLET-BERANGER, Claire
11:00	Coffee break	
11:30	[9] 3D and Novel Packaging technologies	Dr PATTI, Robert
12:15	[38] 3D technologies: new era for packaging and IC development	Dr LAMY, Yann