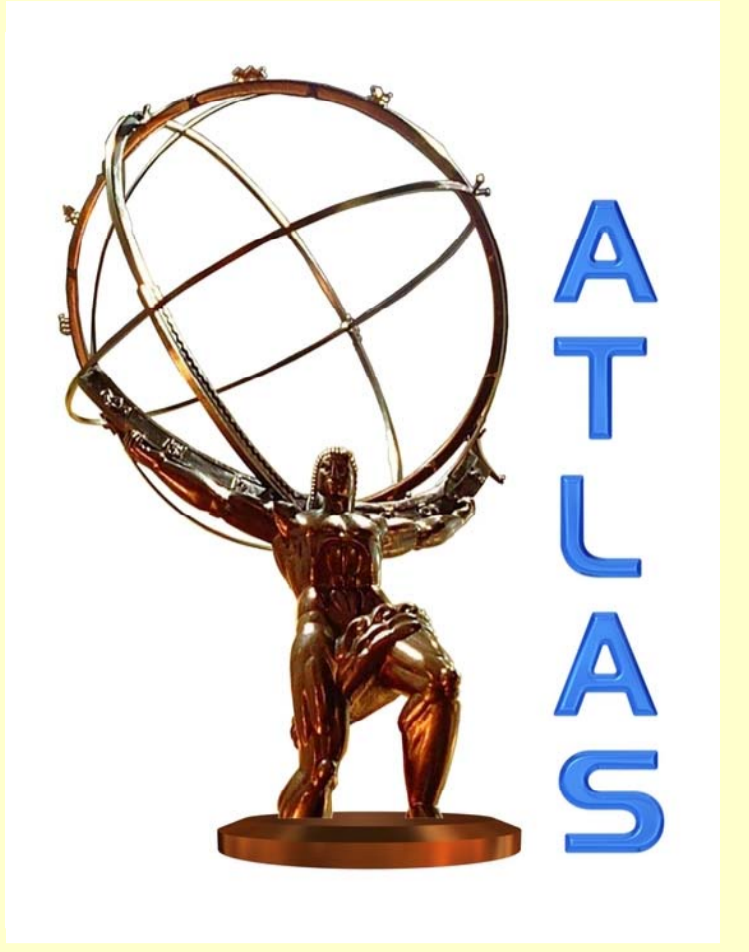
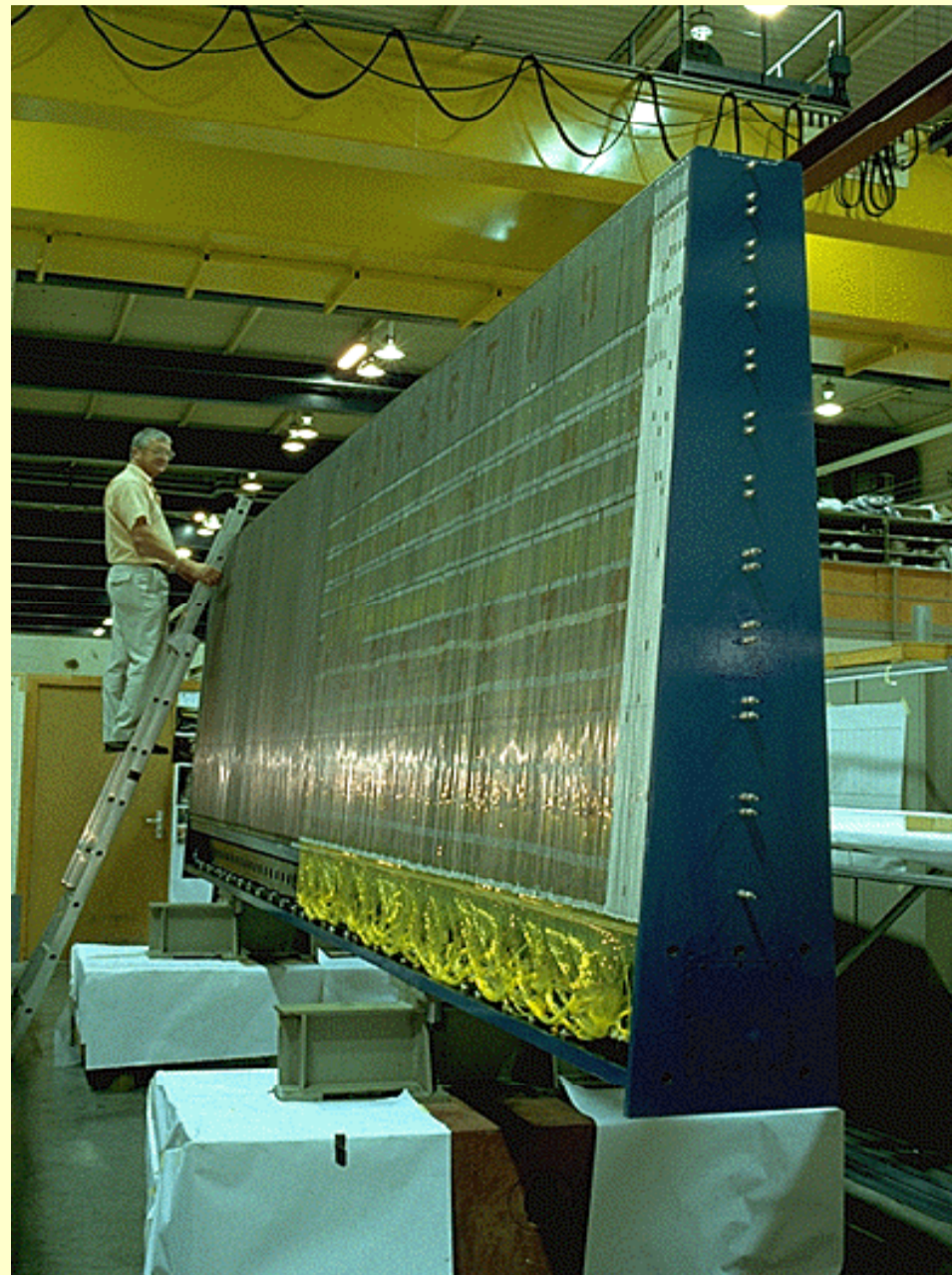


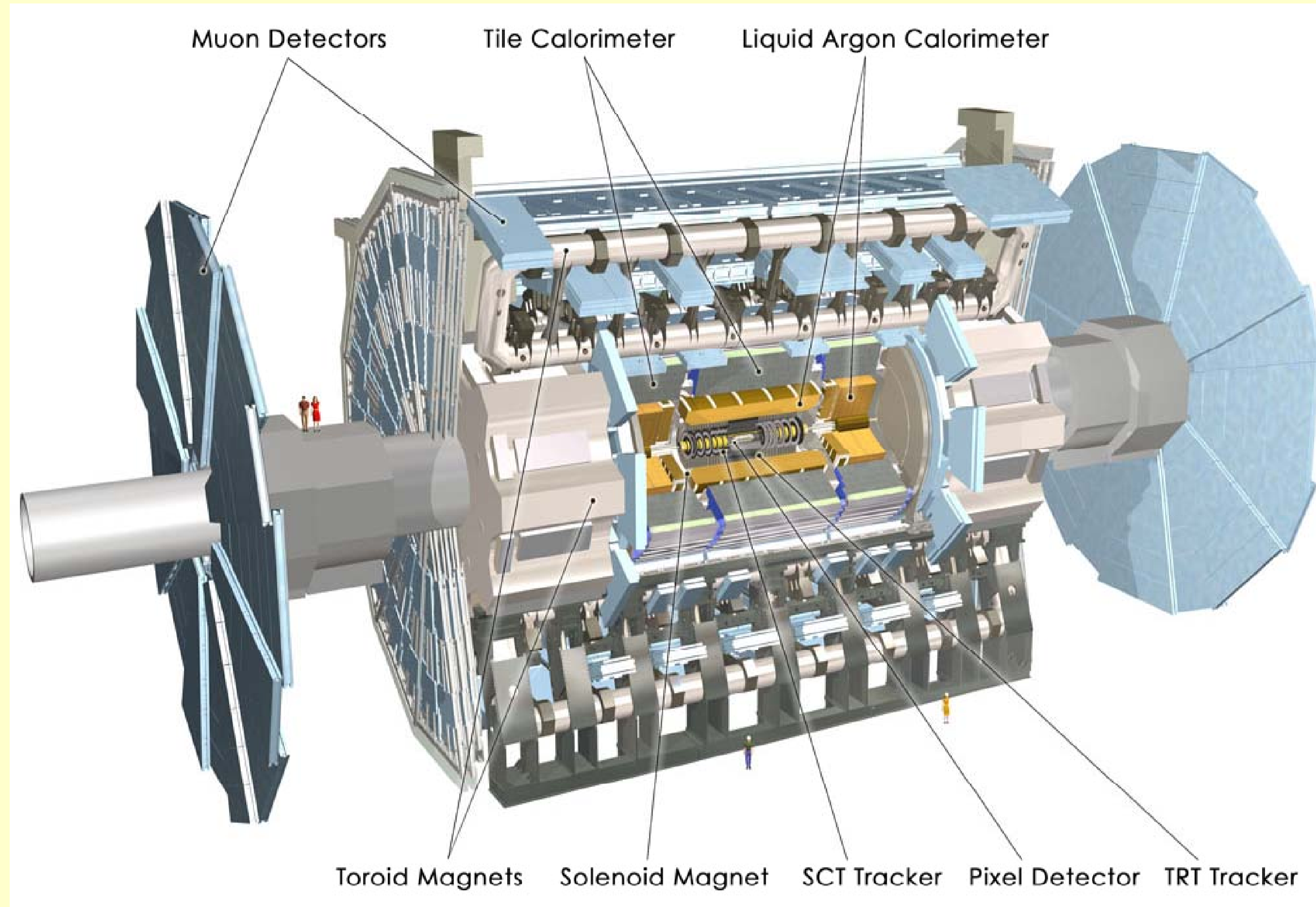
The ATLAS/TILECAL Detector Control System



Team: Joao Pina, Agostinho Gomes, Carlos Nuno Marques (LIP, Lisboa), Tiago Batista, Luis Granado Cardoso, Bernardo Sotto-Maior Peralva, Fernando Varela Rodriguez (CERN, Geneva), Giorgi Arabidze, Nikos Giokaris (University of Athens, Athens), Mohamed Ouchrif (Université Blaise Pascal, Clermont-Ferrand), Laura Sargsyan (YerPhi, Yerevan)



TILECAL module



TILECAL is composed of 3 cylinders, one central barrel and two extended barrels with each cylinder composed of 64 modules.

Back-end (Software):

- Distributed Supervisory Control And Data Acquisition system (SCADA) running on PCs
- SCADA system PVSSII commercial program from Austrian company ETM

Front-end (electronics):

- PLC's (Programmable Logical Controller)
 - ▶ HV micro boards and HV opto boards
- ELMB (Embedded Local Monitor Board)

Field bus

- CANbus (Controller Area Network)
 - ▶ MODbus

Communication Protocols

- ▶ DIM (Distributed Information Management)
- ▶ OPC (OLE for Process Control)

CUSTOM-MADE COMPONENTS FOR THE DCS

- ▶ a new firmware for the ELMB (non-ATLAS standard)
- ▶ Several PVSS scripts for control of the low voltage power supplies (examples: majority for temperatures probes; automatic retrieving of data for off-line analysis)
- ▶ All control and security of low voltages power supplies (software interlocks)
- ▶ All High Voltage Control system (photomultiplier's voltage control) implementation of a DIM server

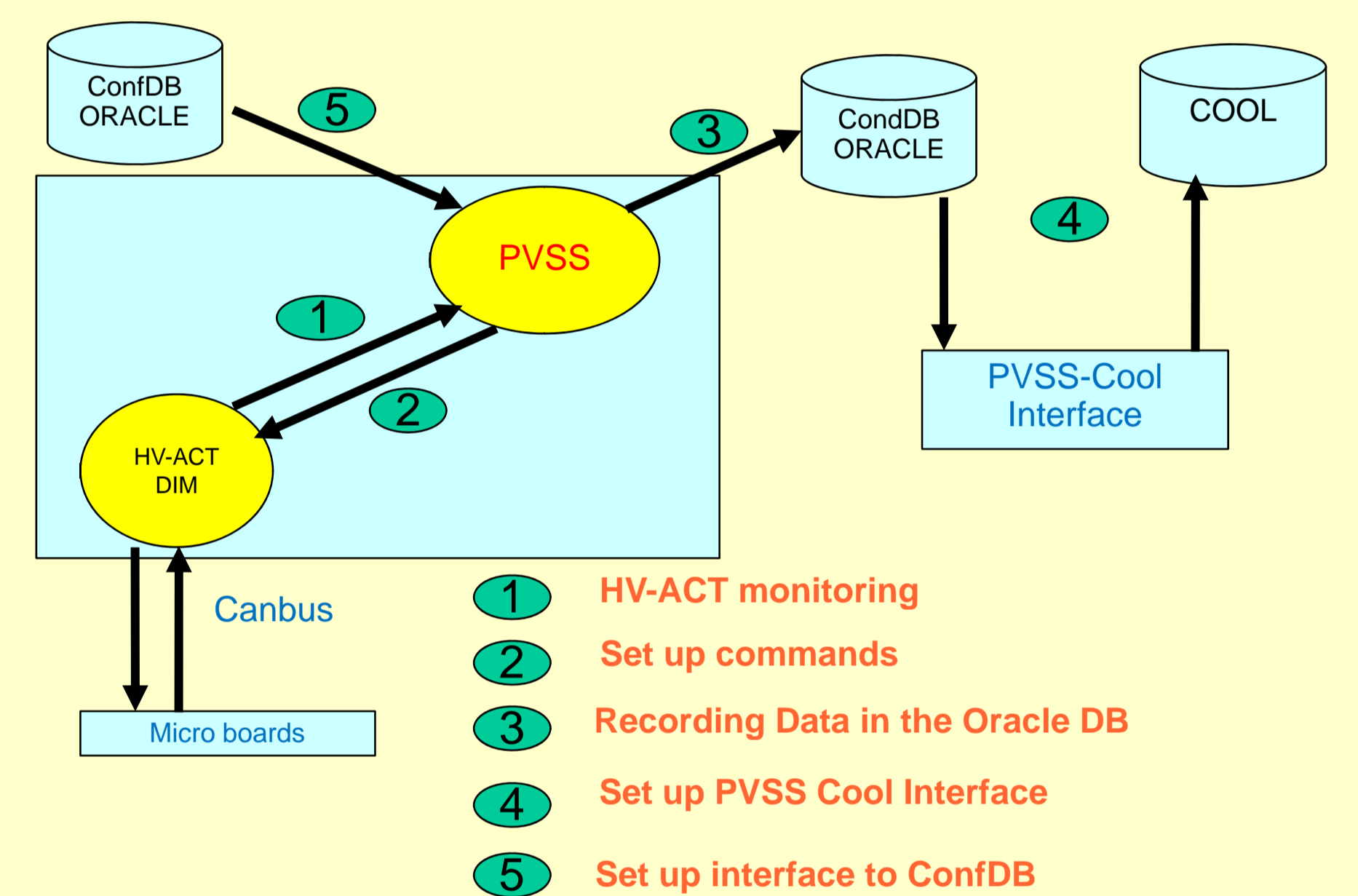
TILECAL DCS MAIN SYSTEMS

HIGH VOLTAGE (HV) – power supplies the Photomultiplier's (400 to 1000 V) - monitoring of the voltages and temperatures

LOW VOLTAGE (LV) – supplies power to HV distributor, Digitizers and Motherboard (eight voltages from 3 to 15V) – DCS monitors voltages, currents and temperatures using Embed Local Monitor Board (ELMB). DCS also provides control of the system.

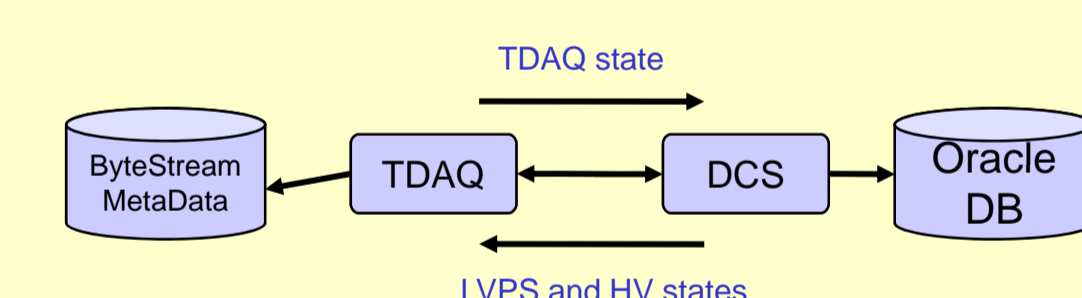
COOLING – cooling for both Low voltage and High Voltage systems - DCS monitors temperatures and pressures in cooling sectors. Also provides monitoring of temperatures inside LV and HV system.

HIGH VOLTAGE CONTROL

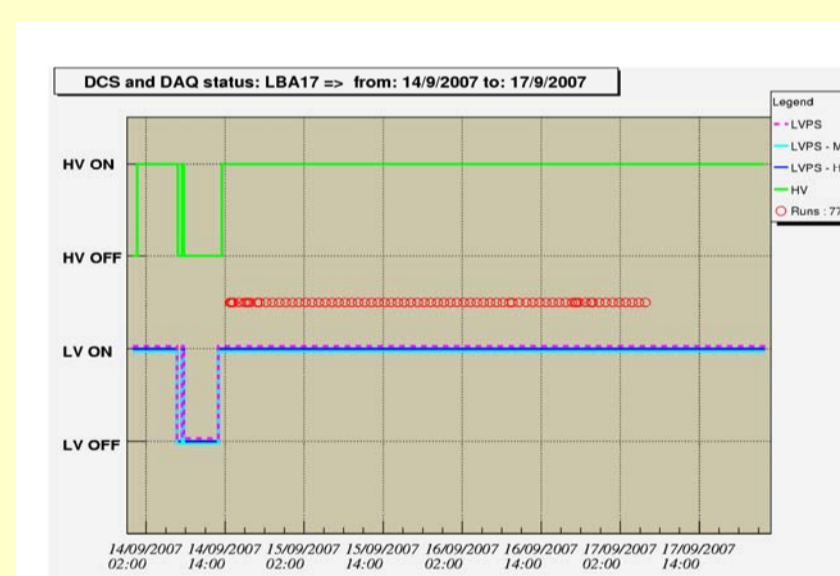


DAQ DCS COMMUNICATION (DDC)

Mechanism to exchange information between TDAQ and DCS based on DIM across Windows and Linux



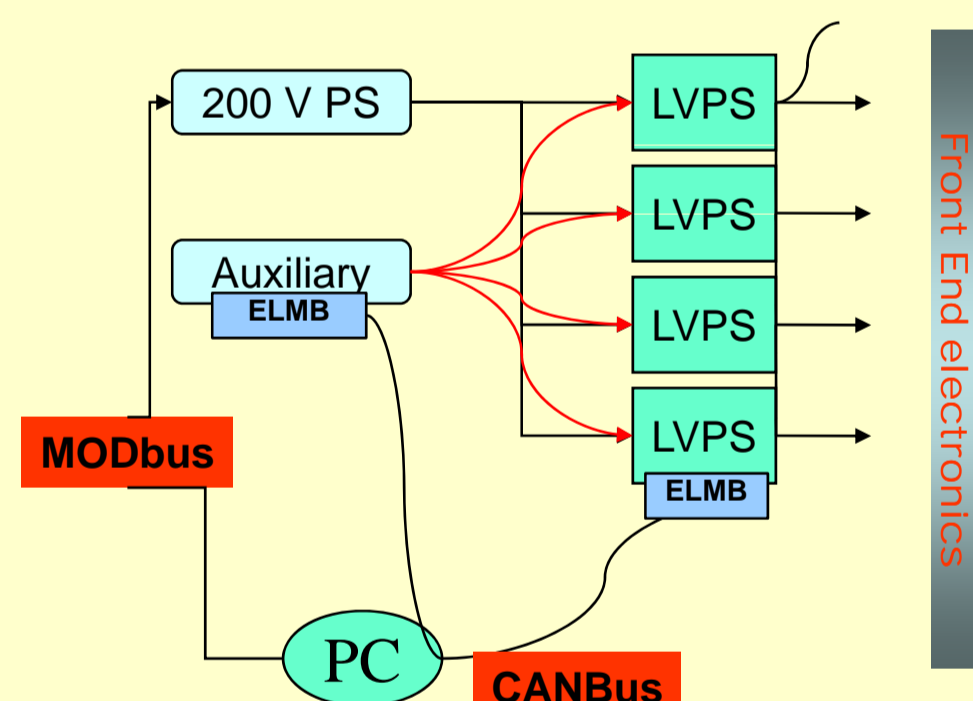
DAQ - DCS Data Flow



Example graph from DCS web interface showing activities of DCS and TDAQ

LOW VOLTAGE CONTROL

- LVPS 8 Output voltages
- Auxiliary Boards control the LVPS
- Equipment:
 - 256 LVPS
 - 64 Auxiliary Boards
 - ~2000 canais AO
 - ~16000 canais AI
 - 10 between readings

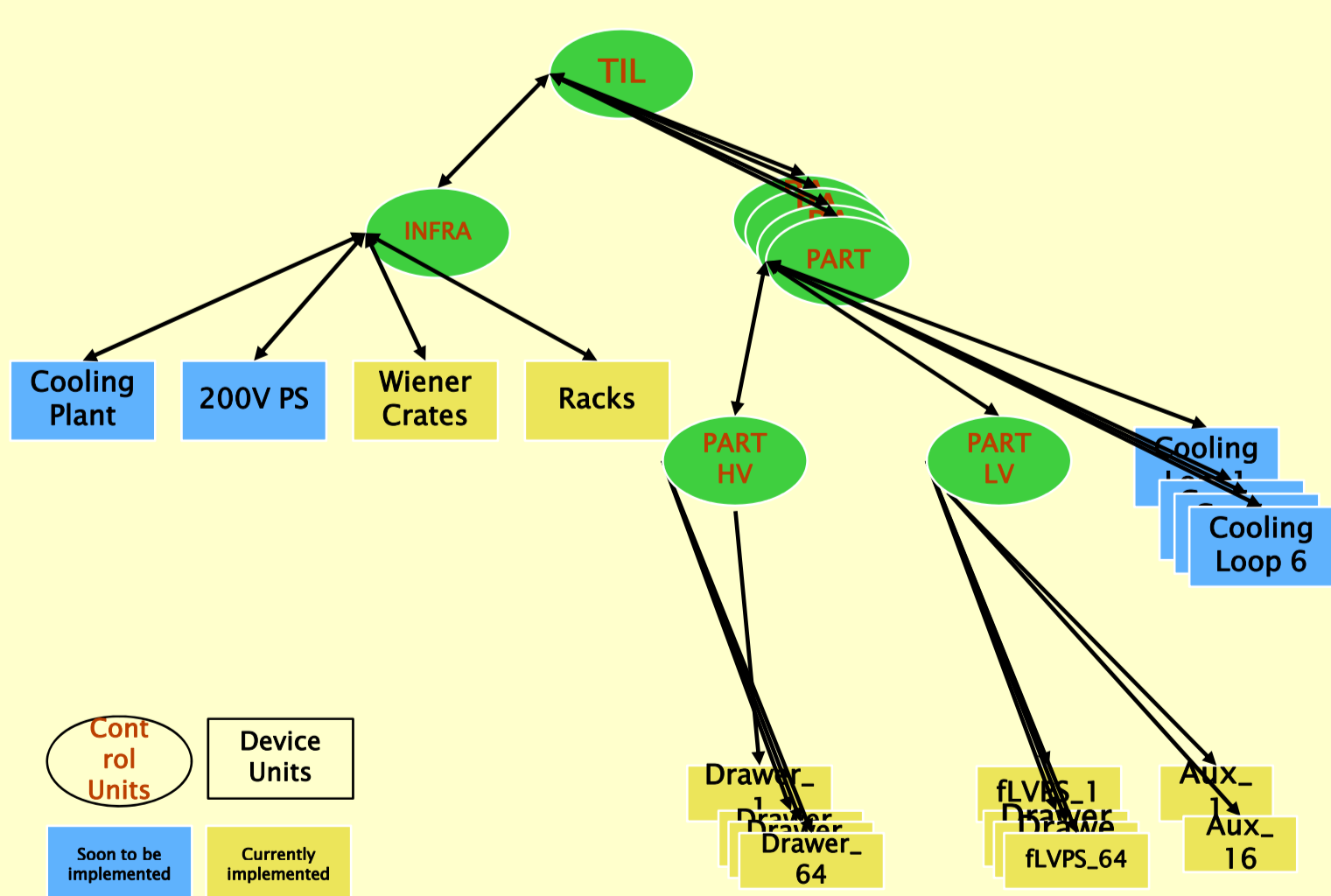


CESIUM LASER

Calibration systems with their own control

Finite State Machine (FSM)

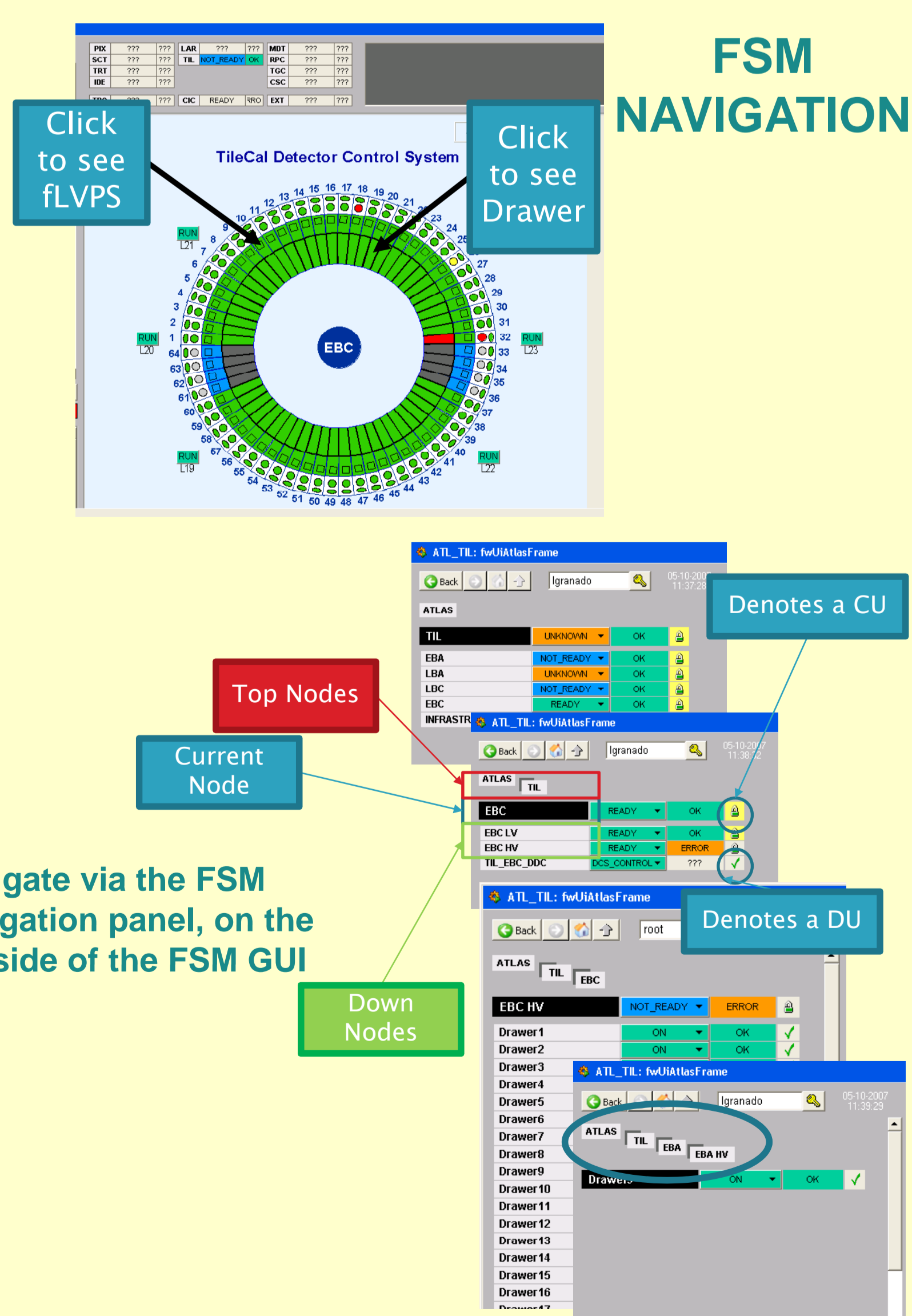
FSM will be extensively used in ATLAS DCS. It allows, among other, automatic detector operations, single subsystem operations and integration of the control in the ATLAS DCS.



The TILECAL FSM hierarchy

The basic FSM elements are Device Unit (DU) and Control Unit (CU). The DU provides the interface with the hardware, the CU (with a complex control program) integrates the DUs in the hierarchy.

FSM NAVIGATION



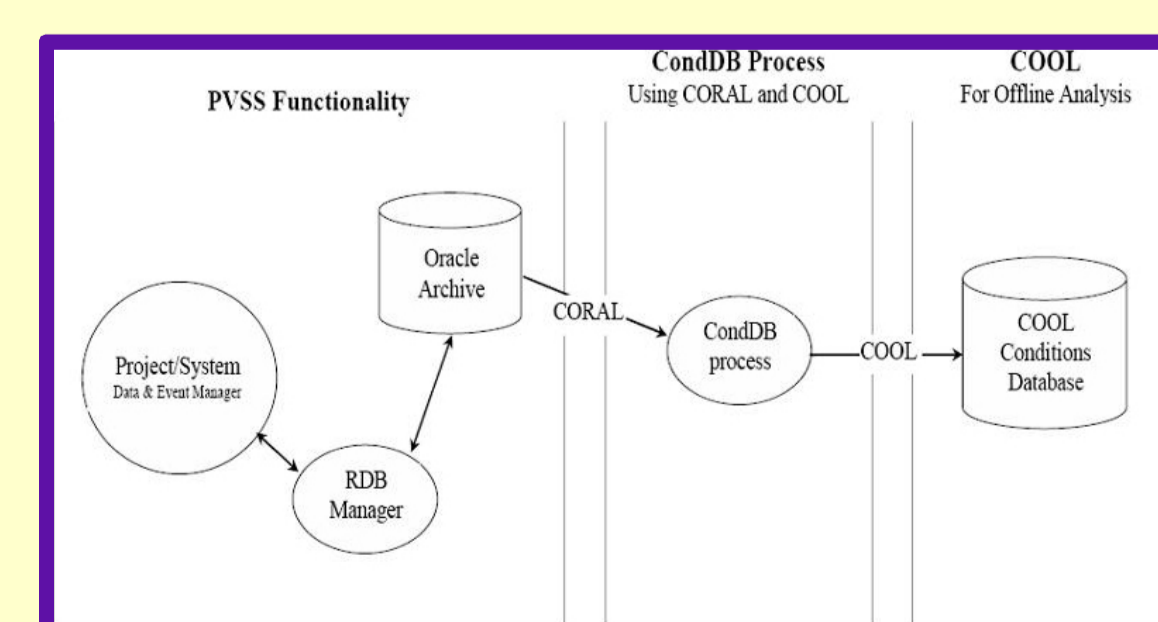
Data Storing

The TILECAL DCS will use three types of databases:

CONDITIONS DATABASE: PVSS ORACLE DATABASE - database to store data that is relevant for understanding detector behaviour

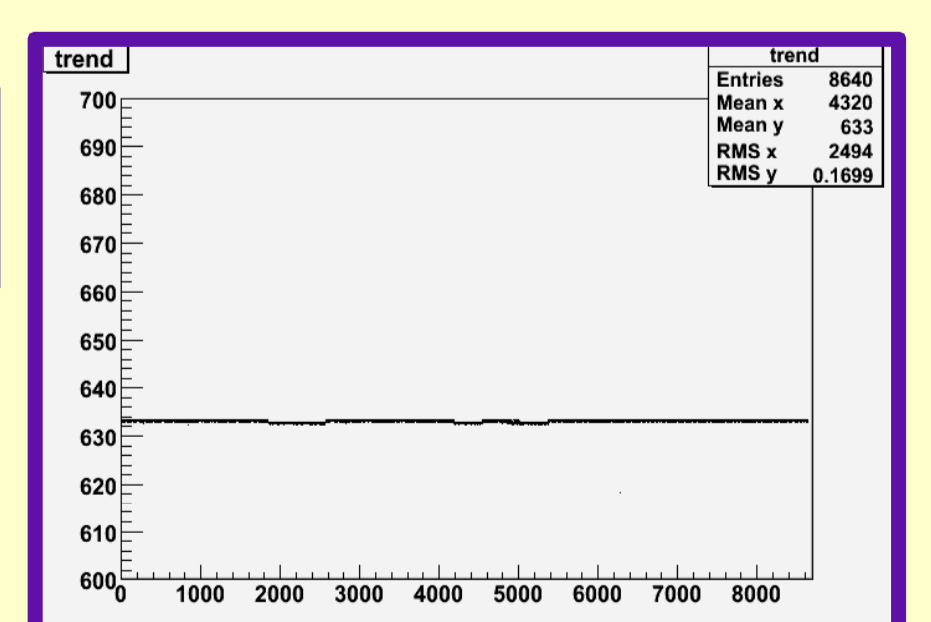
COOL DATABASE: database that will store data that is relevant for off-line physics analysis

CONFIGURATION DATABASE: ORACLE database to store settings (like output values, alert limits), system structure (lists and hierarchies of devices), device properties (like configuration of archiving, smoothing, etc.)



PVSS to COOL Data Flow

Shift on LBC11 HV pmt 38 June 15th 2007



LBC11 HV pmt 38 Δ max = 0.6 V r.m.s = 0.20 V

