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## **Benchmarking on System on Chip Architecture and fast benchmarking**

*Tuesday 14 October 2014 13:30 (30 minutes)*

The traditional architecture for High Energy Physics is x86-64 but in the community there is interest in processor more efficient in term of computing power per Watt. I'll show my measurement on ARM and Avoton processor.

I'll conclude with some measurements on candidate for fast benchmark that are requested by the physics community, mostly to measure the performance of machine in cloud.

**Primary author:** MICHELOTTO, Michele (Universita e INFN (IT))

**Presenter:** MICHELOTTO, Michele (Universita e INFN (IT))

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