HEPiX Fall 2014 Workshop

Tuesday, 14 October 2014

Computing and Batch Systems - Nebraska Union - Regency Suite (13:30 - 15:40)

time	[id] title	presenter
13:30	[30] Benchmarking on System on Chip Archtecture and fast benchmarking	MICHELOTTO, Michele
14:00	[22] Future of Batch Processing at CERN: a Condor Pilot Service	BELLEMAN, Jerome
14:30	[16] Compute node benchmarks for Compact Muon Solenoid workflows	CURY SIQUEIRA, Samir
15:00	[56] HTCondor and HEP Partnership and Activities	TANNENBAUM, Todd
15:20	[57] Releasing the HTCondor-CE into the Wild	BOCKELMAN, Brian Paul

Computing and Batch Systems: Computing and Batch Systems - Nebraska Union - Regency Suite (16:10 - 16:30)

time	[id] title	presenter
16:10	[58] HTCondor on the Grid and in the Cloud	FREY, James