WP18 summary
High-speed data recording and access

CRISP 3rd Annual meeting

June 2-4, 2014
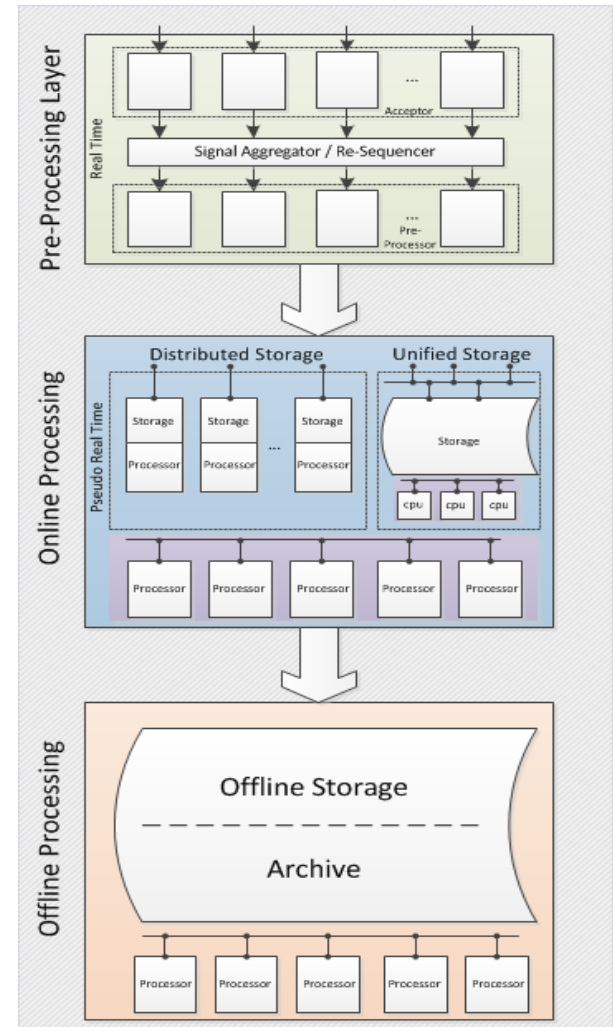
Krzysztof Wrona

Project milestones

- **Milestone 21**
  - Architecture document defined
  - Submitted on Nov 30, 2013

- **Deliverable**
  - Report on Prototype System and Future Work
  - To be submitted by Sep 30, 2014

Architecture

- **Layered architecture**
  - Pre-processing layer
  - Online processing layer
  - Offline processing layer

- **Each layer defines input/output data and is characterized by functional modules**

- **Prototype implementations are being developed for two uses cases: European XFEL and SKA**

Agenda for WP18

- Prototype for High Speed Data Acquisition at European XFEL, Djelloul Boukhelef

- Global Filesystem Option for SKA UV buffer: tests and development of Lustre, Bojan Nikolic