

ATLAS Rx Pixels plugins



Geoffrey Mullier
CHIPP Winter school 2015

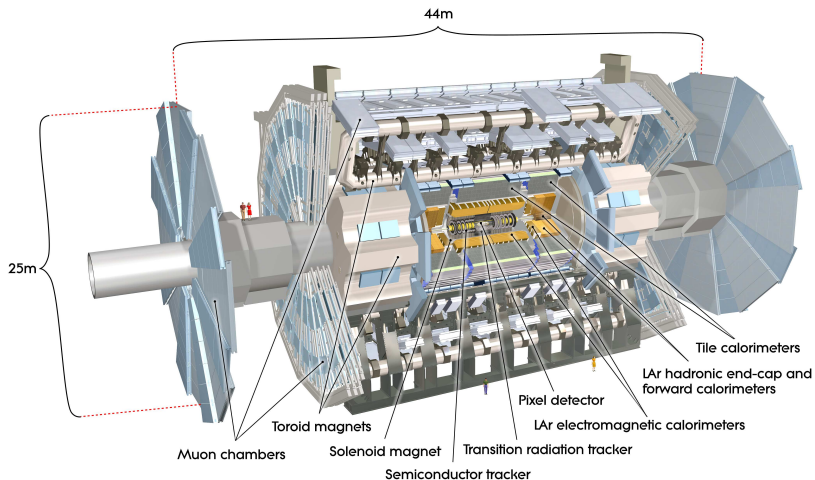
Albert Einstein Center / Laboratory for High Energy Physics

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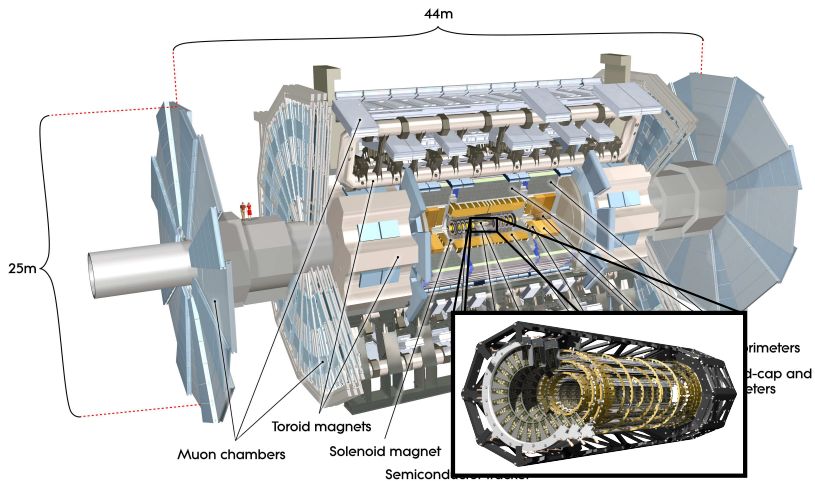
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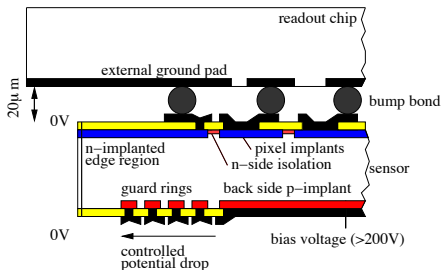
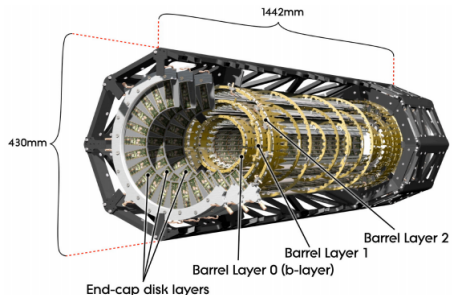
ATLAS structure



ATLAS structure

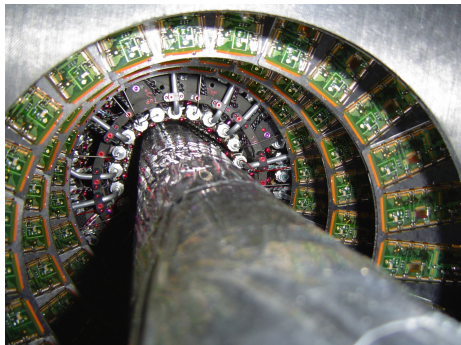


Pixel detectors



Characteristics & usage

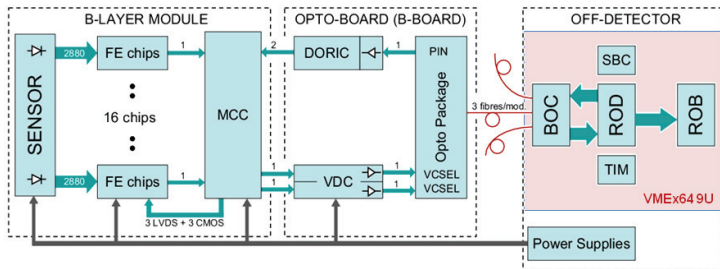
- Semi conductor sensors
- Small sized pixels ($\approx 50 \mu\text{m} \times 300 \mu\text{m}$)
- 3 layers ($\approx 4, 10, 13 \text{ cm}$)
- High granularity \rightarrow spatial resolution $\approx 10 \mu\text{m}$
- Important for vertex identification and track reconstruction
- NEW : IBL



Insertable B Layer

- New layer inside the existing pixel sensor (at ≈ 3 cm)
- Smaller sized pixels ($\approx 50 \mu\text{m} \times 250 \mu\text{m}$)
- Important for High luminosity phases
 - Vertex reconstruction
 - Balancing dead channels

Readout Pixels/IBL

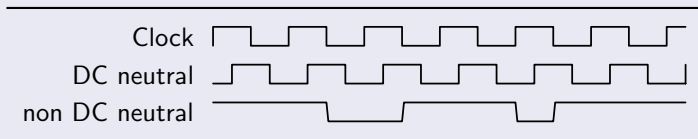


Path from Sensor to Off-Detector



DC neutrality

- A signal is said DC neutral if there is no DC component to the signal in other terms $\lim_{T \rightarrow \infty} \frac{1}{T} \int_0^T V dt = 0$
- The IBL use a DC neutral encoding of data (8b10b)
- The older hardware use a non DC neutral encoding and DC neutral but lower bandwidth (Manchester)



Problems?

Tx Plugins

- None

Rx Plugins

- New SNAP12 plugins don't like non DC neutral signals
- Old FE-I3/MCC, that cannot be replaced, send a non DC neutral signals
- We want to be able to use new BOC design which is all around better
- Cannot produce old ASIC anymore
- Need to design a new Plugin that doesn't mind non DC neutral input

Problems?

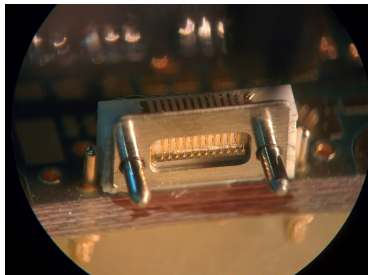
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- Cannot produce old ASIC anymore
- **Need to design a new Plugin that doesn't mind non DC neutral input**

Rx Board philosophy

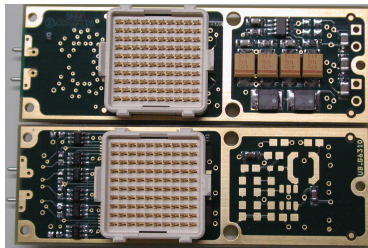
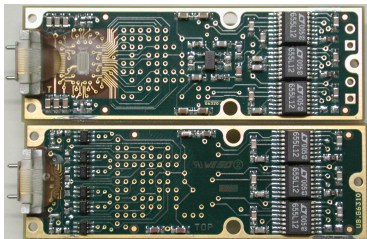


Rx Board philosophy



Pull up / Amplification ?

- Discrete Component
- ASIC

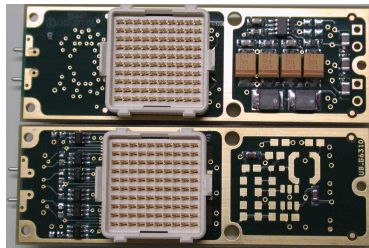
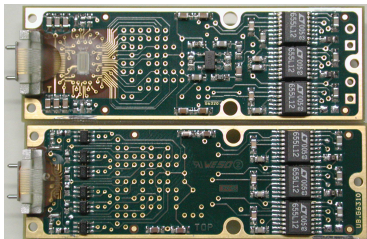


Rx Board philosophy



Pull up / Amplification ?

- Discrete Component
 - ASIC
- } Test them to find it out!



How to test them out?

Electrically

- Verify the characteristics on its own by signal injection onto the electronics
- Measure directly on BOC (Back Of Crate Card)

Do they actually transmit correctly?

- Loopback test using the BOC software
- Bit error rate testing (BERT) using the BOC

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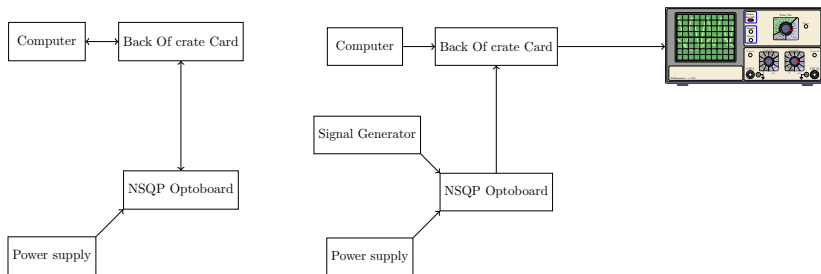
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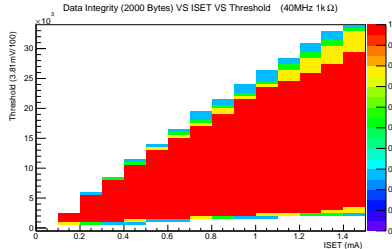
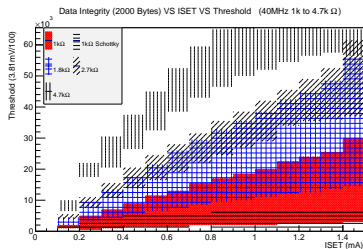
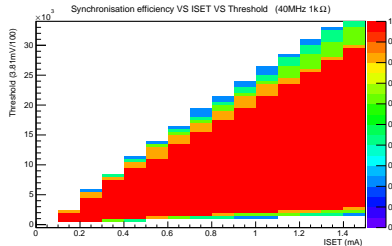
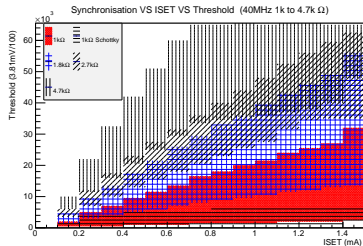


Principle

- Verify the range in which the BOC recognises the incoming signals
- Measure erros from memory of the BOC (FiFo)
- Measure signal on BOC from pattern signal generator



Preliminary testing of the discrete solution



And the ASIC?

Encouraging after first testing but...

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PLEASE STAND BY

Project outlook

Not so far away in space time

- Full test of both "almost" final version of respective designs
- Ageing test of both designs

Further down the road

- Production and testing of 300 plugins
- Installation of said 300 plugins

Thanks for listening



That's all Folks!