Characterization of thin n-in-p planar pixel sensors and status of the new active edge pixel production

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Tested n-in-p Float Zone (FZ) sensors

VTT active/slim edges:

- FE-I3 100 μm
- FE-I4 100 μm
- FE-I4 200 μm

CiS production:

- FE-I4 200 μm
- FE-I4 200 μm
- FE-I4 300 μm

Irradiations:

- $5 \times 10^{15} n_{eq}/cm^2$ (1@KIT + 4@JSI)
- 5×10¹⁵ n_{eq}/cm^2 (KIT)
- $6 \times 10^{15} n_{eq}/cm^2$ (2@KIT + 4@JSI)
- $7 \times 10^{15} n_{eq}/cm^2$ (LANSCE)
- 14×10 $^{15}~n_{eq}/cm^2$ (LANSCE)
- 14×10¹⁵ n_{eq}/cm^2 (LANSCE)

Irradiation facilities:

- JSI: E ≤ 10 MeV reactor neutrons
- KIT: 25 MeV protons
- LANSCE: 800 MeV protons

VTT active/slim edge production

450 μm



four-quadrant ion implantation
 → activated edges

• 11 Guard Rings (GR) + 1 Bias Ring (BR) $d_{edge} = 450 \ \mu m$



• 1 BR $d_{edge} = 125 \ \mu m$

For edge analysis see MPP talk at last RD50 workshop by Stefano

• 1 GR $d_{edge} = 50 \ \mu m$

Charge collection of VTT modules



• Charge collection of 200 μm sensor does not saturate below $V_{\rm bias} =$ 425 V

Test beam setup at DESY





- 4 GeV electrons
- perpendicular incidence on the sensors
- track reconstruction with EUDET telescope

Efficiency of VTT modules



- FE-I3 100 μ m: (99.1 \pm 0.3) % $\Phi = 5 \times 10^{15} n_{eq}/cm^2$, $V_{bias} = 300 V$
- FE-I4 100 μ m: (97.3 \pm 0.3) % $\Phi = 5 \times 10^{15} n_{eq}/cm^2$, $V_{bias} = 500 V$
- FE-I4 200 μ m: (96.9 \pm 0.3) % $\Phi = 6 \times 10^{15} n_{eq}/cm^2$, $V_{bias} = 500 V$



Power dissipation for different sensor thicknesses

- Study the case of $\Phi = 5 \times 10^{15}$ second pixel layer in the hypothesis of replacement at half the life-time of LHC
- Assume an operational bias voltage with 200 V of safety margin with respect to the voltage for which a hit eff. of 97-98 % can be obtained

Thickness [µm]	Op. V _{bias}	$h_{ m leak}(-25^{\circ}C)$	Power
	[V]	$[\mu m A/cm^2]$	[mW/cm ²]
100	500	35	18
200	700	65	45

Reduced risk of thermal runaway for thin sensors in the inner layer

CiS production

- 200 μm thick n-in-p pixel sensors produced at CiS on 4 inch FZ silicon wafers
- Bump-bonded on ATLAS FE-I4 chips at IZM
- Spark protection with 3 μm layer of patterned BCB deposited on the sensor surface at IZM
- GR structure with 450 μ m inactive edge





Irradiation of CiS modules at LANSCE

- 7 × 7 diode array perpendicular to beam to measure beam profile + aluminum foil for dosimetry
- 2 × 2 cm² sensors and 1 × 1 cm² Al foil titled at 30° degree w.r.t. the beam



Fluence according to diodes and foil



Analog scan of module irradiated to $7 \times 10^{15} n_{eq}/cm^2$ (rotated by 90° degree w.r.t. diode array)

IV curves of CiS sensors

IV curves before irradiation



 Given the inhomogenous irradiation the leakage currents cannot be directly compared between LANSCE and KIT irradiated modules as curves do not scale linearly with the nominal fluence



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Charge collection of CiS modules



Expected charge in silicon: 14 ke for 200 μ m 21 ke for 300 μ m CCE of 200 μm sensors:

- 7×10 $^{15}~n_{eq}/cm^2~\rightarrow$ (45 \pm 6) %
- $14 \times 10^{15} n_{eq}/cm^2 \rightarrow (35 \pm 5) \%$

CCE of 300 μm sensors:

• 14×10 $^{15}~n_{eq}/cm^2 \rightarrow$ (25 \pm 4) %

Effiency of CiS modules irradiated at LANSCE

200 μ m, 7×10¹⁵ n_{eq} /cm², thr. 1600 e

200 μ m, 14×10¹⁵ n_{eq}/cm², thr. 1600 e



B. Paschen (MPP München)

Charge sharing for unirradiated modules

FE-I3 100 µm, thr. 1800 e FE-I4 200 µm, thr. 1600 e 50 공 nck CS1 400 Entries 2549139 Track x fum 0.4 Ĕ CS2 Track x [Track x CS3 Track x [um] Track x [µm] CS4 ack 400 400 Entric Track x fiim Track x lum

- Maximum spread of charge cloud for 100 μ m sensor at $V_{\text{bias}} =$ 20 V is about 5 μ m
- Maximum spread of charge cloud for 200 μ m sensor at $V_{\text{bias}} =$ 50 V is about 7 μ m
- Measured at the same time, therefore same telescope resolution of about 7 to 10 μm for both modules
- Observed charge sharing increased in FE-I4 probably due to lower threshold in the measurement (1600 e vs 1800 e)

... Charge sharing for irradiated modules



 After irradiation the effect of charge sharing is stronger for the 100 μm thick sensors than for the 200 μm thick sensor due to the higher charge collection and the higher electric field at equal bias voltages

Summary of charge collection properties





- d=150 μm, Φ=4, HLL d=285 μm, Φ=5, CiS d=100 μm, Φ=5, VTT-B d=100 μm, Φ=5, VTT-C d=200 μm, Φ=6, VTT-D d=200 μm, Φ=7, CiS-A1 $[\Phi] = 10^{15} n_{eq}/cm^2$
- d=75 μm, Φ=10, HLL



- d=285 µm, Φ=10, CiS



d=300 um, Φ=14, CiS-C

 $[\Phi] = 10^{15} n_{eq}/cm^2$

2nd production of active edge pixels at ADVACAM

- In collaboration with Glasgow, Göttingen, LAL, CLIC CERN-LCD, Geneva University for medical applications
- 50, 100, 150 μm thick sensors (5 FZ p-type wafers for each thickness):
 - FE-I4 quad sensor
 - FE-I4 single chip sensors with different geometries
 - Omegapix sensors
 - TIMEPIX sensors for CLIC R&D
 - CLICpix sensors for CLIC R&D
 - Pixel and strip structures for medica applications



FE-I4 Single Chip Modules

100 μm edge Bias Ring Bia punch-through structures

100 μm edge Bias Ring + Guard Ring punch-through structures





50 μ m edge

one Guard Ring,

no punch-through

structures

FE-I4



Status of the new ADVACAM production









150 um

100 um

Status of the new ADVACAM production



- All implantations completed including four-quadrants implantation in the trenches
- Waiting for results of the Glasgow R&D on thin chip flip-chipping at ADVACAM before defining the target chip thickness for this production







150 um

100 um

Conclusions

- Good charge collection and hit efficiency for 100 μm thick sensors with and without active edges
- Studies performed with 200 and 300 μm thick n-in-p sensors up to a fluence of 14×10^{15} n_{eq}/cm^2
 - \rightarrow modules are still functional after a very inhomogenous irradiation

And future plans ...

- Characterize thin pixel sensors at higher fluences ($\geq 10^{16} n_{eq}/cm^2$) with new productions at ADVACAM (50, 100, 150 μ m thick with active edges) and CiS (100-150 μ m thick)
- ullet ightarrow see talk by T.Wittig on R&D avtivities at CiS