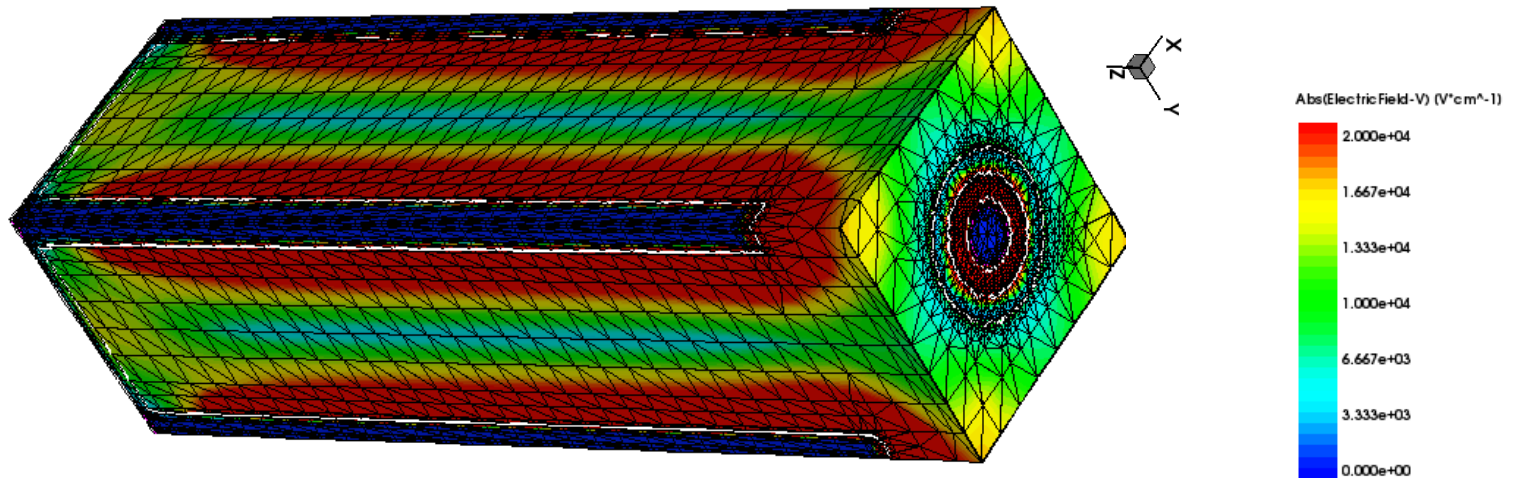


TCAD simulated surface damage in proton irradiated strip sensors: Investigation of interface traps vs non-uniform 3-level model

25th RD50 Workshop, 19-21 Nov. 2014

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- ❑ **Simulated surface properties after proton irradiation**
 - Motivation: Measured & simulated CCE(x)
 - Interface traps vs non-uniform 3-level model
 - Simulations & comparison with measurements

- ❑ **3D sensor simulations**
 - Motivation
 - Simulated structure & electrical characteristics
 - Hit position dependency of the signal

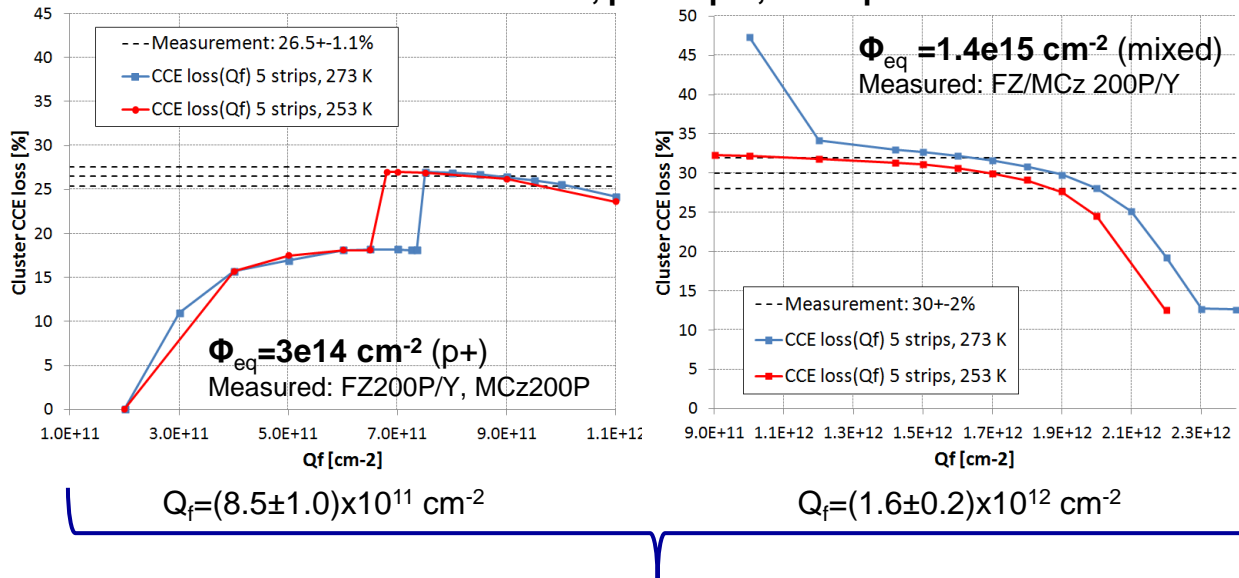
- ❑ **Summary**

Simulated surface properties after proton irradiation

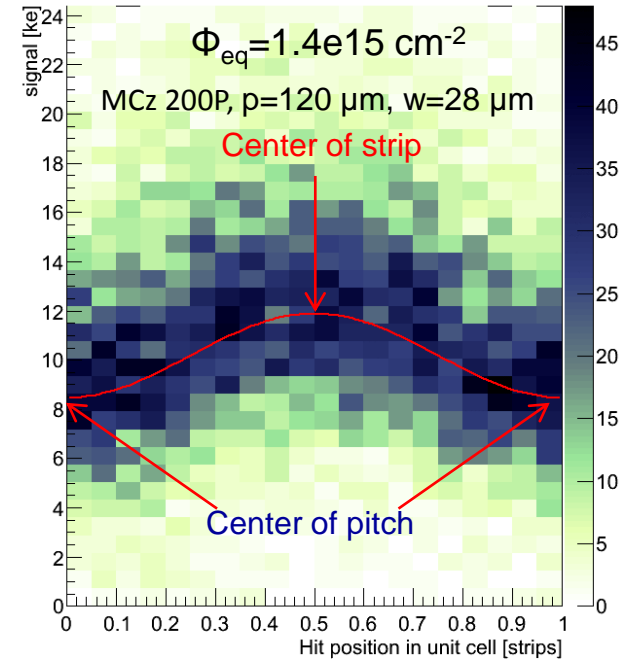
Motivation: Measured & simulated CCE(x)

- **TCAD:** Synopsys Sentaurus
- **Proton model:** Tuned by R. Eber from the PTI-model
- **3-level model within 2 μm of device surface + proton model in bulk:**
 - R_{int} & C_{int} in line with measurement (see back-up slides) **also at high fluence & Q_f**
 - Can be tuned to equal bulk properties (TCT, V_{fd} & I_{leak}) with proton model
→ suitable tool to investigate CCE(x)

200P sensor, p=120 μm, w=28 μm:



- **Test beam measured position dependency of CCE [T. Mäenpää, 2013]**



Measured: strip isolation ok,
 CCE loss between strips ~30%

3-level model within 2 μm of device surface

□ Preliminary parametrization of the model for fluence range $3e14 - 1.5e15 \text{ cm}^{-2}$



Type of defect	Level [eV]	σ_e [cm ²]	σ_h [cm ²]	Concentration [cm ⁻³]
Deep acceptor	$E_C - 0.525$	$1e-14$	$1e-14$	$1.189 \cdot \Phi + 6.454e13$
Deep donor	$E_V + 0.48$	$1e-14$	$1e-14$	$5.598 \cdot \Phi - 3.959e14$
Shallow acceptor	$E_C - 0.40$	$8e-15$	$2e-14$	$14.417 \cdot \Phi + 3.1675e16$



From Delhi University contribution to Phase II talk of simulation WG:

The interface trap density N_{it} can play very important role in irradiated Si sensor,

- $[N_{it}] \sim [Q_F]$ [1]
- A significant number of N_{it} states are deep traps, affecting the SC near interface [1]
- R_{int} simulations for X-ray irradiated strip sensors indicates a prevalence in N_{it} of acceptor states

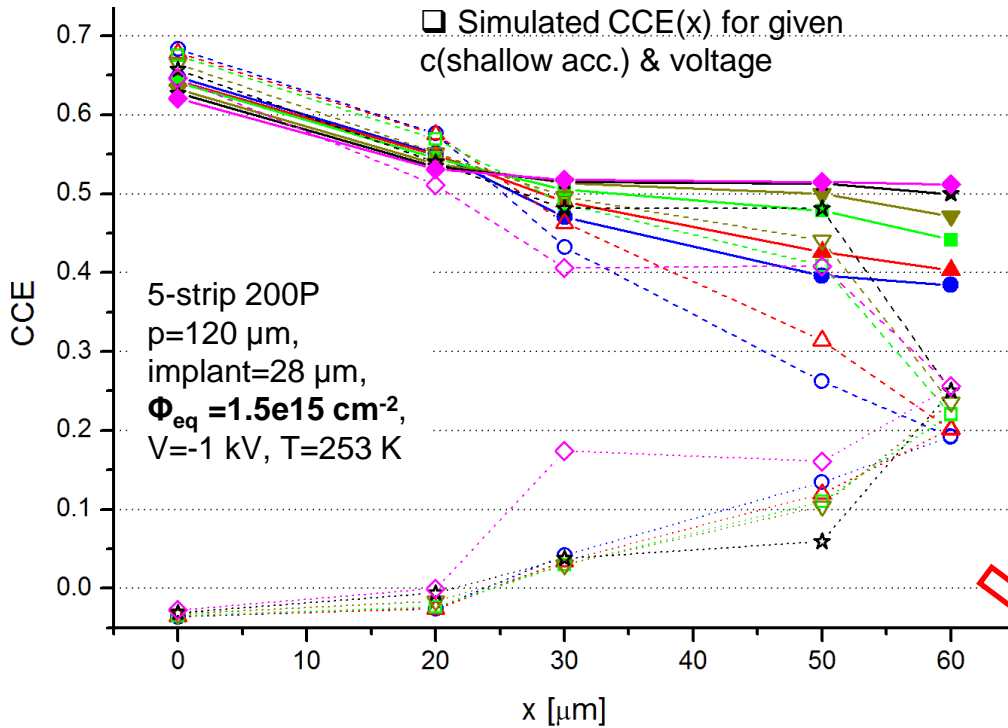
Model of N_{it} in Silvaco TCAD simulations:

1. We have assumed that $[N_{it}] = [Q_F]$
2. For a given N_{it} , 60% of the states are deep traps ($E_C - 0.6\text{eV}$) and 40% are shallow states ($E_C - 0.39\text{eV}$) with $\sigma_n = \sigma_p = 1\text{e-}15\text{cm}^2$ [1]

- Promising results of simulated R_{int} for Silvaco ATLAS
- Is it possible to replace 3-level model close to surface with N_{it} in Synopsys Sentaurus?
- Opportunity to study the depth distribution of trap levels responsible of observed CCE loss between strips?

[1] J. Zhang, DESY Thesis-2013, "X-ray radiation damage studies and design of a Si Pixel sensor for different fluences for science at the XFEL"

CCE(x): Simulation method



CCE loss:

40.6%

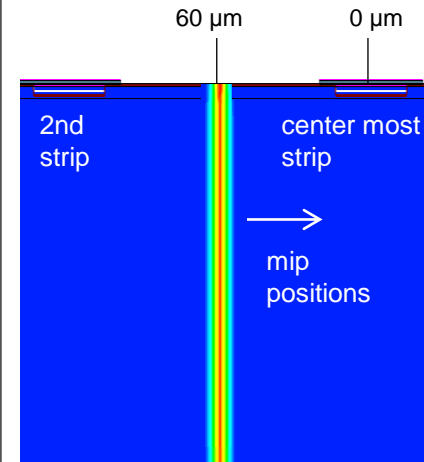
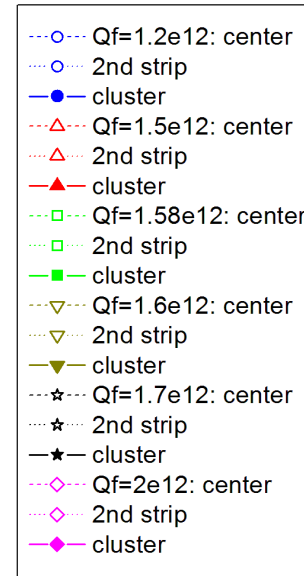
37.3%

31.1%

25.4%

20.3%

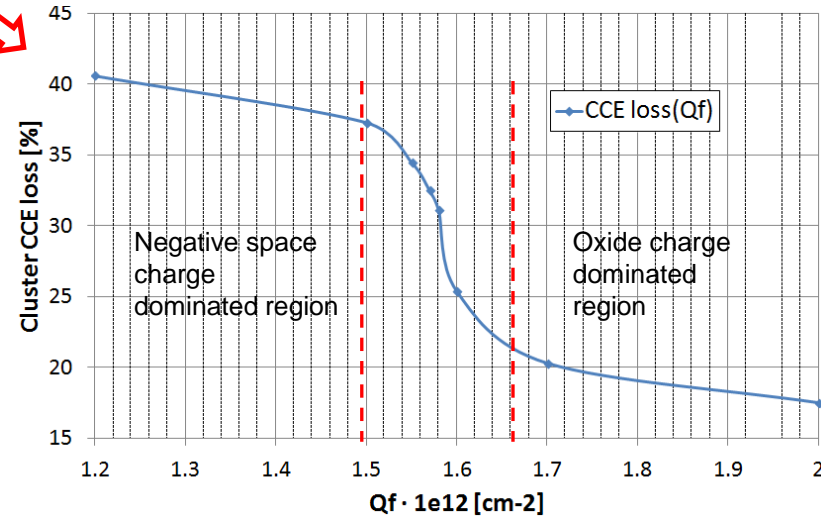
17.5%



When strips are isolated: Q_{coll} at center strip increases as position of charge injection moves closer & Q_{coll} at 2nd strip drops down

Acceptor traps remove both accumulation layer & signal electrons: **better radiation damage induced strip isolation \rightarrow larger CCE loss between the strips**

Increased $Q_f \rightarrow$ more traps are filled \rightarrow charge sharing between strips increases, undepleted region between strips grows \rightarrow CCE loss decreases



CCE(x): Implementation of interface traps

□ 1: parameters used in Silvaco

Type of defect	Level [eV]	σ_e [cm ²]	σ_h [cm ²]	Density [cm ⁻²]
Deep acceptor	$E_C - 0.60$	1e-15	1e-15	$0.6 \cdot N_{it}$
Shallow acceptor	$E_C - 0.39$	1e-15	1e-15	$0.4 \cdot N_{it}$

□ 2: 1 shallow acceptor

Type of defect	Level [eV]	σ_e [cm ²]	σ_h [cm ²]	Density [cm ⁻²]
Shallow acceptor	$E_C - 0.40$	8e-15	2e-14	$1.0 \cdot N_{it}$

□ 5-strip 200P region 5 sensor @ $V=-1$ kV, $T=253$ K

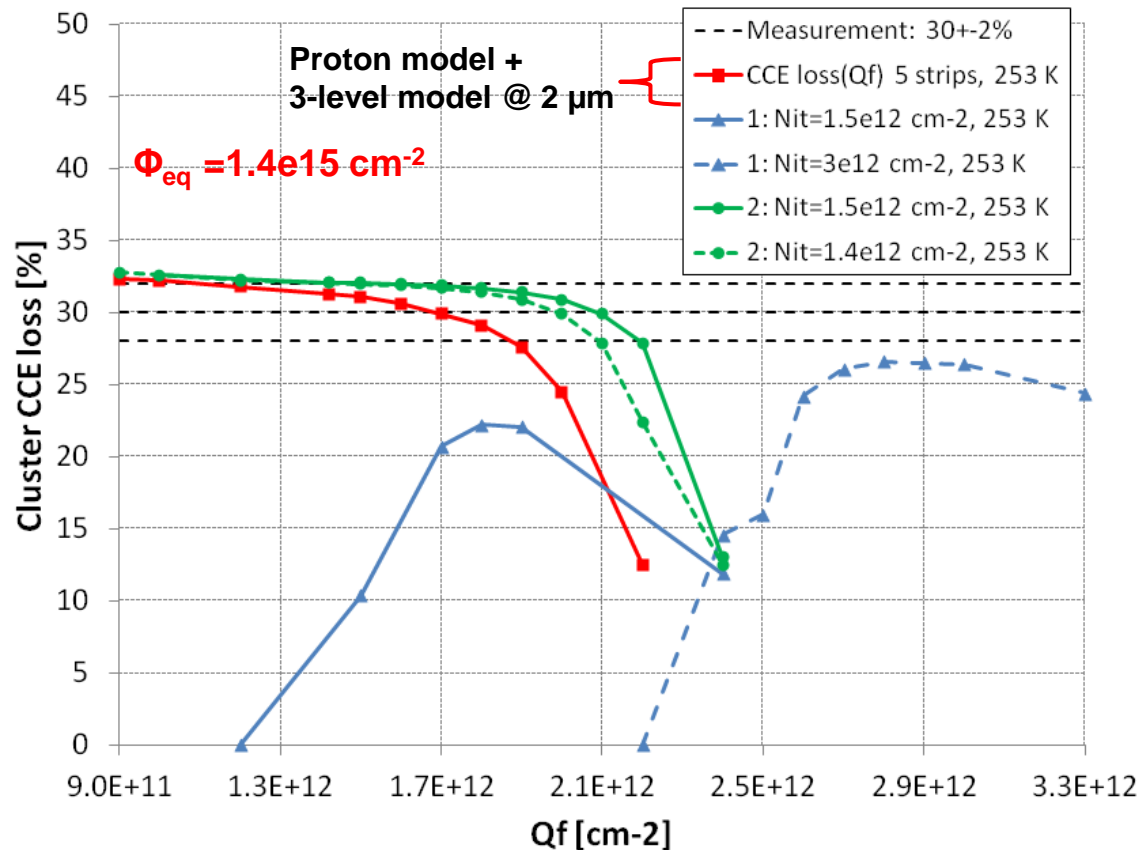
□ Simulated radiation damage:

- Proton model + interface traps from table 1(2)
- Proton model + 3-level model @ $2 \mu\text{m}$ from surface

□ 1: Measured CCE loss is not reproduced with realistic Q_f values

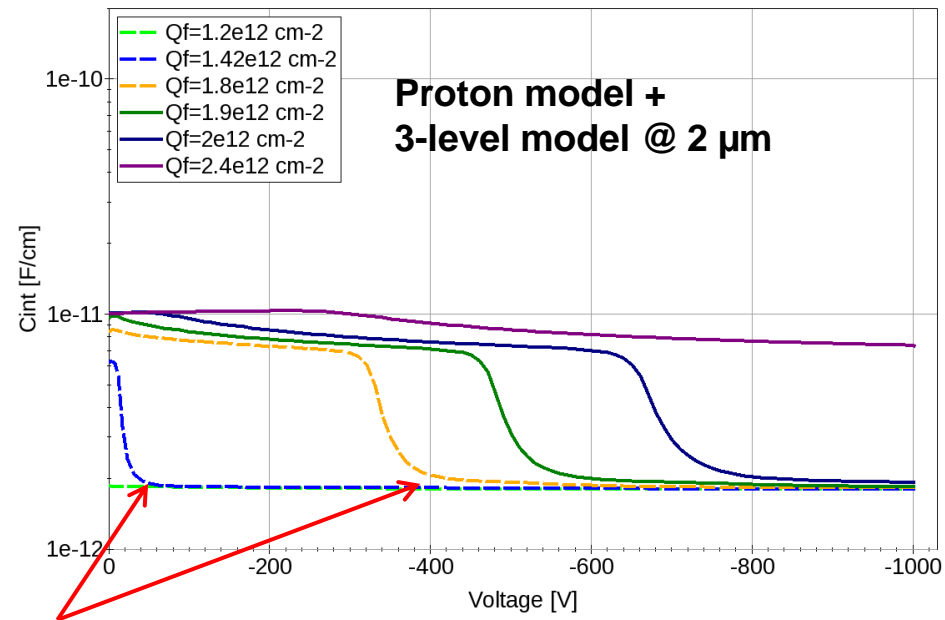
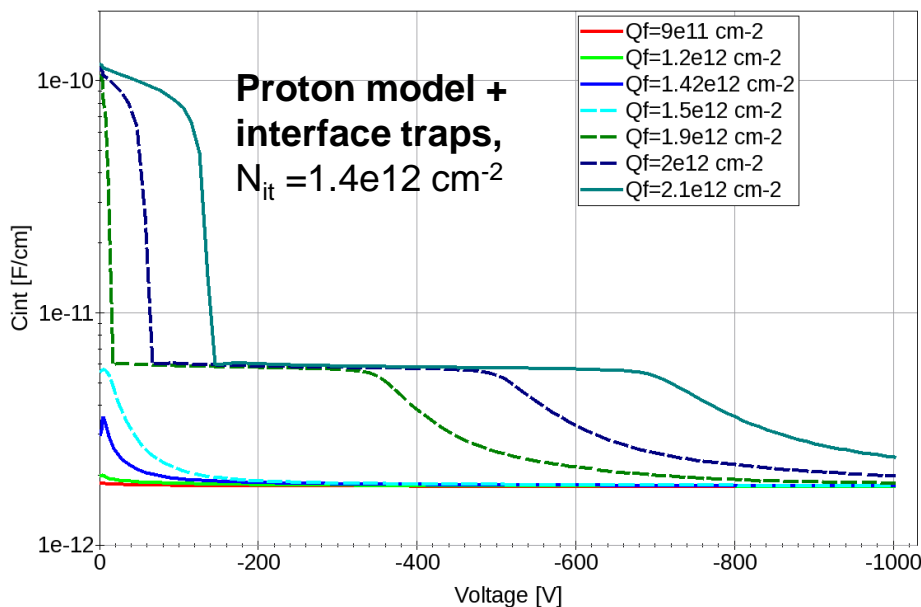
- Deep acceptors increase negative undershoots at strips with longer drift distances of carriers \rightarrow CCE loss goes to zero also at high Q_f

□ 2: Measured CCE loss is reproduced @ $Q_f=(1.9 \pm 0.3)10^{12} \text{ cm}^{-2}$ for $N_{it}=1.5e12 \text{ cm}^{-2}$, $Q_f=(1.8 \pm 0.3)10^{12} \text{ cm}^{-2}$ for $N_{it}=1.4e12 \text{ cm}^{-2} \rightarrow$ possible to tune Q_f range of matching CCE loss



C_{int} : N_{it} vs non-unif. 3-level model @ $\Phi_{eq} = 1.4e15 \text{ cm}^{-2}$

- ❑ Device structure corresponding to previous slide
- ❑ **Dashed lines:** Q_f values where CCE loss between strips matches measurement
- ❑ **3-level model @ 2 μm from surface:**
 - Geometrical value $\sim 1.8 \text{ pF/cm}$ reached within 0-400 V when CCE loss matches measurement
- ❑ **Interface traps:**
 - Geometrical value reached within 180 V -1 kV when CCE loss matches measurement
 - Over $O(1)$ higher initial values at high Q_f
- ❑ **Measurement:** $C_{int} \sim 1.8 \text{ pF/cm}$ reached at 0 V



Higher $Q_f \rightarrow$ higher V needed to reach geometrical C_{int}

CCE(x): Interface traps @ $\Phi_{eq} = 3e14 \text{ cm}^{-2}$

□ 5-strip 200P region 5 sensor @ $V=-1 \text{ kV}$, $T=253 \text{ K}$

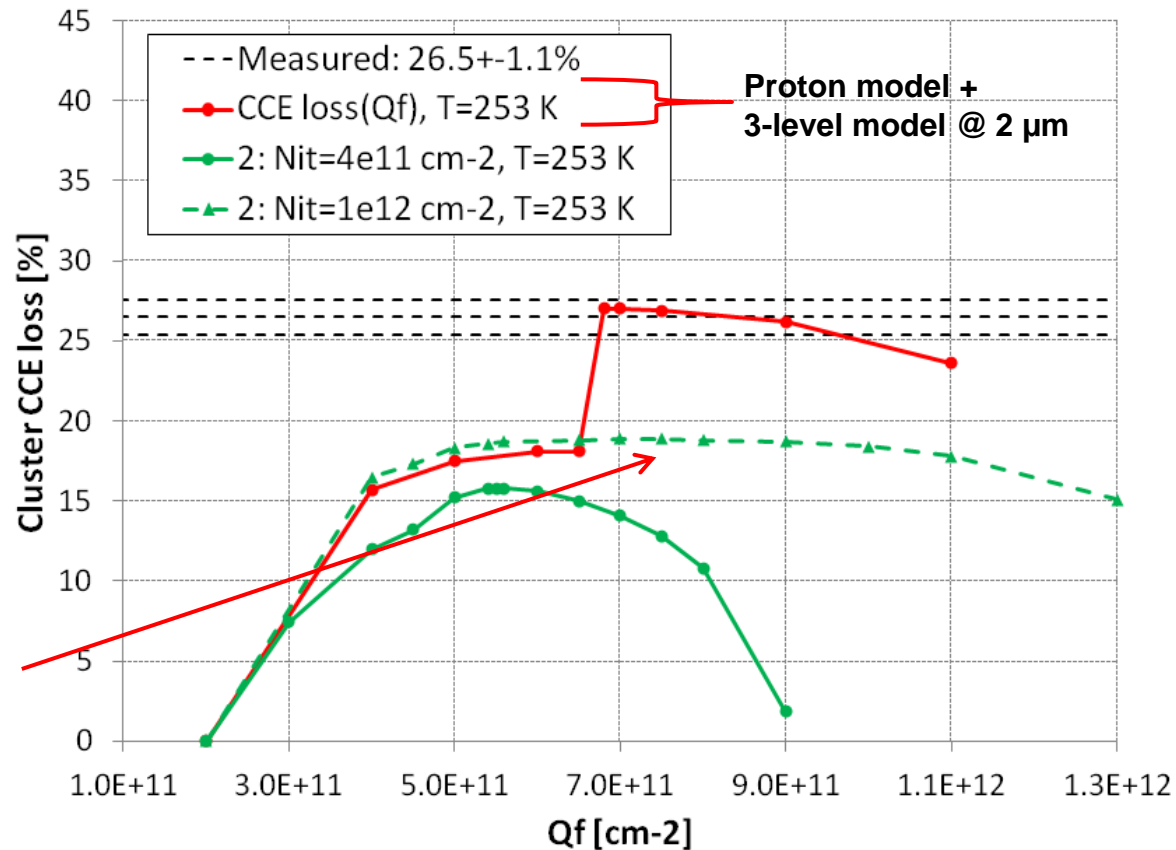
- Simulated radiation damage:
 - Proton model + interface traps from table 2
 - Proton model + 3-level model @ $2 \mu\text{m}$ from surface

□ 2: Measured CCE loss is not reproduced with realistic Q_f values → **not possible to parametrize $N_{it}(\Phi)$ with trap levels/parameters under test**

Very high N_{it} still produces only ~18% CCE loss between strips

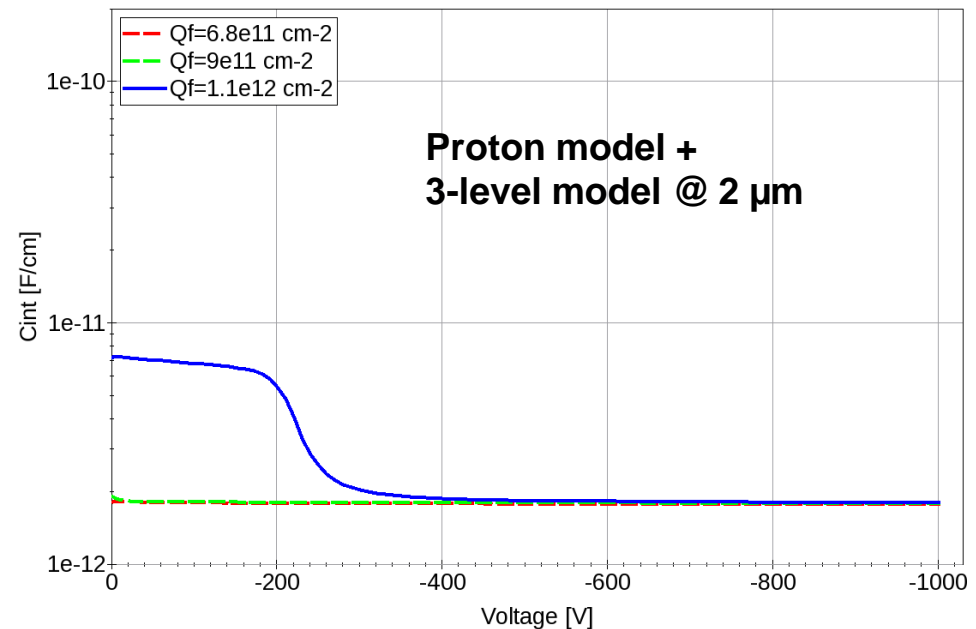
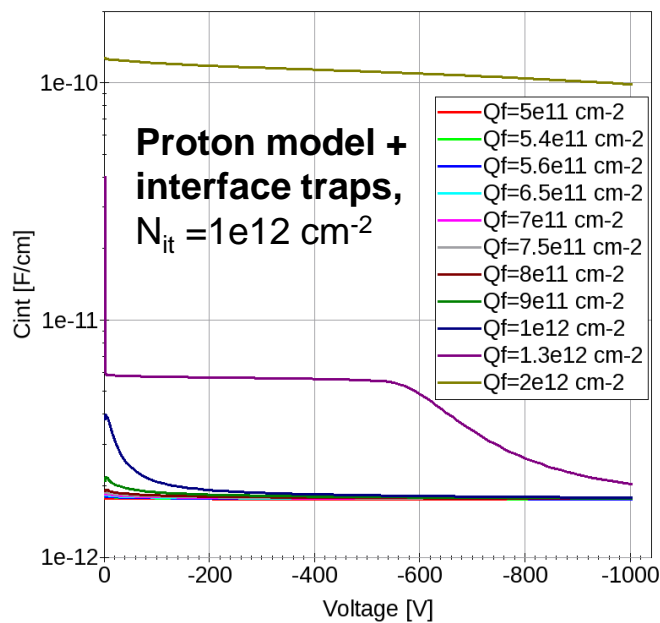
□ 2: 1 shallow acceptor

Type of defect	Level [eV]	σ_e [cm^2]	σ_h [cm^2]	Density [cm^{-2}]
Shallow acceptor	$E_C - 0.40$	$8e-15$	$2e-14$	$1.0 \cdot N_{it}$



C_{int} : N_{it} vs non-unif. 3-level model @ $\Phi_{eq} = 3e14 \text{ cm}^{-2}$

- ❑ Device structure corresponding to previous slide
- ❑ **3-level model @ 2 μm from surface:**
 - Geometrical value $\sim 1.8 \text{ pF/cm}$ reached at 0 V when CCE loss matches measurement
- ❑ **Interface traps:**
 - Geometrical value reached at low V up to $Q_f = 1e12 \text{ cm}^{-2}$ (no match with measured CCE loss)
- ❑ **Measurement:** $C_{int} \sim 1.8 \text{ pF/cm}$ reached at 0 V

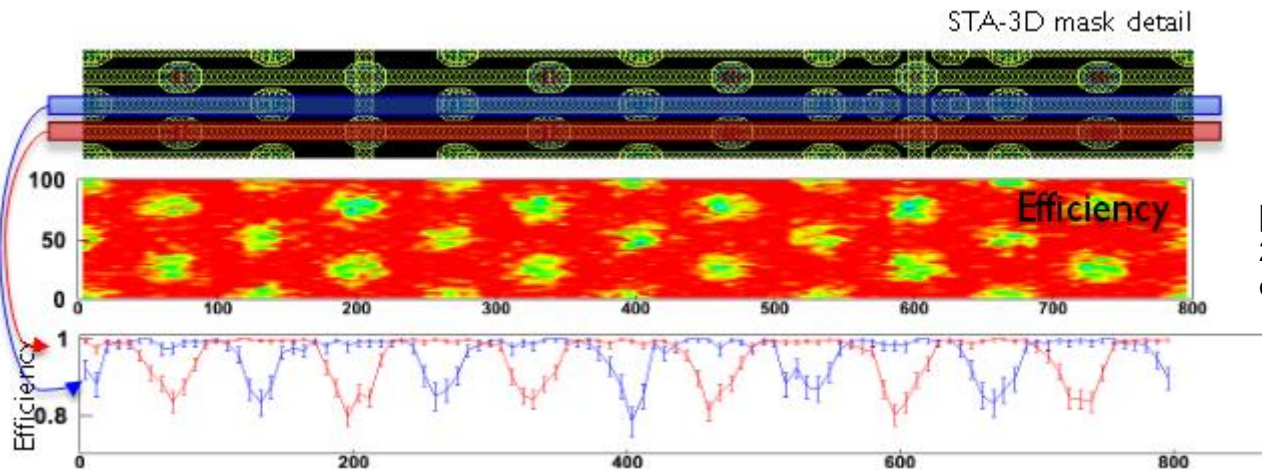


Conclusion from slides 7-10: Deeper distribution of shallow acceptors reproduces measured CCE loss between strips & C_{int} more closely

3D sensor simulations

- ❑ **3D sensors:** Most promising choice for extremely high fluence environments. Now populate 25% of the ATLAS IBL
- ❑ **3D geometry:** Large signal & reduced trapping probability → **higher radiation tolerance**
- ❑ **Downsides of 3D sensors include hit position dependent signal size:**

Electrodes are parallel to track ($B=0$, 0°) at normal incidence



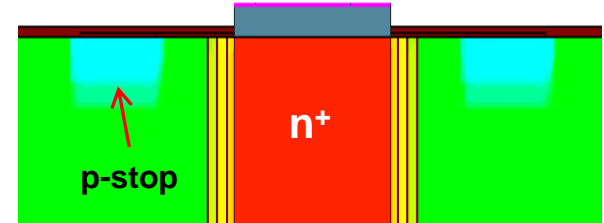
[P. Hansson et al., SLAC, 2010 Vienna Conference on Instrumentation]

- ❑ **TCAD simulations can also be applied for the design optimization of 3D sensors**
- ❑ **Possible to simulate hit position dependence of 3D sensor with realistic thickness?**

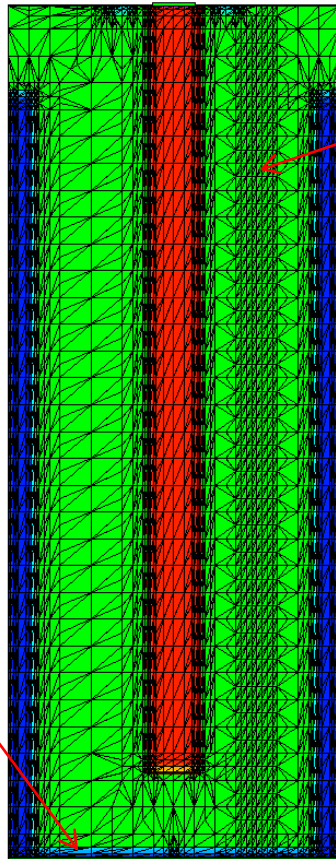
Double type column 3D-sensor simulations: structure

- ❑ **Double-side double type column 3D-sensor structure**
- ❑ 55x55x200 μm^3 structure, 250 nm oxide layer & 500 nm Al on both planes
- ❑ P-type bulk with 180 μm n^+/p^+ columns ($r = 5 \mu\text{m}$)
- ❑ All p^+ contacts connected together by the backplane Al
- ❑ p-stop depth = 1.5 μm , $r_{\text{in}} = 10 \mu\text{m}$, $r_{\text{out}} = 15 \mu\text{m}$

DC-coupled front contact ($R_{\text{bias}} = 50 \Omega$)



Diagonal cut

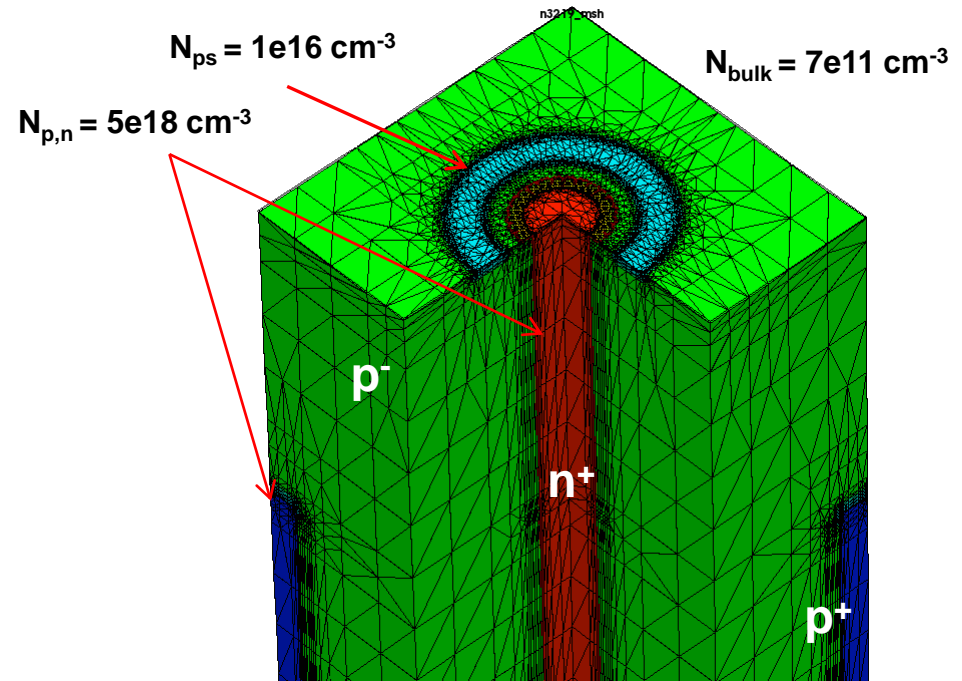


- ❑ Reduced mesh size around MIP trajectory

- ❑ 29 615 mesh points (> 30 k: memory allocation crash)
- ❑ Column doping profiles by error function

- ❑ p^+ doping at backplane to reduce low field region

Doping profiles
(Oxide layer transparent for clarity)



- Symmetrical E distribution is produced: hot spots at column ends
- Full depletion voltage $V_{fd} \approx 10$ V: agrees with measured

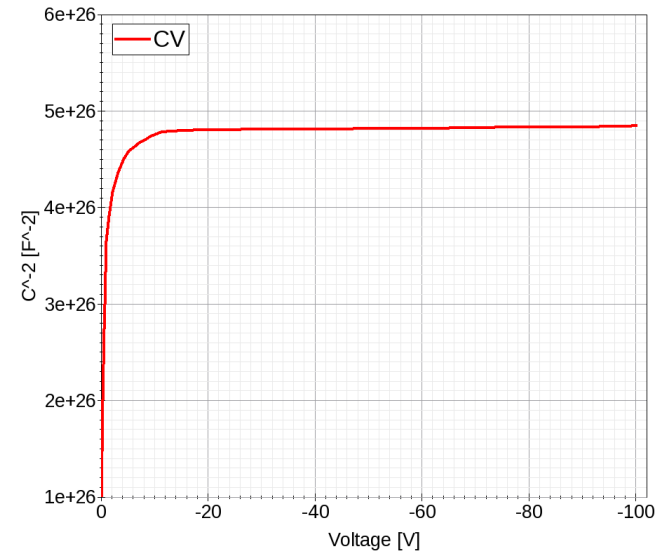
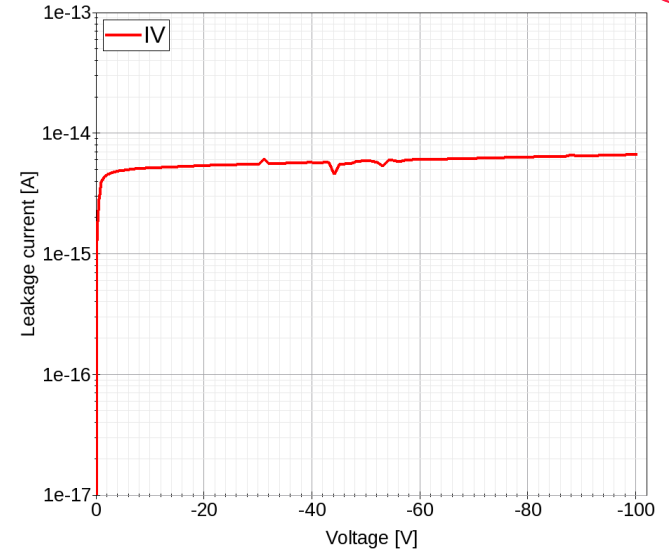
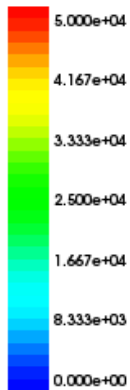
Diagonal cuts:

E @ V=-20 V:

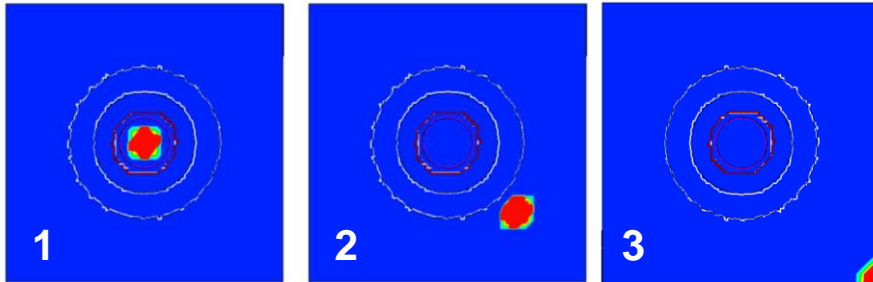
E @ V=-100 V:

- Low field regions @ p-stop curvatures & p-column corners

Abs(ElectricField-V) ($V \cdot cm^{-1}$)

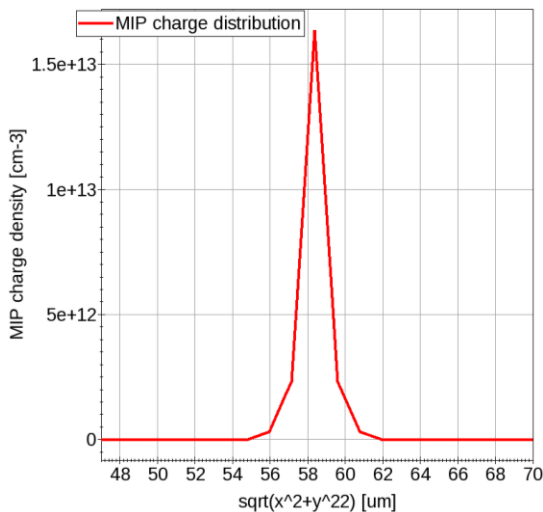


Double-column 3D-sensor: Signal hit position dependency

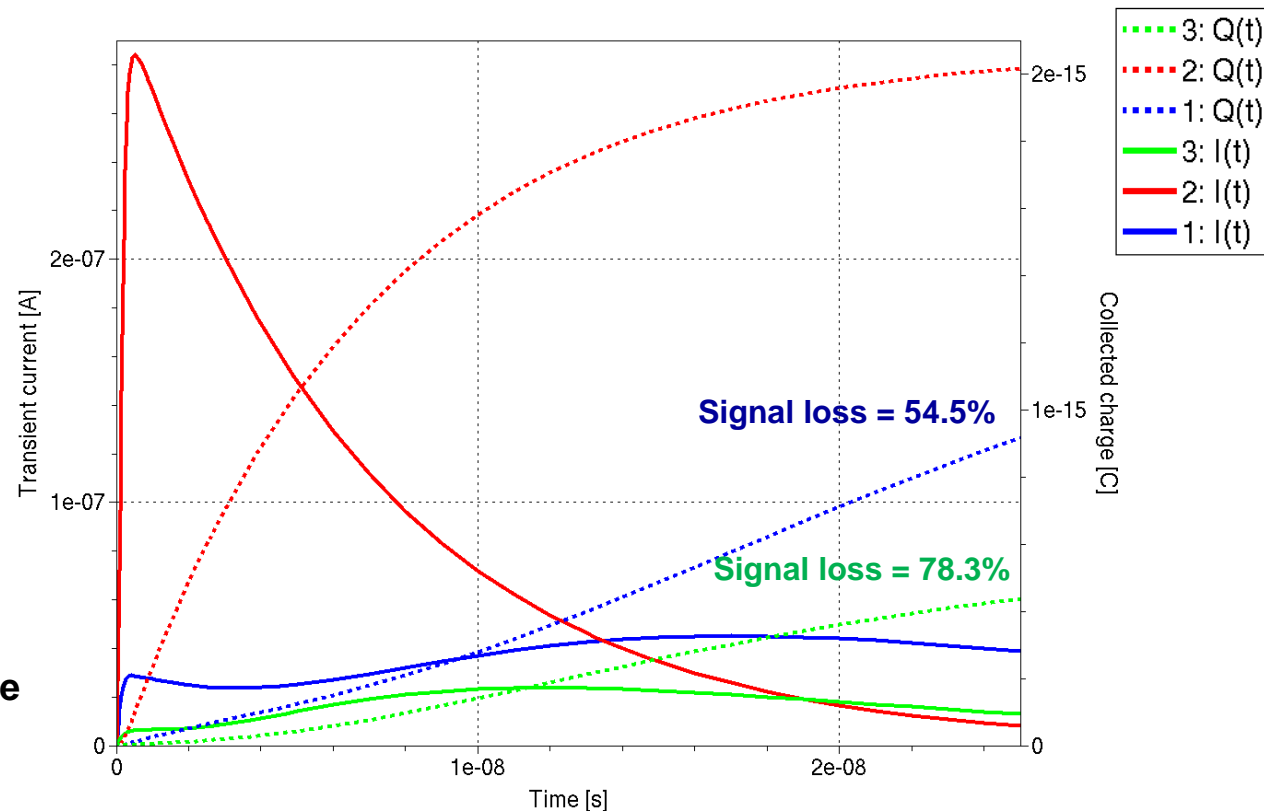


- Hit position 1: n+ column
- Hit position 2: halfway between columns
- Hit position 3: p+ column
- Operation @ V = -100 V
- Long collection time: to be investigated (mesh size, collection electrode parameters?)

Gaussian spread of MIP generated charge density, LET: $\sigma = 1 \mu\text{m}$



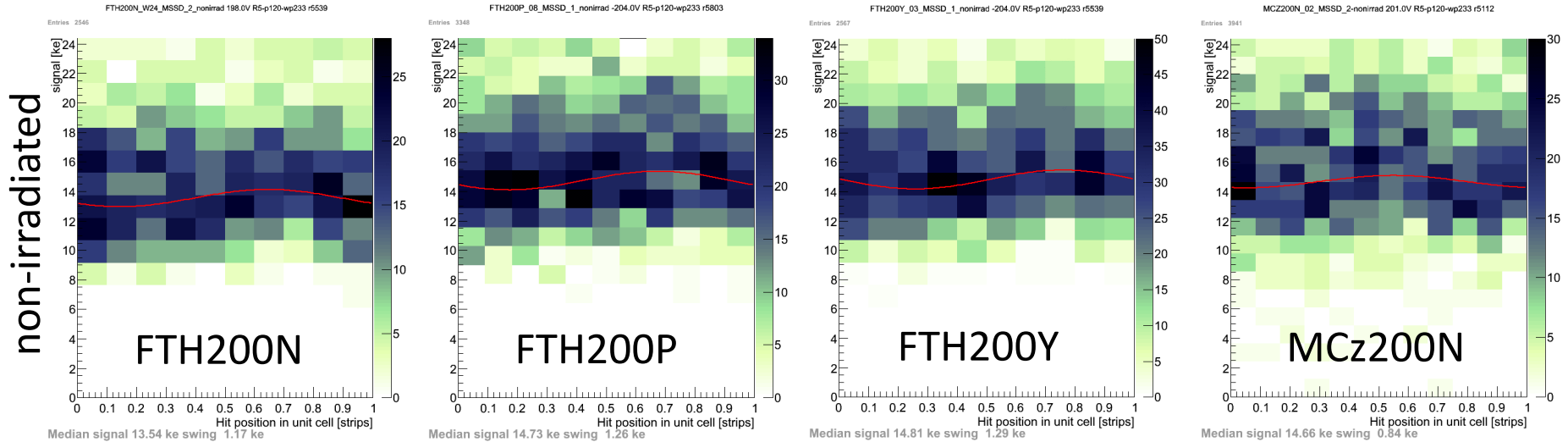
- Charge collection in electrode region: **Measured signal loss 40-60%** [J. Hasi, PhD thesis]



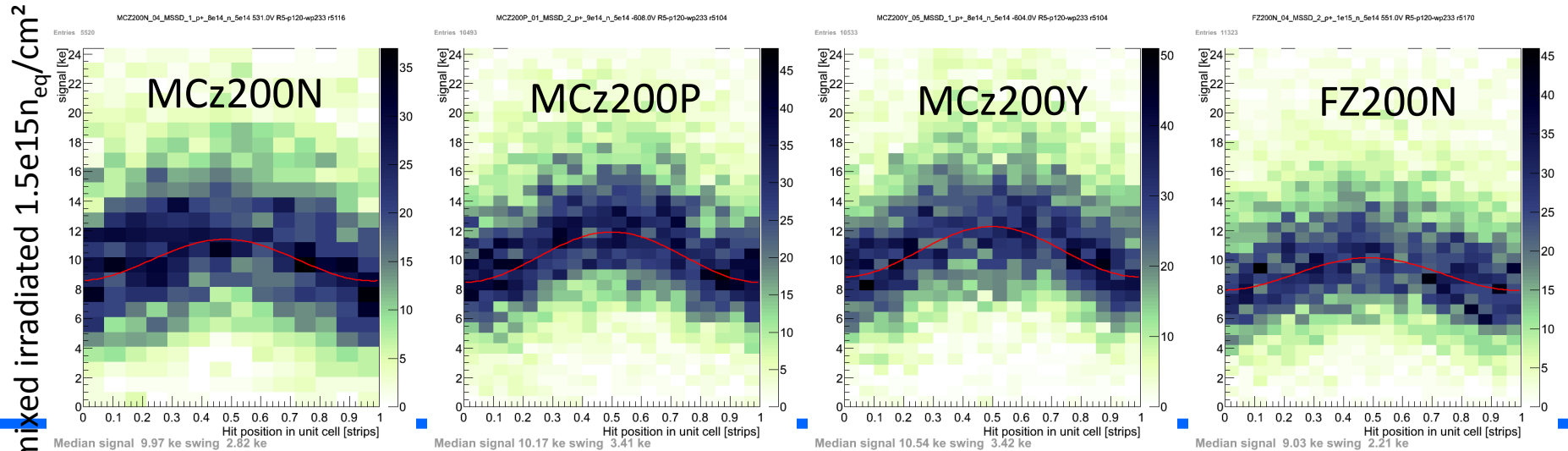
- ❑ **2-level model + interface traps & non-uniform 3-level model were applied for CCE(x) & C_{int} simulations in Sentaurus TCAD**
- ❑ **Interface traps:**
 - Deep & shallow acceptors do not reproduce agreement with measurement for the investigated fluence & Q_f range
 - One shallow acceptor reproduces measured CCE(x) @ $\Phi_{eq} = 1.4e15$ cm⁻², no match at lower fluence → high initial C_{int} values & geometrical value reached only after > 180 V
- ❑ **Interpretation: Deeper distribution of shallow acceptors reproduces measured surface properties more closely**
- ❑ **Double-side double type column 3D-sensor simulated successfully**
 - Hit position dependence reproduced: **over 50% smaller charge collected from column hit**
 - Further investigation needed for unexpectedly long collection times

Backup: SiBT measured CCE loss between strips

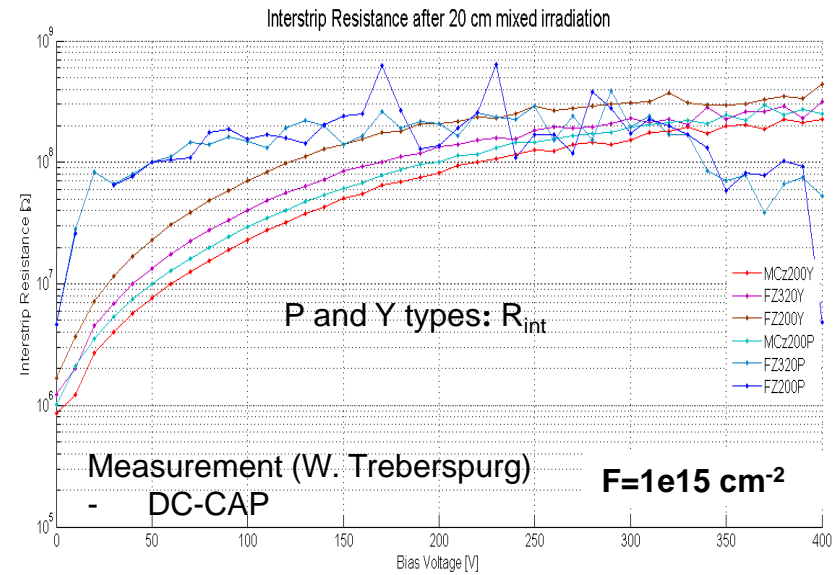
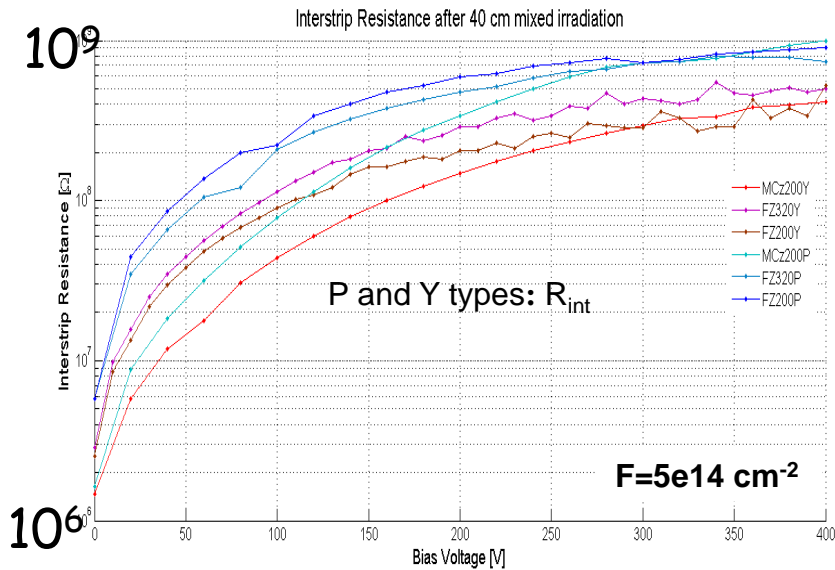
Signal loss in-between strips ($p=120\mu\text{m}$, $w/p\sim 0.23$)



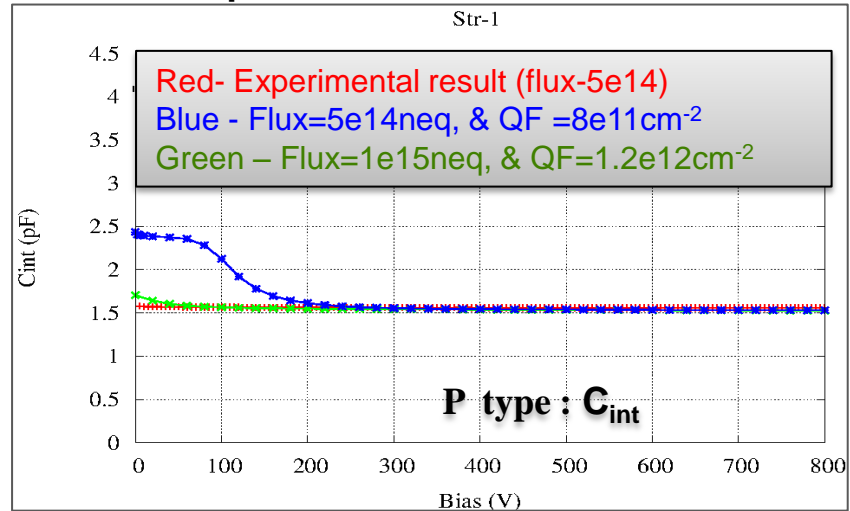
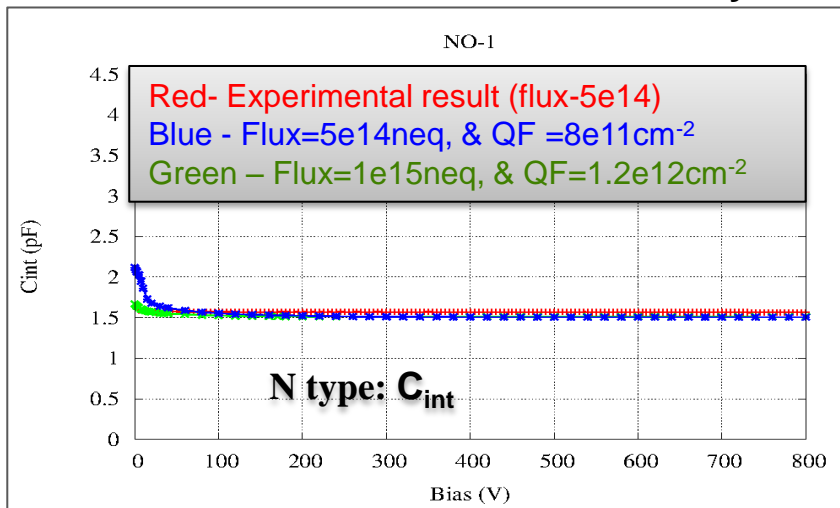
No loss before irr.; after irr. $\sim 30\%$ loss; all technologies similar [Phase-2 Outer TK Sensors Review]



Backup: Measured R_{int} & C_{int}



Simulations by Silvaco Atlas 5-trap model



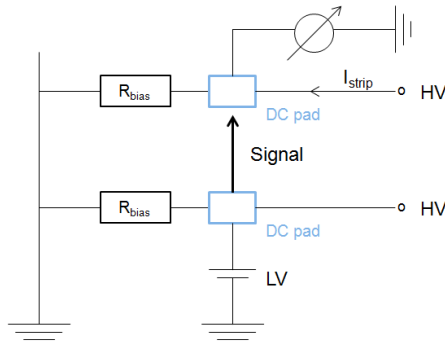
Backup: simulated R_{int} & C_{int}

- 3 strip structure, $V_{strip1} = V_{strip3} = 0$, $V_{strip2} = LV$ and $0 V$
- $V = -HV$ at the backplane
- Interstrip resistance (R_{int}) is defined as (Induced Current Method):

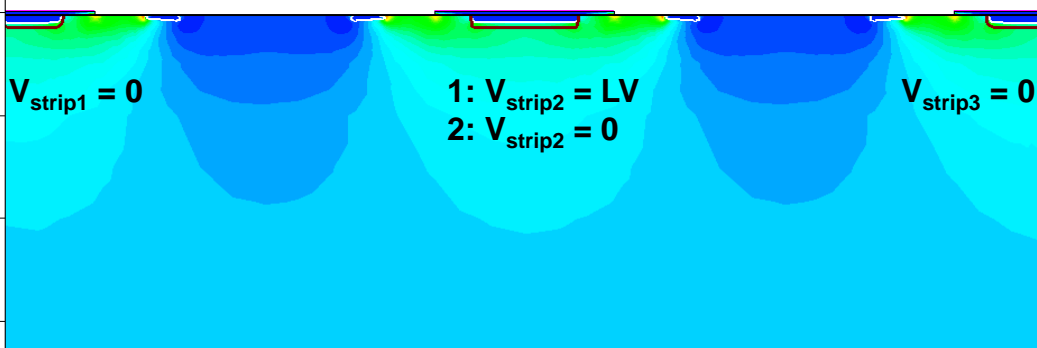
$$R_{int} = \frac{V_2(LV)}{\frac{I_1(LV) + I_3(LV)}{2} - \frac{I_1(0) + I_3(0)}{2}}$$

- R_{int} is plotted as a function of applied voltage V

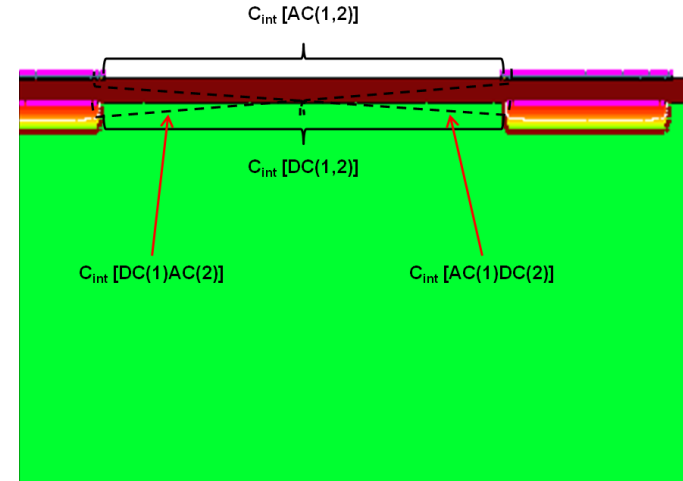
- Electrical circuit diagram of R_{int} measurement:



R_{int} simulation principle



C_{int} simulation principle



$$C_{int} = 2 * [AC(1,2) + DC(1,2) + AC(1)DC(2) + DC(1)AC(2)]$$