

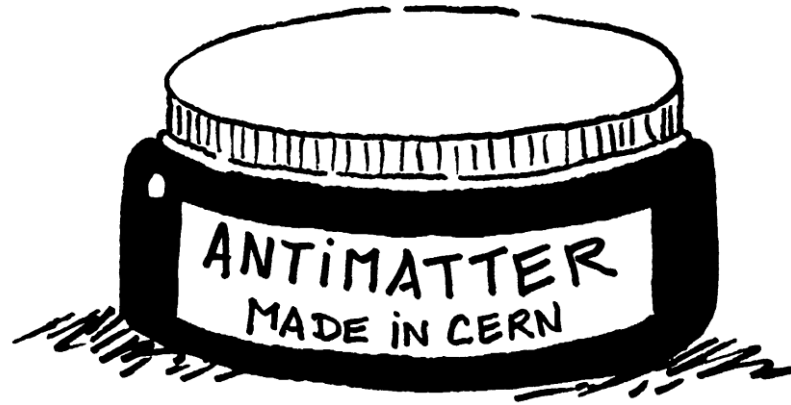
MCP ANALYSIS ON ALPHA

Howard Chiao



What Is ALPHA?

- **A**ntihydrogen
- **L**aser
- **P**Hysics
- **A**pparatus
- Located at the Antiproton Decelerator (AD), a storage ring at CERN
- Collaboration between 14 universities and 7 countries

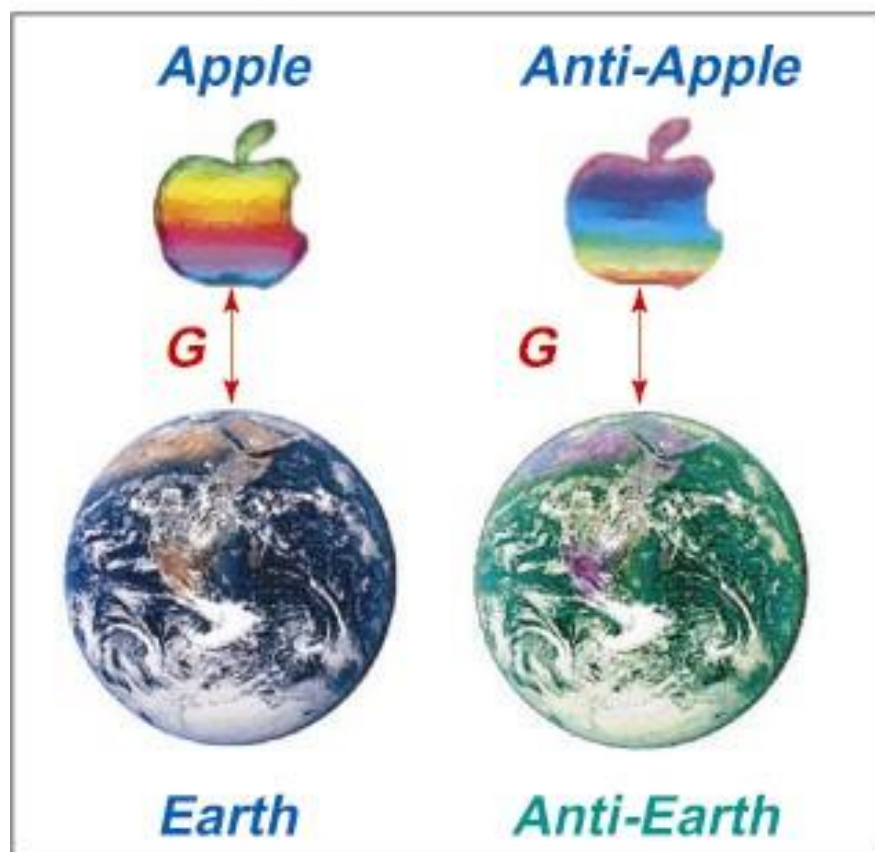


What Does ALPHA Do?

- Mixing positrons and antiprotons to create antihydrogen in low temperature
- Trap antihydrogen in a magnetic trap
- Test CPT symmetry through comparison of the atomic spectra
- Find out the interaction between gravity and antimatter

What Does ALPHA Do?

CPT Symmetric Situation



Not:



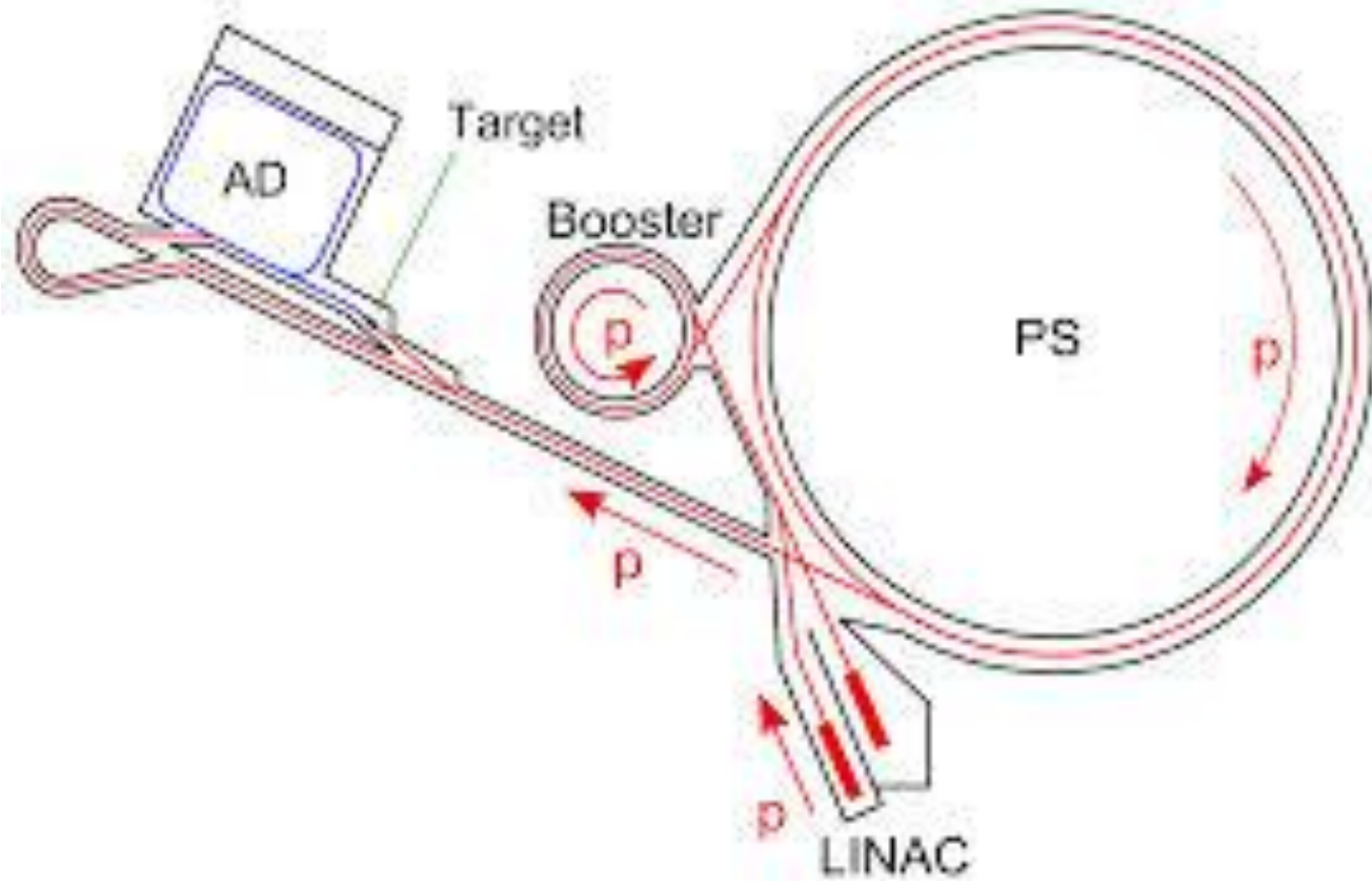
How To Get Positron?

- ALPHA uses the positron produced in the beta-plus (e^+) decay of sodium-22 atoms.













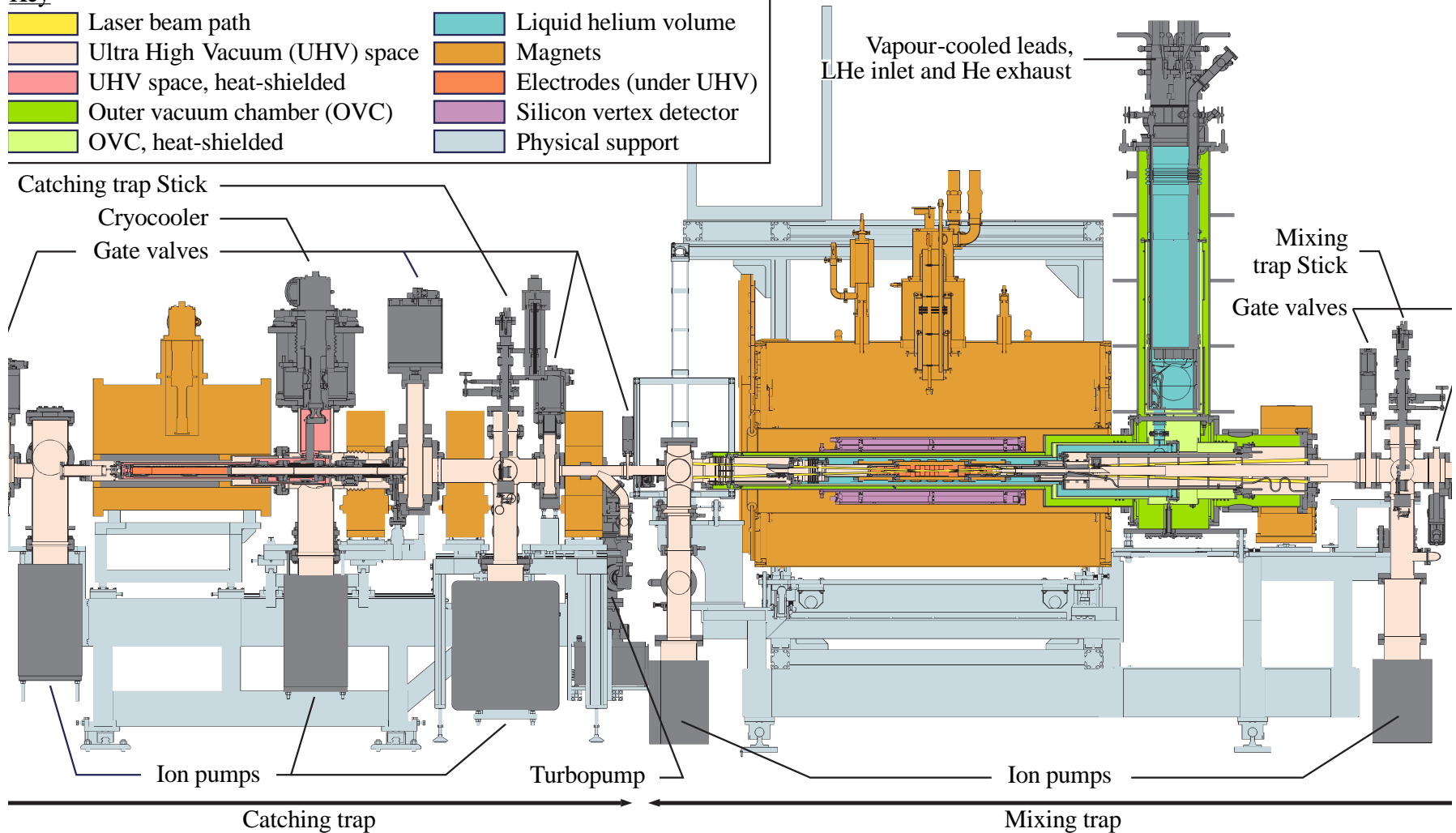
- Collides with nitrogen molecules to lose energy by producing an excitation of nitrogen.
- Positrons are then sent to the accumulator.

How To Get Antiproton?



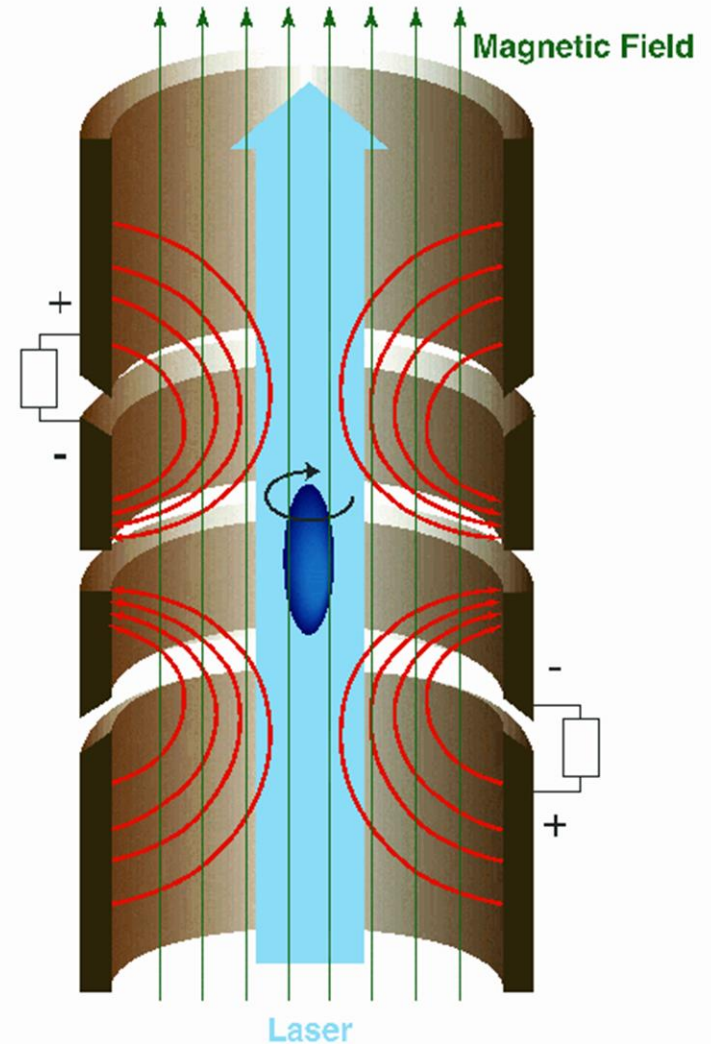
The Setup of ALPHA Apparatus

Key	
	Laser beam path
	Ultra High Vacuum (UHV) space
	UHV space, heat-shielded
	Outer vacuum chamber (OVC)
	OVC, heat-shielded
	Liquid helium volume
	Magnets
	Electrodes (under UHV)
	Silicon vertex detector
	Physical support



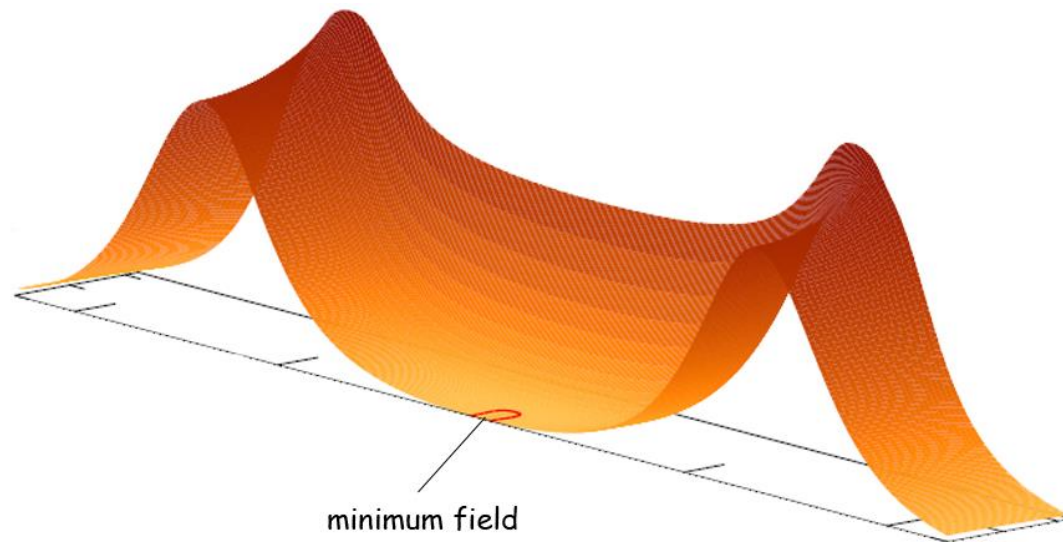
Penning Trap

- A widely-used charged particle trap
- Uniform magnetic field
- Quadratic electric potential



Magnetic Trap

- Trapping neutral particles with magnetic dipole moments (ex. Antihydrogen)
- Only antihydrogen with temperature lower than 0.5K can be trapped.

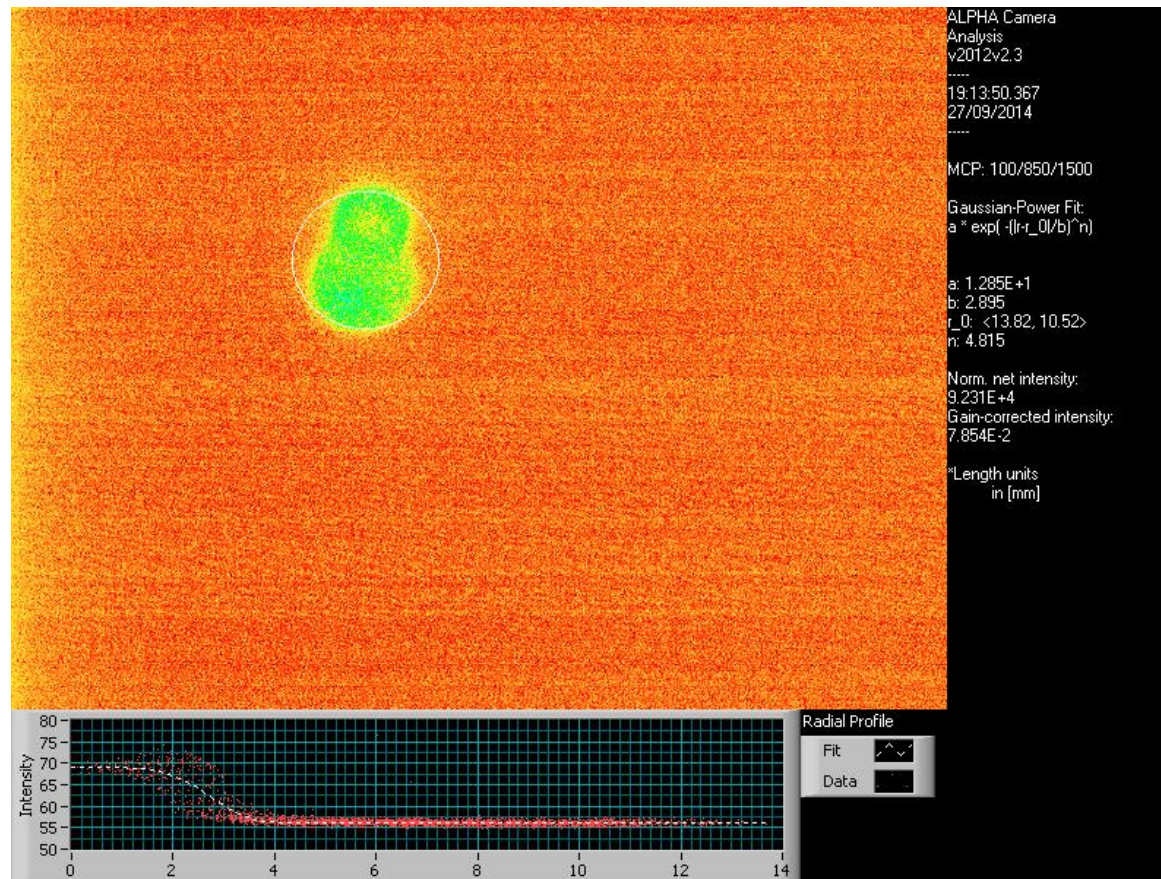


Microchannel Plate Detector (MCP)

- The “camera” of particle beams.
- An array of small electron multipliers
- Large potential difference across
- Particles are accelerated onto a phosphor screen to produce light.

My Project

- Image recognition and analysis for two circles



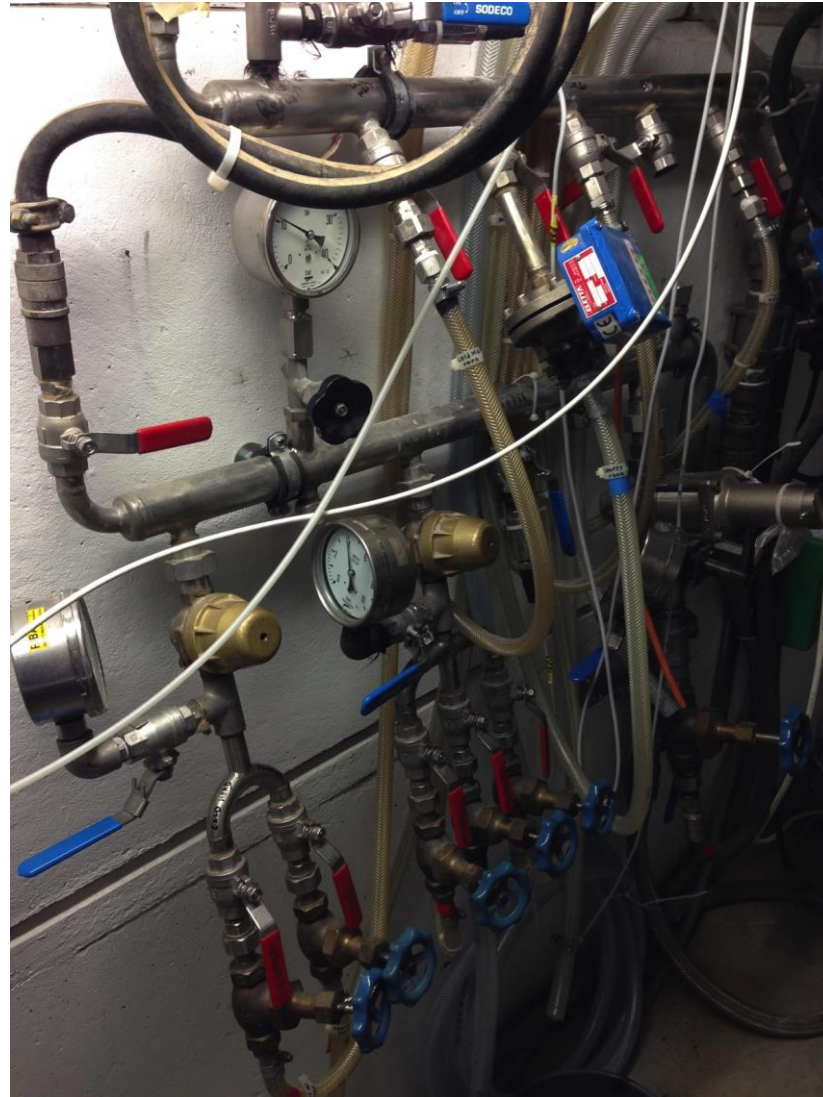
Other Responsibilities

- Taking shifts
- Assembling zombie computers
- Helium transfer
- Hardware setup (e.g. MCP Voltage Test, PMT, Soldering, Plumbing, etc.)
- Optimize beam (e.g. Running Sequences)
- Temperature Analysis for Pbar and e+
- Trapping Analysis for antihydrogen
- Baselines

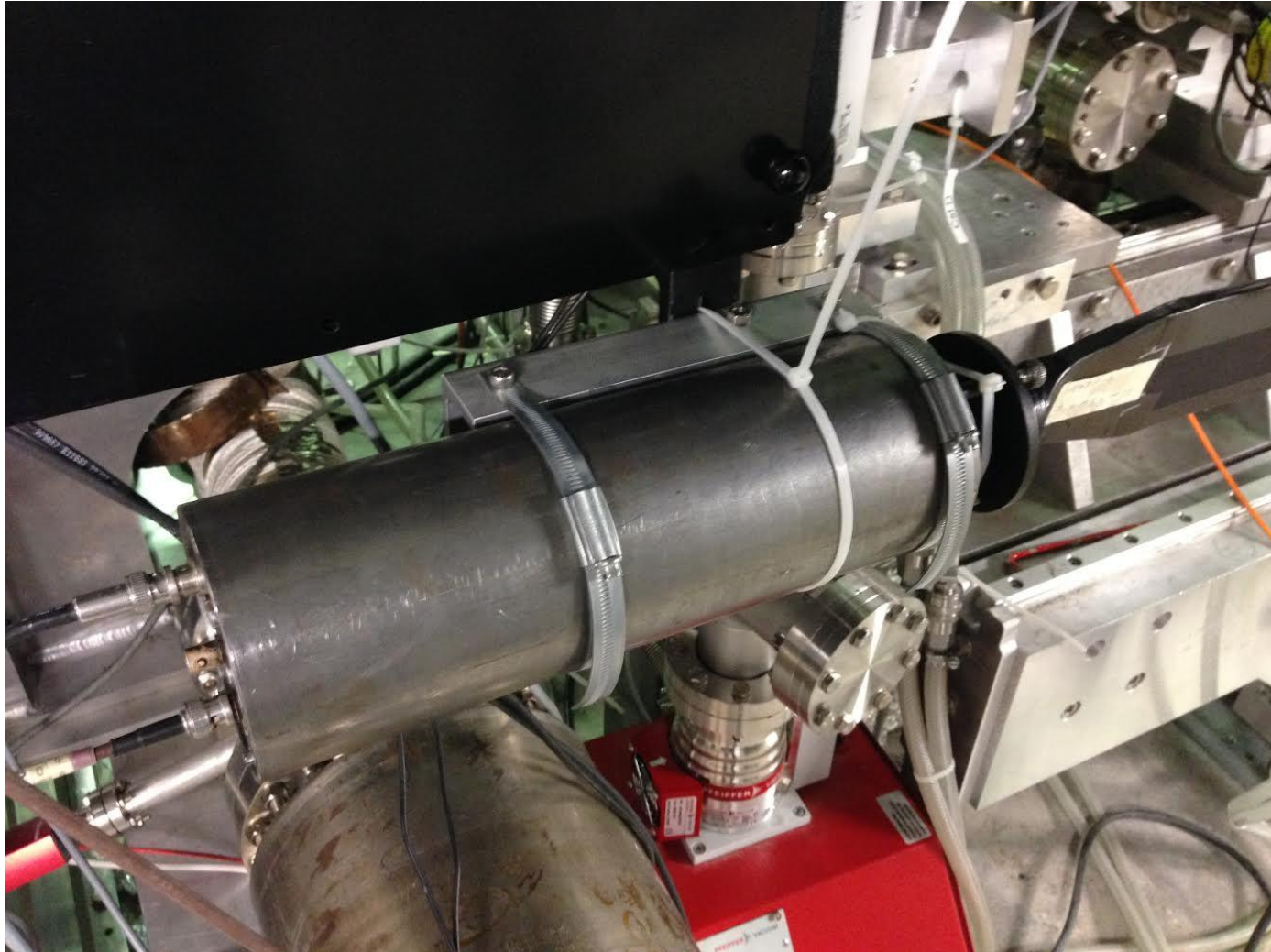
Helium Transfer



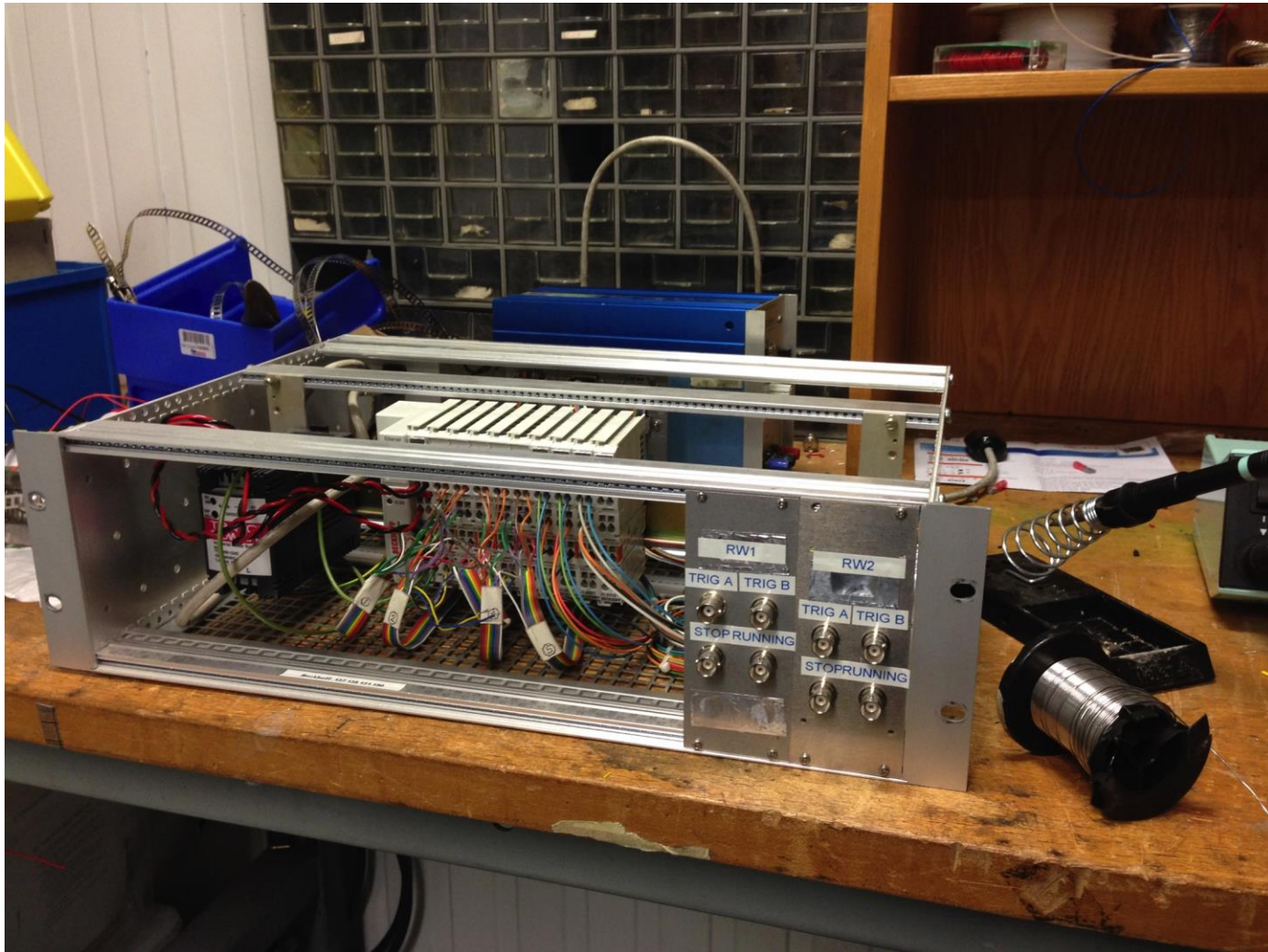
Plumbing



Setting Up PMT9



Soldering



Sequencer

The screenshot displays the MC Master Control Sequencer interface, which is divided into three main sections for Sequencer 1, Sequencer 2, and Sequencer 3. Each sequencer is configured with specific parameters and status indicators.

Sequencer 1: cat-Catching-cat

- Status Indicators (Sequencer 1):** Digital Driver (Green), Analog Driver (Green), MIDAS (Green).
- Queue A:** cat-Catching-cat (Ready). Queue description: "Enter Queue File Name here or use Open button".
- Preparation Queue:** Contains a sequence of events for "Pbars(20141106\CatchandDump_v2.23_stacks_and_timing.cat)".
- Execution Queue:** Queue: CatchNDump. Contains a sequence of events for "Pbars(20141106\CatchandDump_v2.23_stacks_and_timing.cat)".
- Control Buttons:** Execute Queue, Abort Queue Immediately, Abort Queue After Current Sequence Ends, Abort Queue At End of Current Queue Cycle, Reset or Zero All Outputs.

Sequencer 2: rct-Recatching-rct

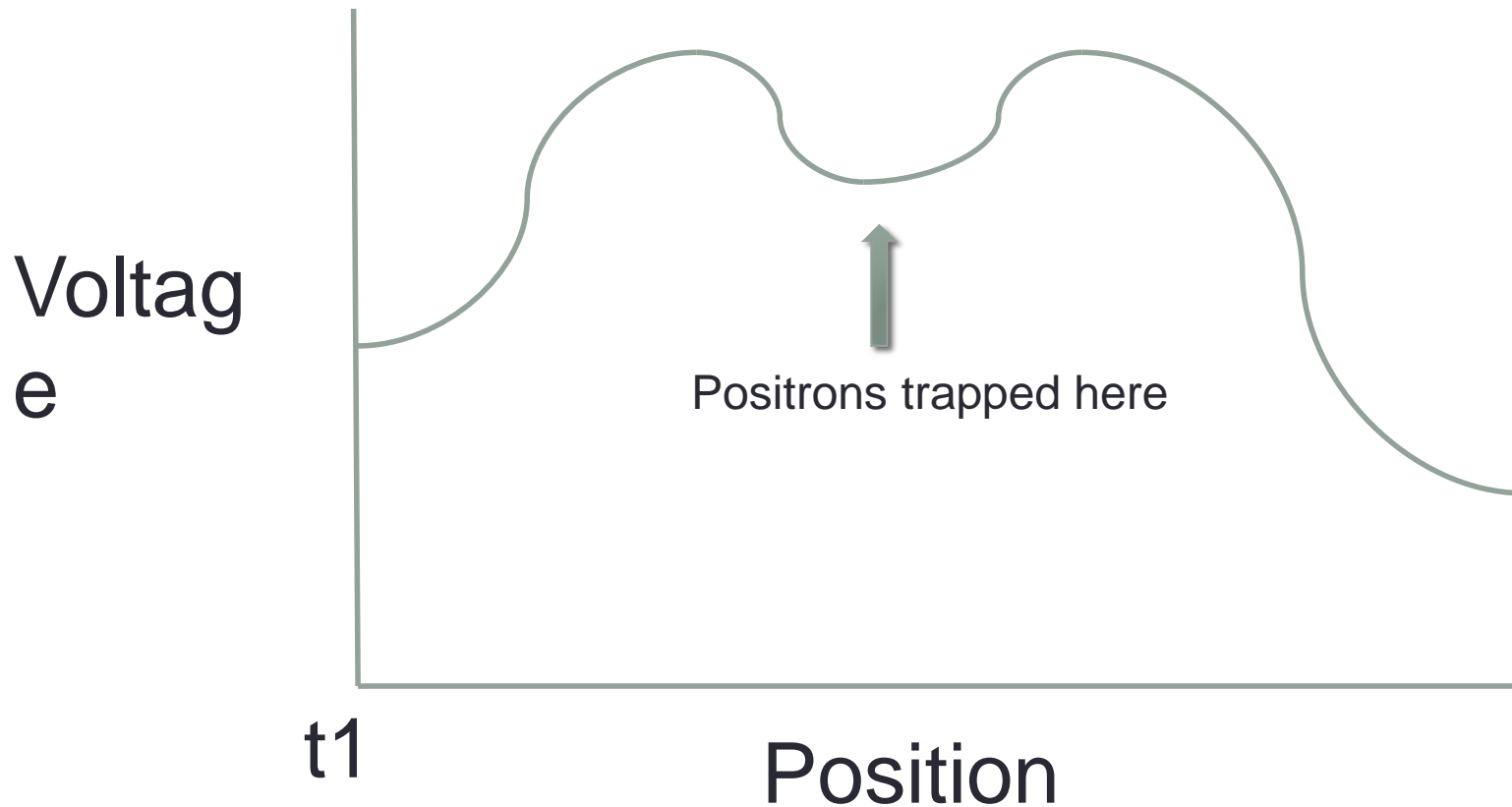
- Status Indicators (Sequencer 2):** Digital Driver (Green), Analog Driver (Green), MIDAS (Green).
- Queue A:** rct-Recatching-rct (Ready). Queue description: "Enter Queue File Name here or use Open button".
- Preparation Queue:** Contains a sequence of events for "Pbars(20141106\E13E14_Tdiag_Dump_v1.0.rct)".
- Execution Queue:** Queue: Queue A. Contains a sequence of events for "Pbars(20141106\E13E14_Tdiag_Dump_v1.0.rct)".
- Control Buttons:** Execute Queue, Abort Queue Immediately, Abort Queue After Current Sequence Ends, Abort Queue At End of Current Queue Cycle, Reset or Zero All Outputs.

Sequencer 3: atm-Atom-atm

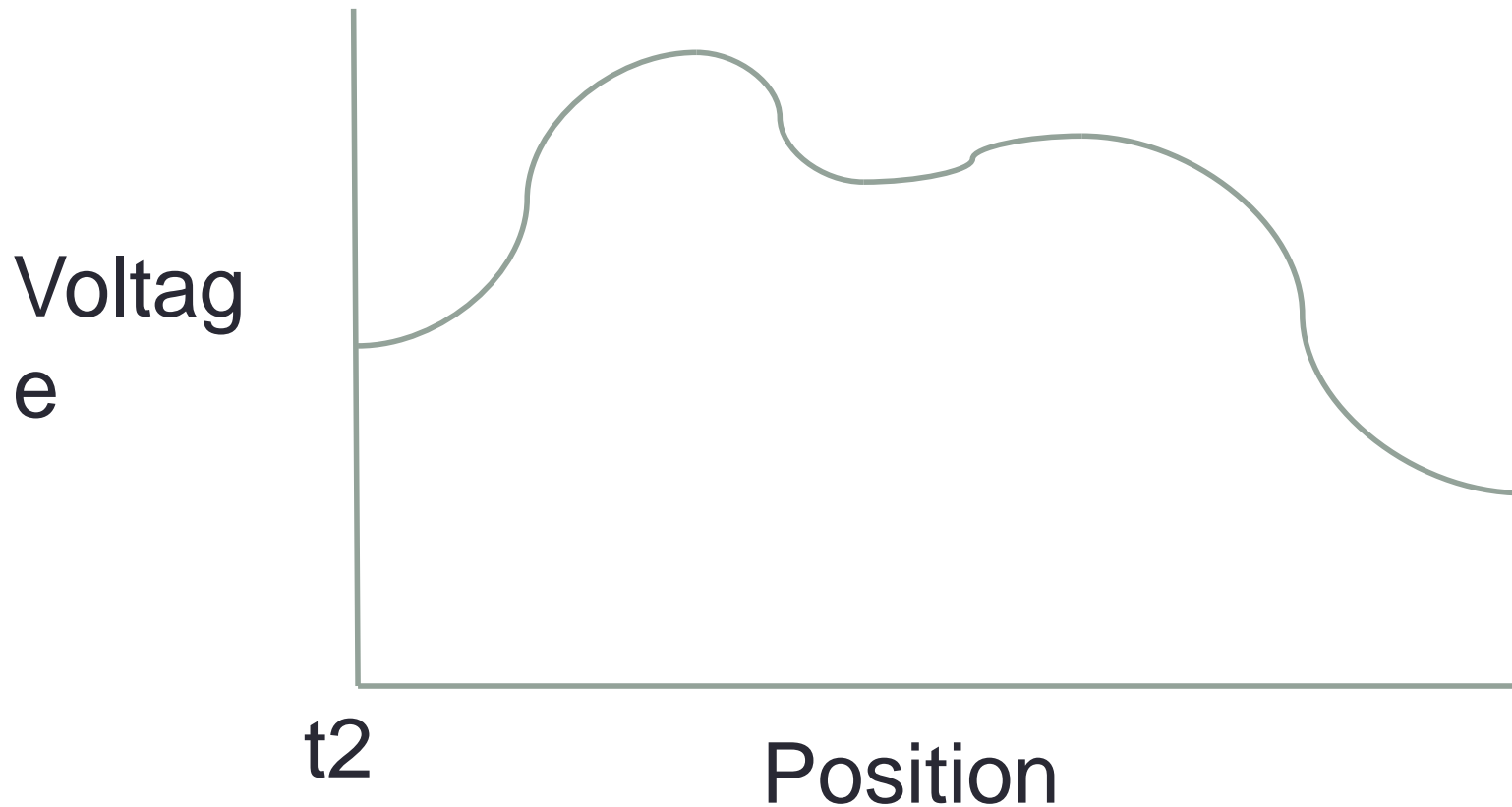
- Status Indicators (Sequencer 3):** Digital Driver (Green), Analog Driver (Green), MIDAS (Green).
- Queue A:** atm-Atom-atm (Ready). Queue description: "Enter Queue File Name here or use Open button".
- Preparation Queue:** Contains a sequence of events for "Pbars(20141109\ARMixing1.09.atm)".
- Execution Queue:** Queue: 59356. Contains a sequence of events for "Pbars(20141109\ARMixing1.09.atm)".
- Control Buttons:** Execute Queue, Abort Queue Immediately, Abort Queue After Current Sequence Ends, Abort Queue At End of Current Queue Cycle, Reset or Zero All Outputs.

The interface also includes a sidebar on the left with various control options (e.g., CatchNDump, Queue A, atm) and a bottom section for downloading sequencer events log entries.

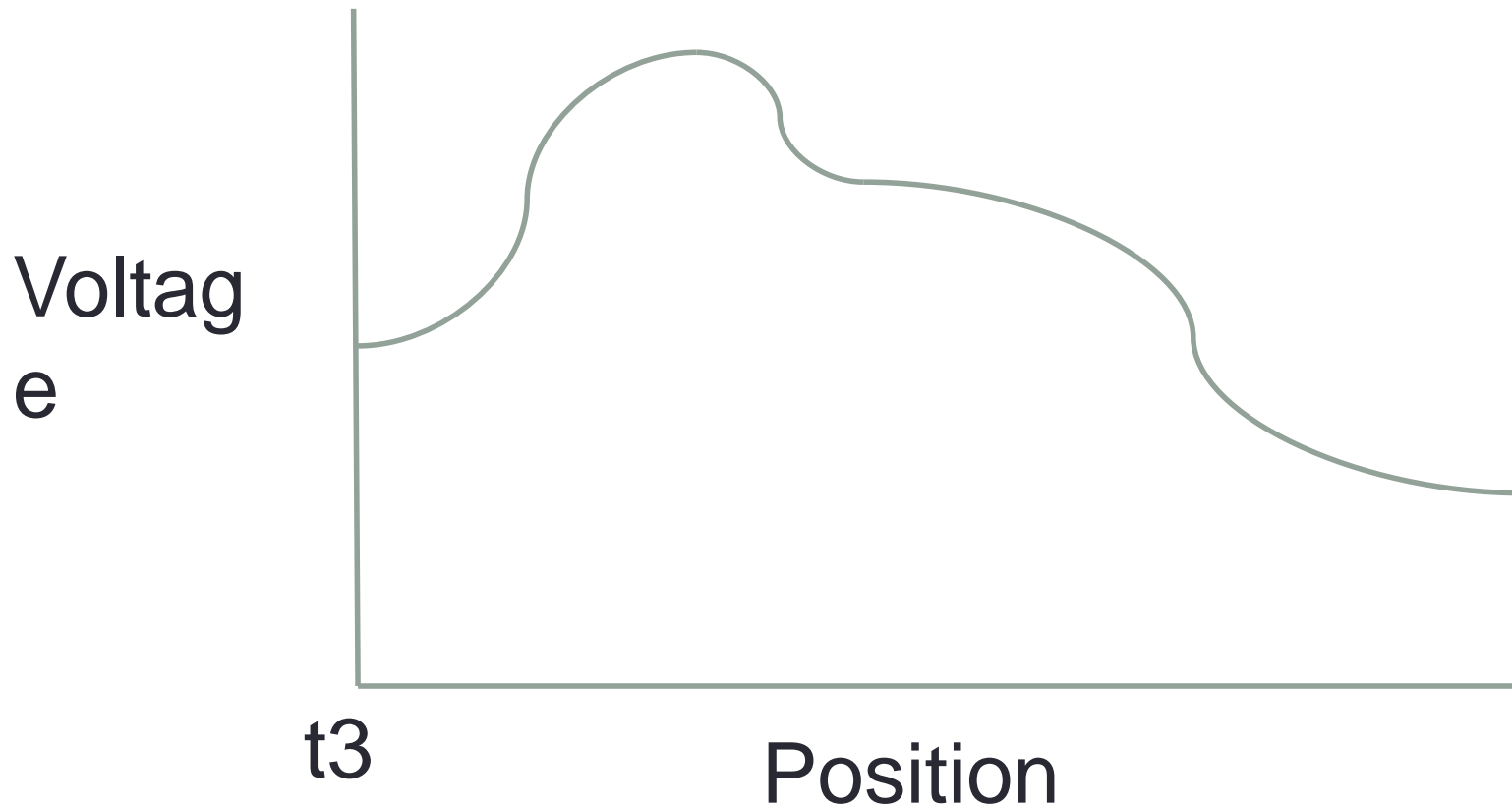
TDiag – Temperature Measurement



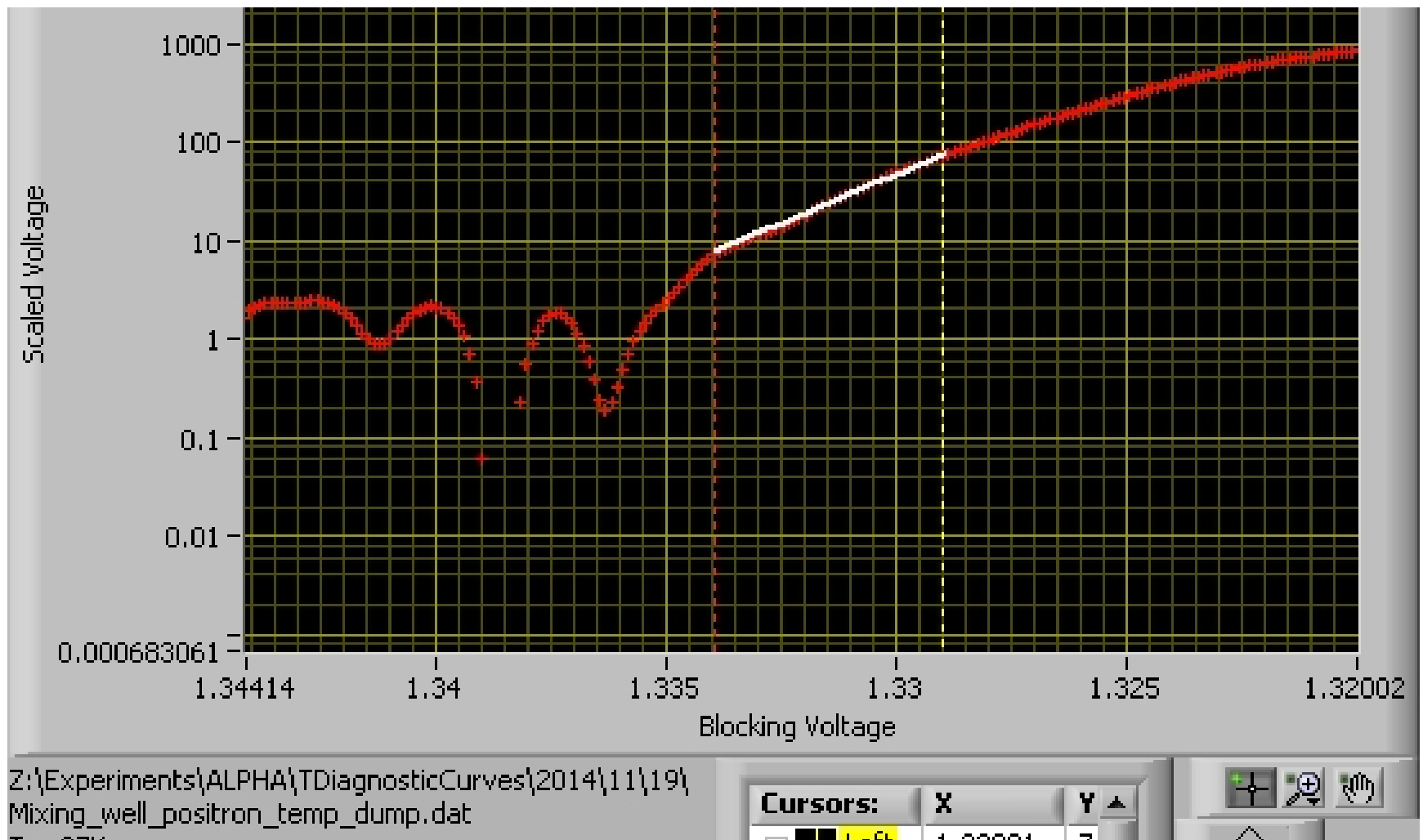
TDiag – Temperature Measurement



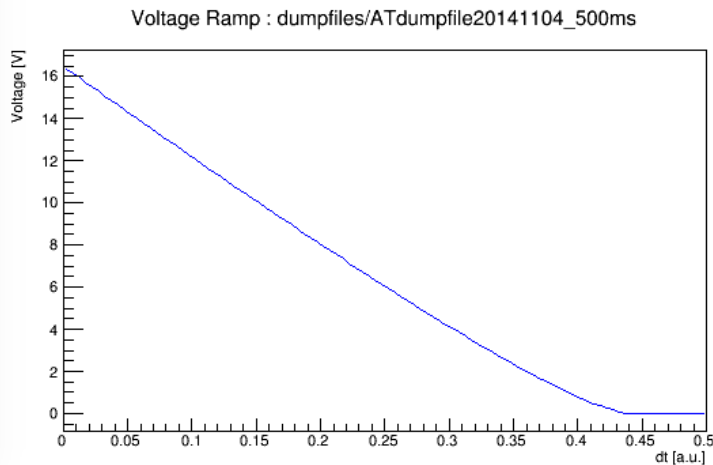
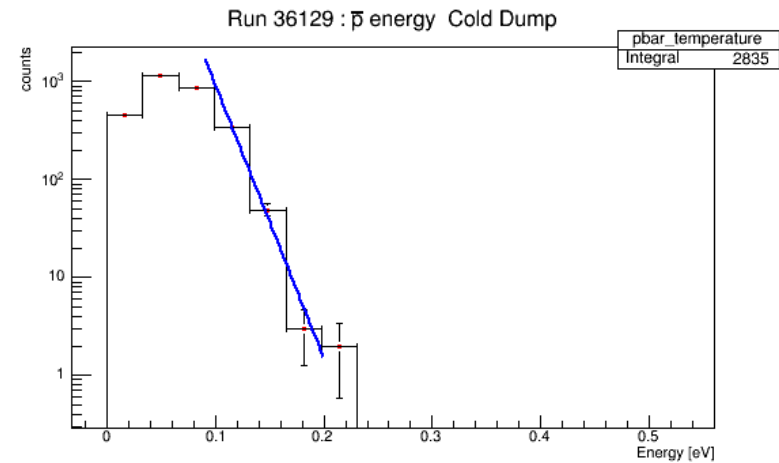
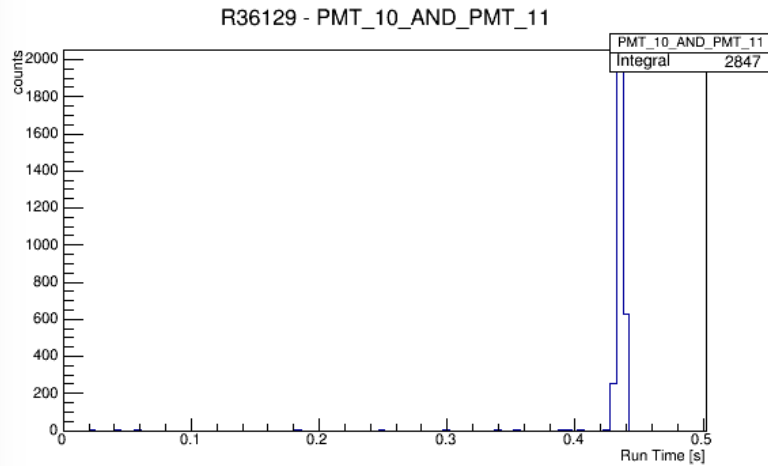
TDiag – Temperature Measurement



Half Load, RW, EVC 100%, TDiag



Full Xfer, Cool, RW, Ekick, TDump

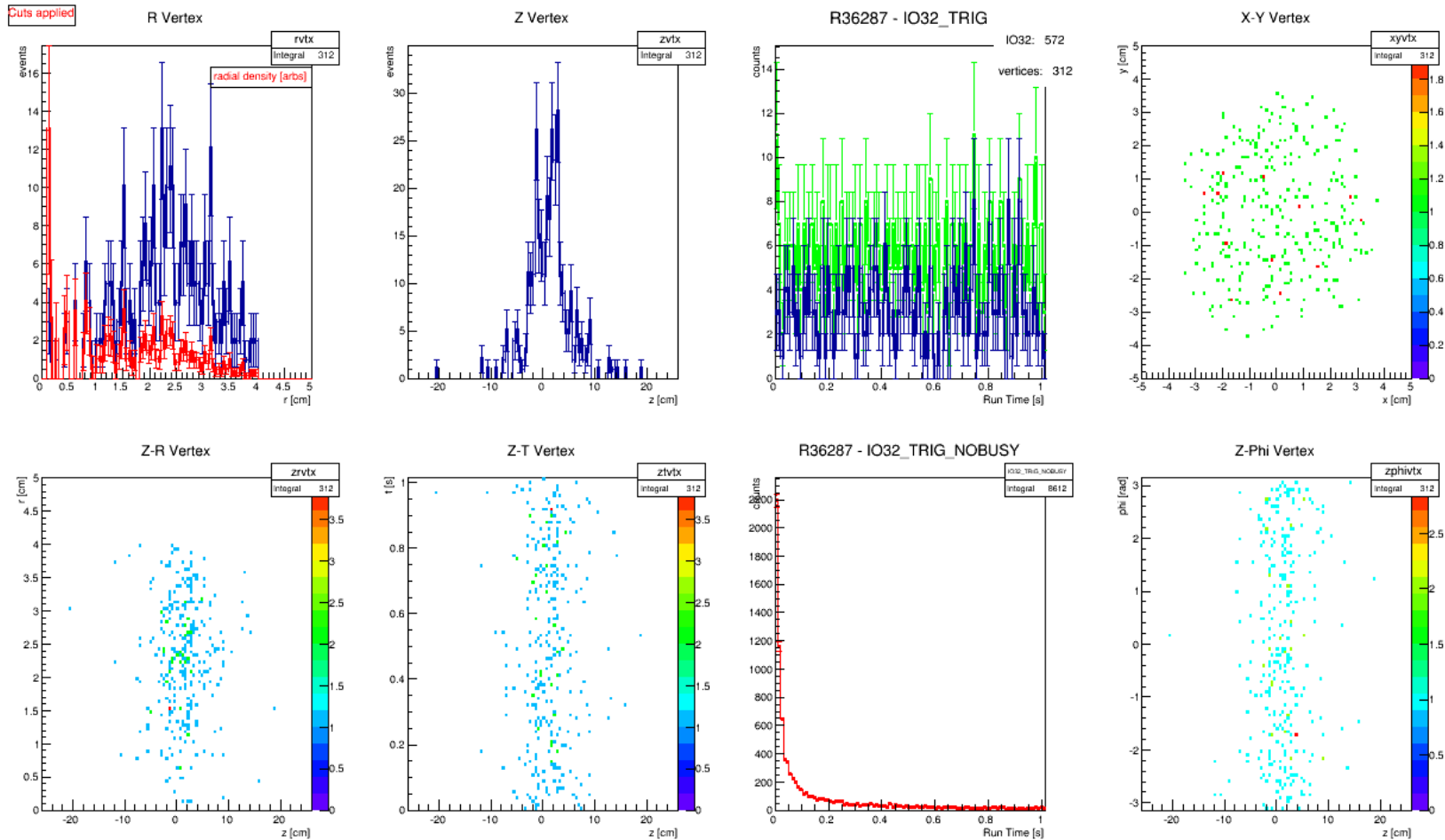


Energy Resolution 164.8 meV

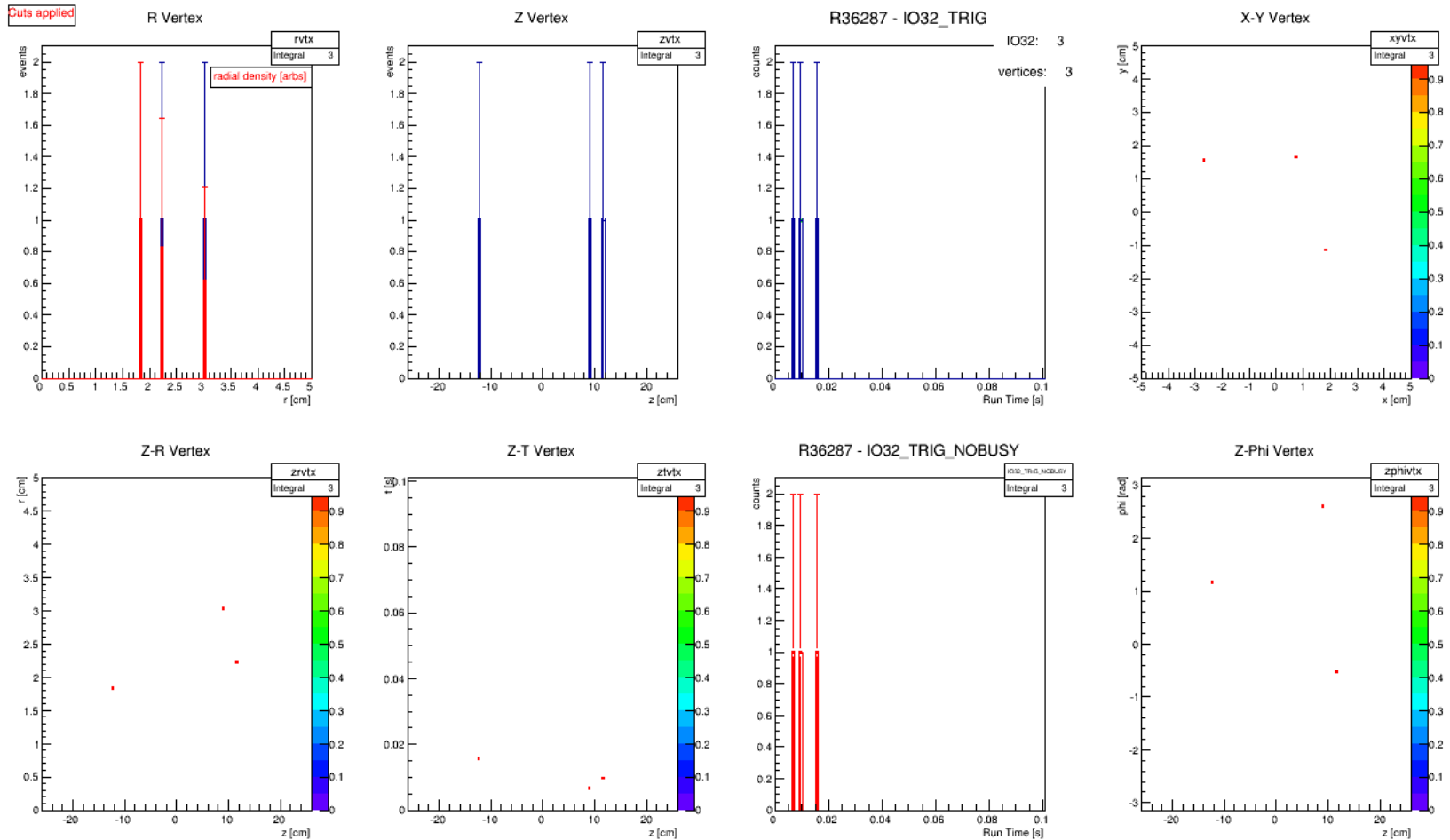
Integral 2830.0

\bar{p} temperature
(180.76 \pm 10.43) K

Analyzing Trapping Data



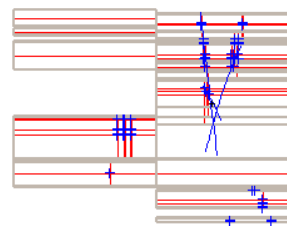
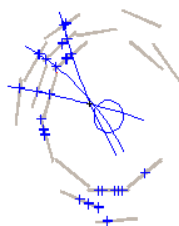
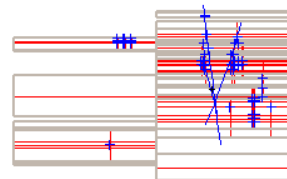
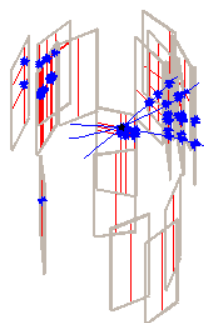
Analyzing Trapping Data



Passed-Cut

Top View
 Side View
 Front View
 All Views
 X3D
 1Si
 2Si
 3Si
 Supports
 Next
 XNext
 Hit Only
 M. Carlo
 Recons.
 All Sil
 Tracks
 — Included
 — Not near Trap
 — Shared Hits
 — Bad Chi2


Run 36287, Event 8251, Trigger 7997, VF48 Tim



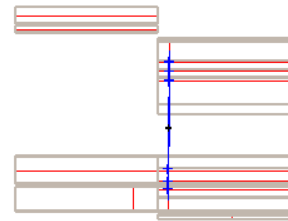
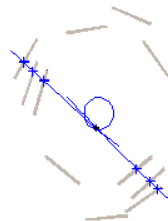
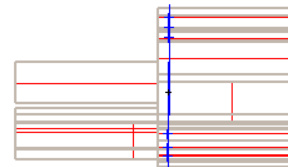
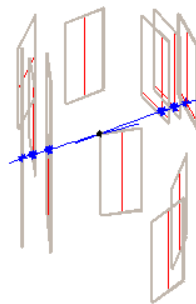
Cosmic Ray

Top View
 Side View
 Front View
 All Views
 X3D
 1Si
 2Si
 3Si
 Supports
 Next
 XNext
 Hit Only
 M. Carlo
 Recons.
 All Sil
 Tracks

— Included
 — Not near Trap
 — Shared Hits
 — Bad Chi2



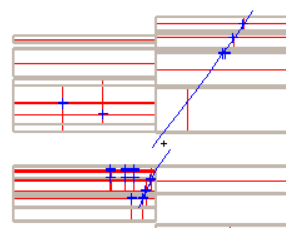
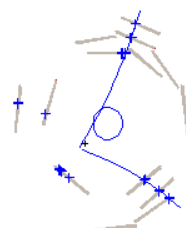
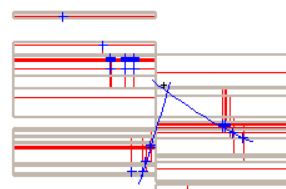
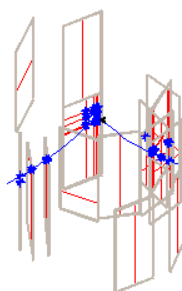
Run 36270, Event 8701, Trigger 8557, VF48 Tim



Non-Trivial

Top View
 Side View
 Front View
 All Views
 X3D
 1Si
 2Si
 3Si
 Supports
 Next
 XNext
 Hit Only
 M. Carlo
 Recons.
 All Sil
 Tracks
 — Included
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 — Shared Hits
 — Bad Chi2


Run 36270, Event 8700, Trigger 8556, VF48 Tim

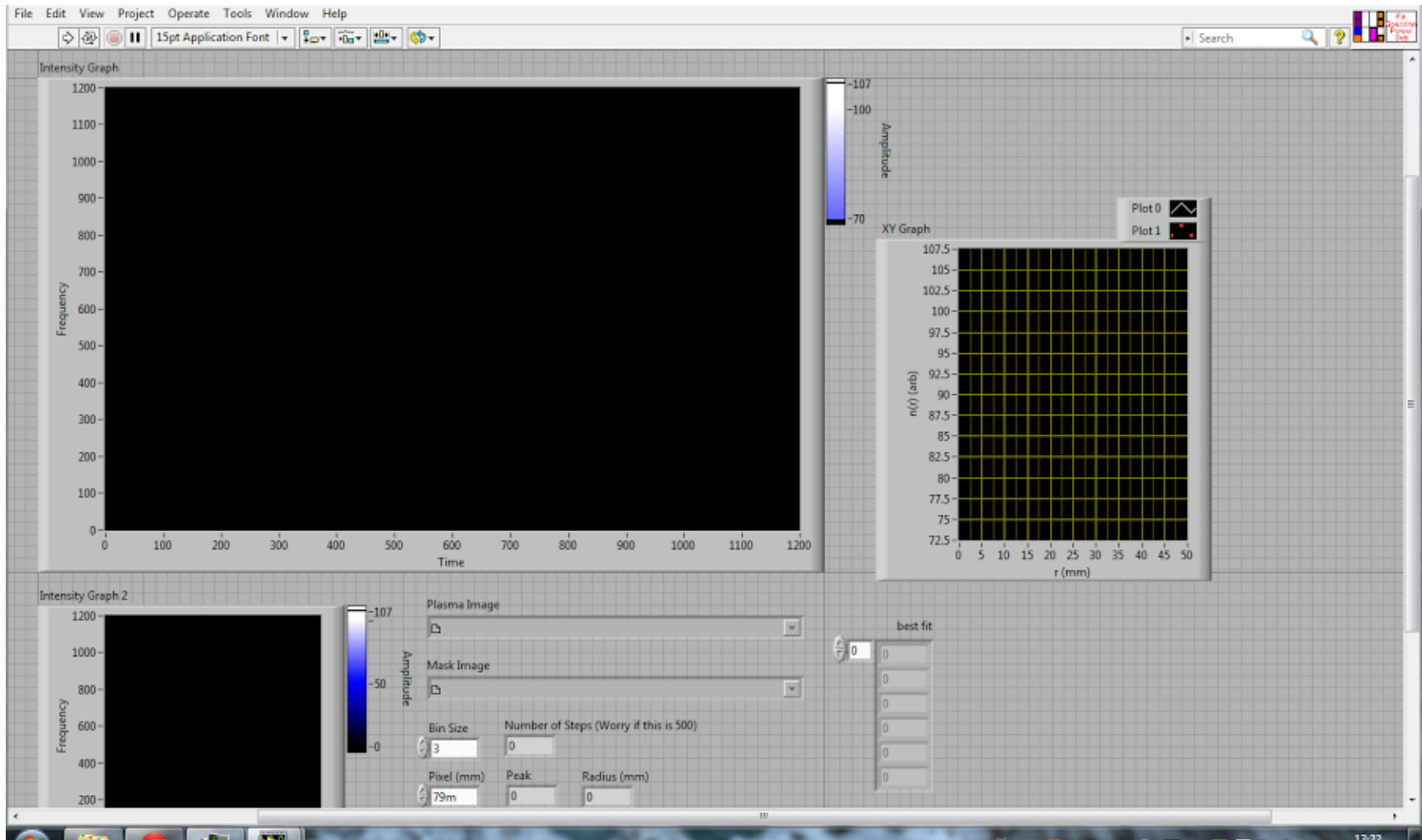


Working on: CUDA for Faster Analysis

- A parallel computing platform and programming model invented by NVIDIA.
- Enables dramatic increases in computing performance by harnessing the power of graphical processing unit.



Current GPU Analysis Interface



Thank you, Steven and Jean, for giving me this enriching experience, precious time in Europe and exposure to the best antihydrogen experiment in the world.

Thank you, my fellows, for teaching me useful conversational English and make me laugh all the time together.

