

## Activity I: TCAD simulation studies Activity II: Readout electronics emulator

### **Team for Activity I**



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ALICE ITS – upgrade and O2 Asian Workshop 2014 @ Pusan, South Korea



## **Explorer chip: Sector 1**

- Deletion region and electric field profile
- Depletion region and electric field profile for various Geometrical arrangement
- Comprehensive study of doping (N-layer, P-layer, Epi-layer)
- I-V and C-V results of an isolated diode pixel



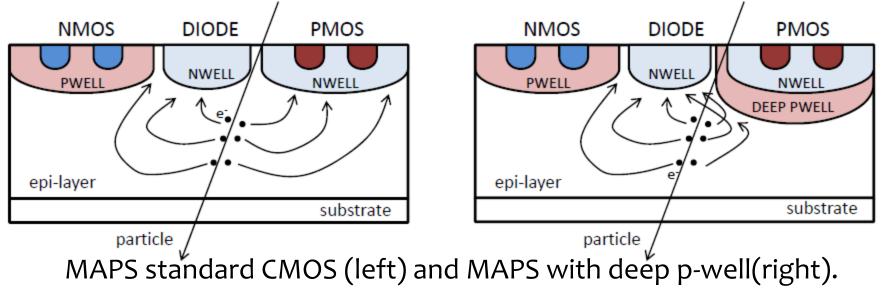
## **Previous work: Explorer chips**



To optimize the sensors and its configureation **Monolithic Active Pixel Sensors** using TowerJazz 0.18 μm CMOS Imaging Process



\* Special **deep p-well** prevents the n-well containing PMOS transistors from collecting signal charge from the epitaxial layer



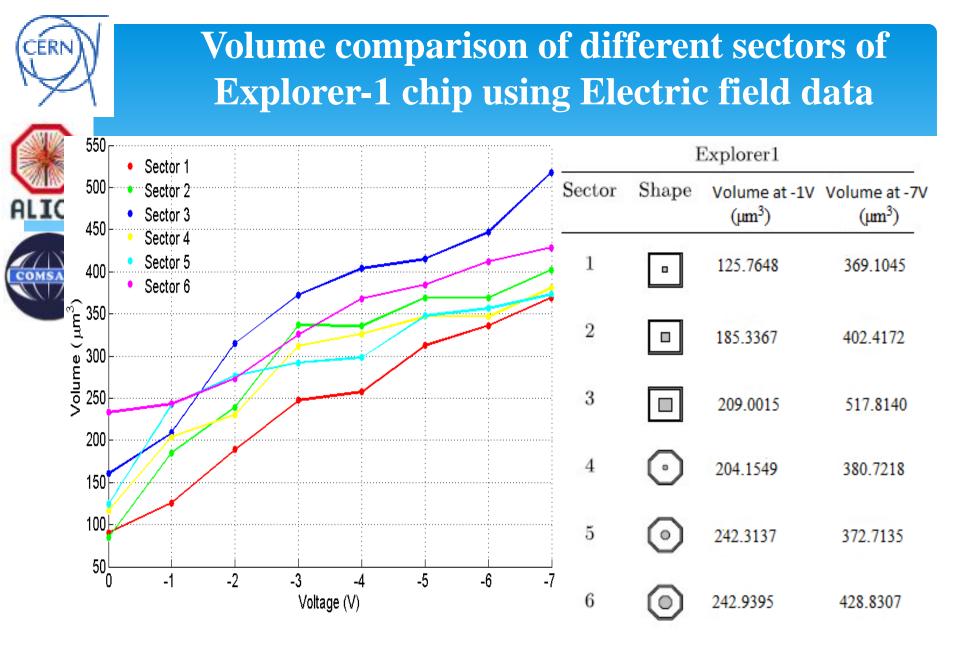


## **Diode geometries for Explorer chips**





|    |        | Ex      | plorer0          |   | Explorer1 |         |                  |                 |
|----|--------|---------|------------------|---|-----------|---------|------------------|-----------------|
|    | Sector | Shape   | Diameter<br>[µm] | $\begin{array}{c} {\rm Spacing} \\ {\rm [\mu m]} \end{array}$ | Sector    | Shape   | Diameter<br>[µm] | Spacing<br>[µm] |
|    | 1      | 0       | 2                | 0   | 1         | •       | 0.45             | 3.375           |
|    | 2      | 0       | 3                | 0   | 2         |         | 1                | 3.1             |
|    | 3      | 0       | 4                | 0   | 3         |         | 2                | 2.6             |
|    | 4      |         | 3                | 0   | 4         | $\odot$ | 0.53             | 3.335           |
|    | 5      | 0       | 3                | 0.6   | 5         | $\odot$ | 1                | 3.1             |
|    | 6      | Ø       | 3                | 1.04  | 6         | ٢       | 2                | 2.6             |
|    | 7      | 0       | 2                | 1.54  | 7         |         | 0.45             | 0.28(Top)       |
|    | 8      | 0       | 3                | 0   | 8         | $\odot$ | 0.53             | 0.28(Top)       |
| gr | 9      | $\odot$ | 3                | 1.04  | 9         | $\odot$ | 3                | 2.1             |









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- \* Grouping of two Sector 1s and their formation
- \* Electric Field Comparison of the structures
- \* 2x2 matrix of Sector 1
- \* Summary and Future work



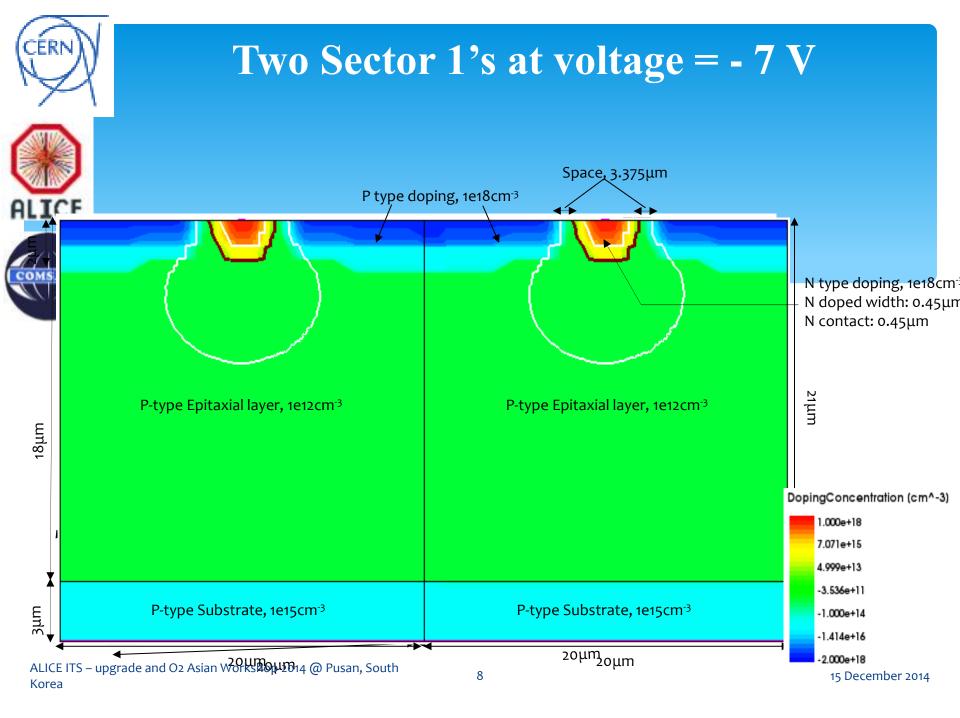
# Optimization of diode separation $(20 \ \mu m \text{ to } 8 \ \mu m)$

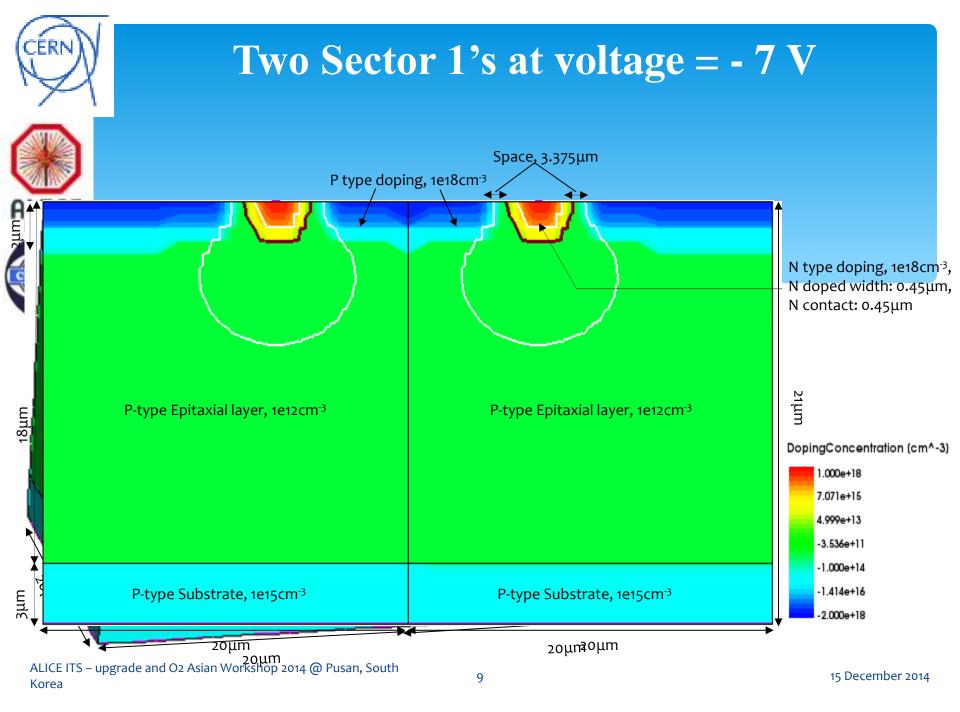


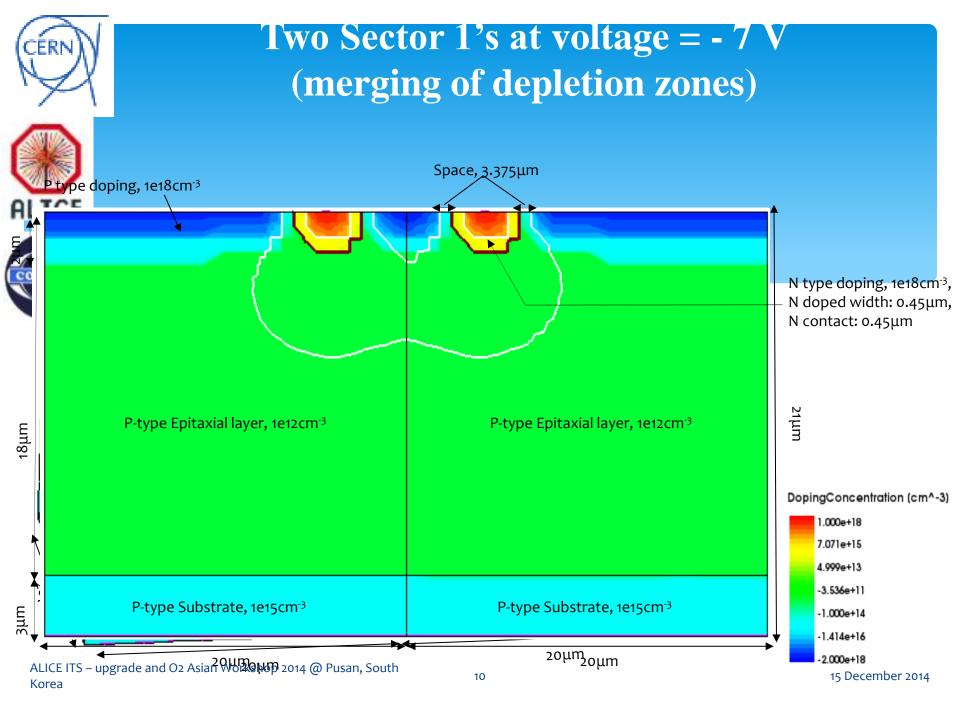
# Grouping of two Sector 1s and their formation Electric Field Comparison of the structures

2x2 matrix of Sector 1











## Outline

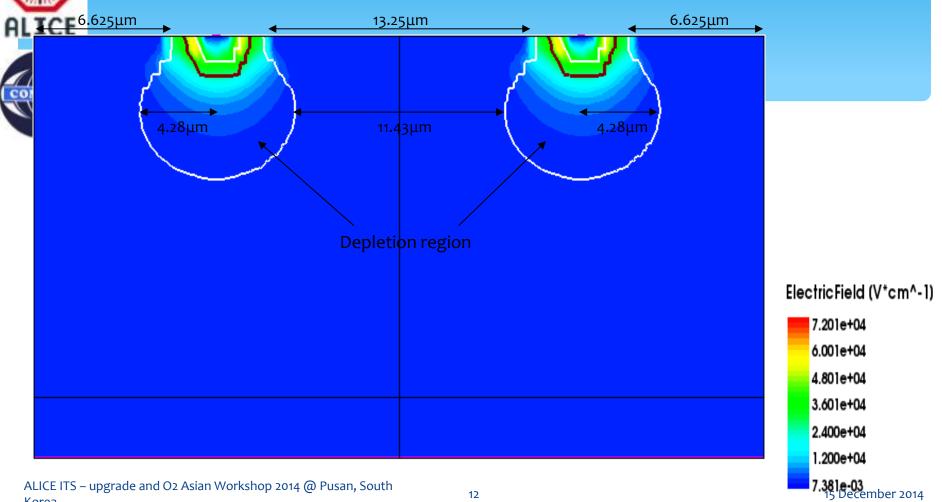


#### Grouping of two Sector 1s and their formation Electric Field Comparison of the structures

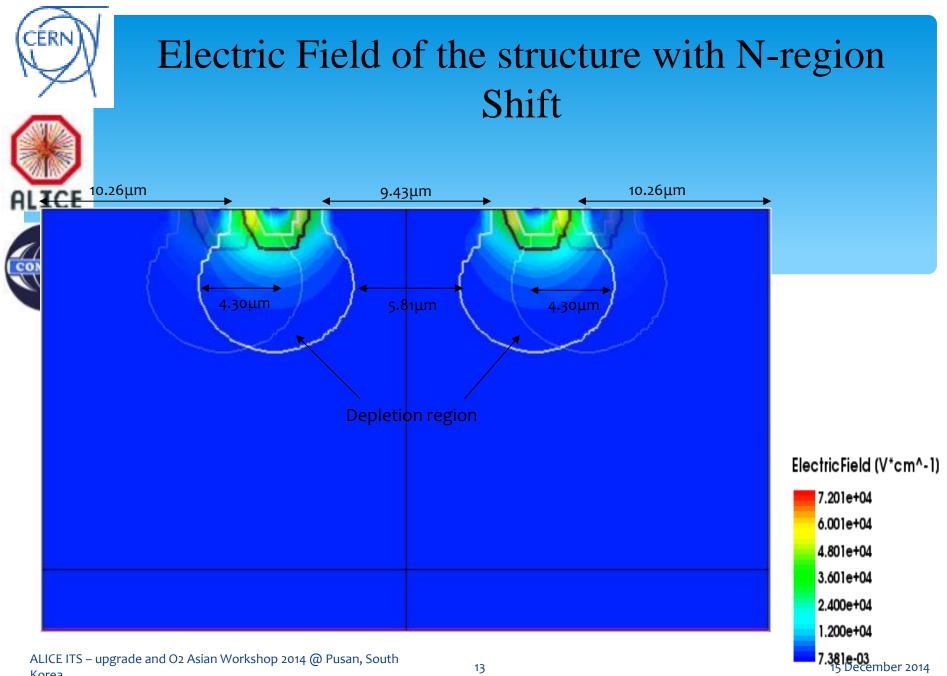
\* 2x2 matrix of



### **Electric Field profile of the structure with no N**region Shift

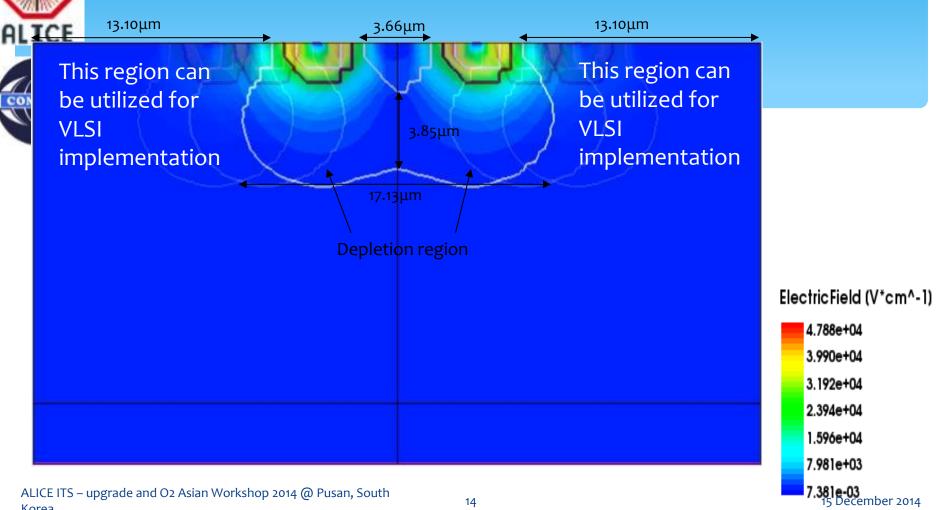


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## Electric Field of the structure with N-region Shift at center





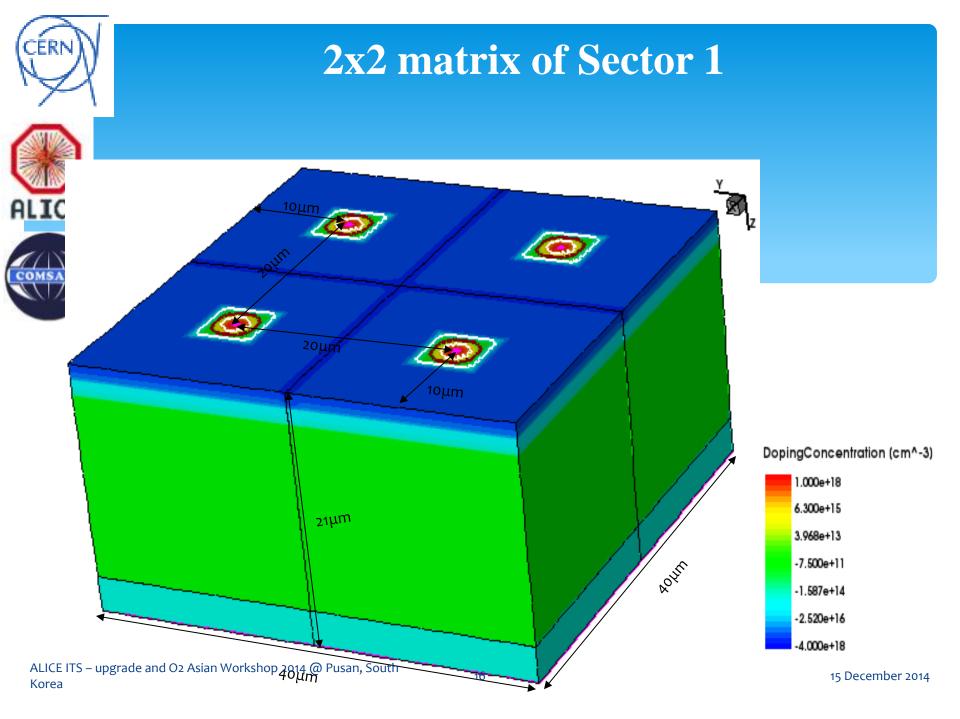
## Outline

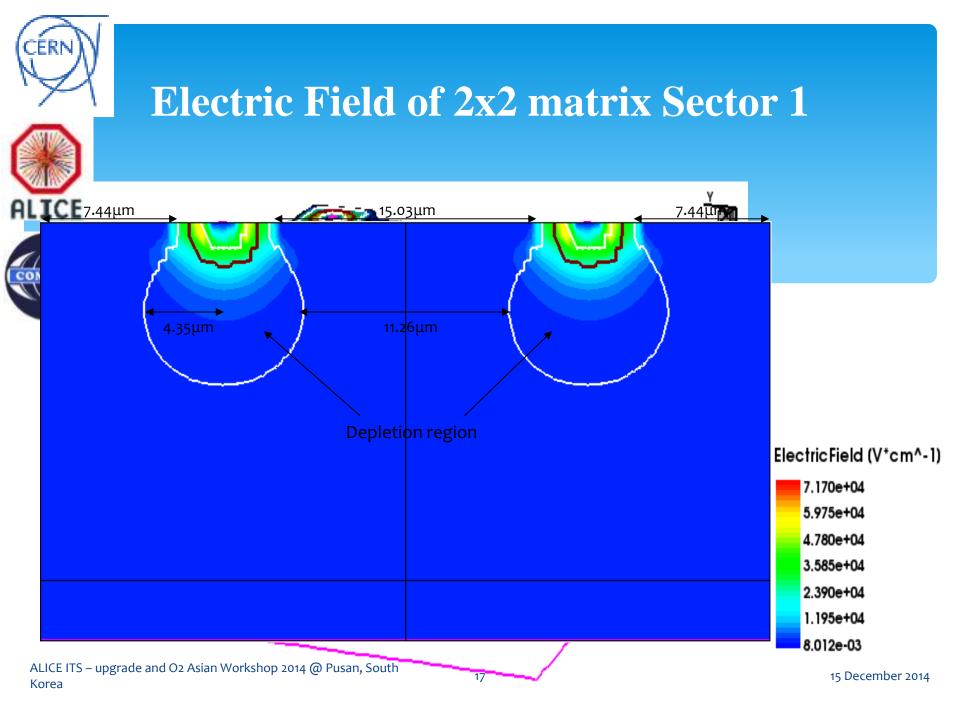


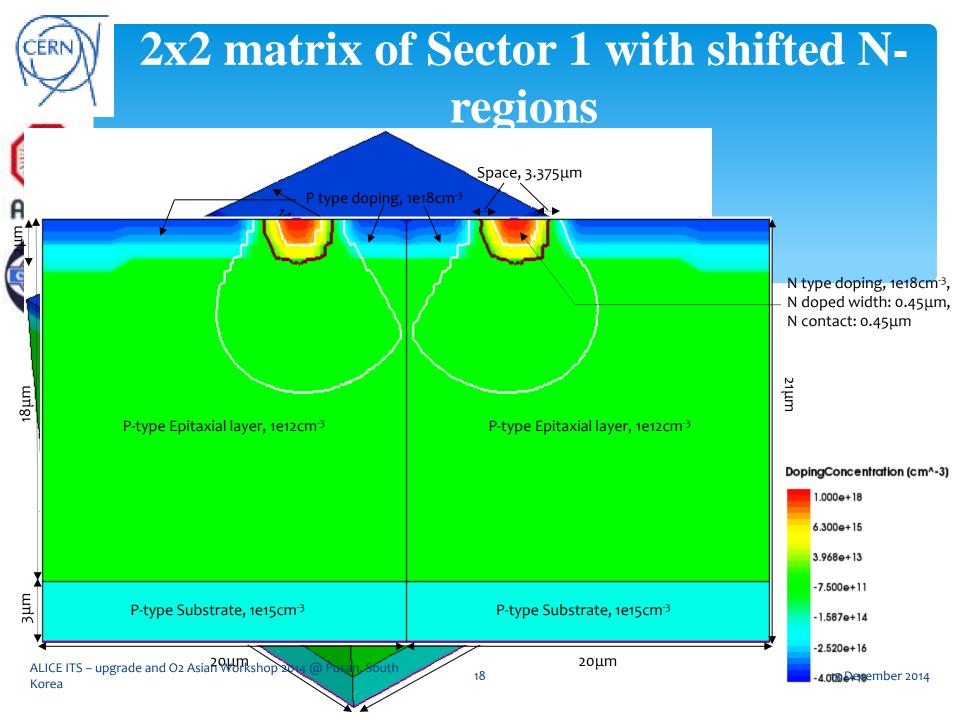
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2x2 matrix of Sector 1

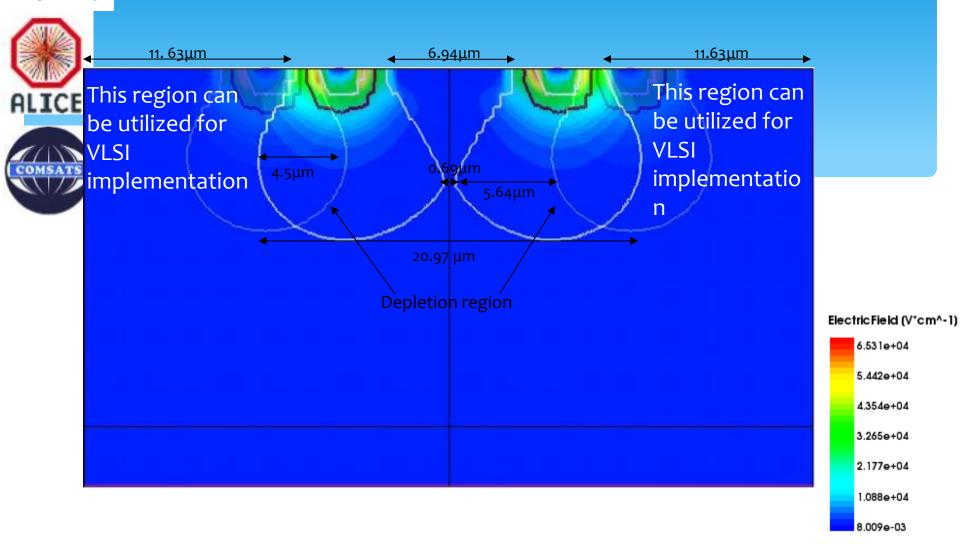
\* Summary and Future work



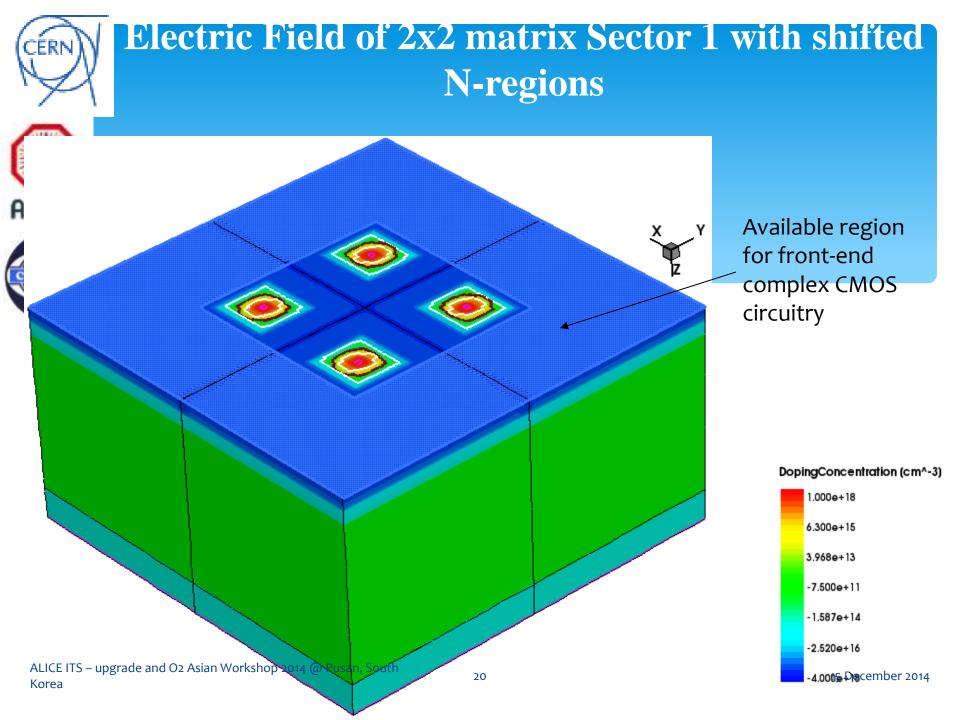


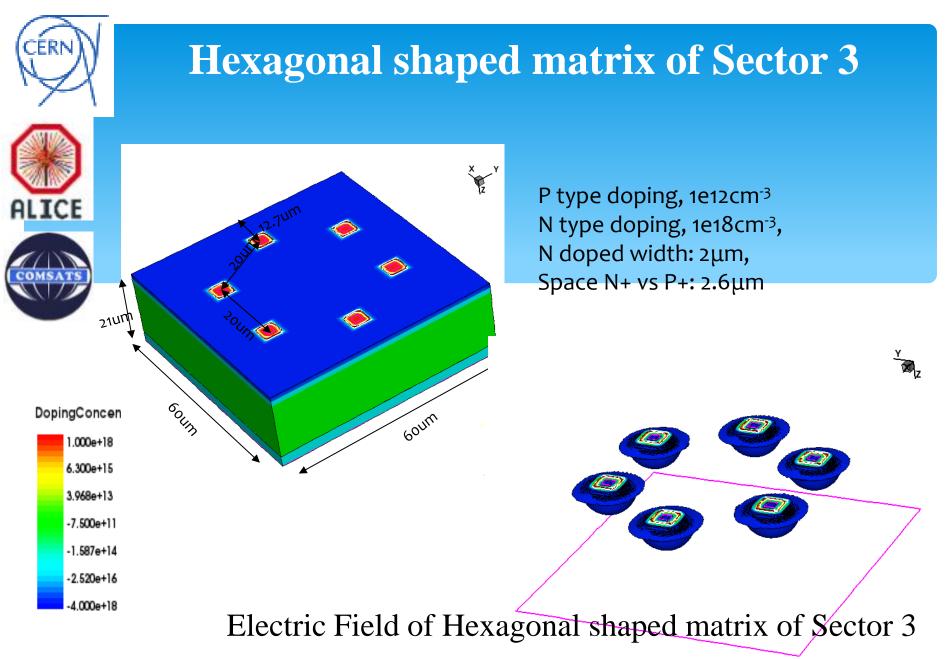


# Electric Field of 2x2 matrix Sector 1 with shifted N-regions



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## **Future work**



Space for VLSI implementation.

The optimized minimum distance between two anode is observed to be 16um in diamond and hexagonal shaped matrix. Next step: Grouping seven Sector 3 in body centered hexagonal shape.

## **Detailed studies of Doping densities**

- \* The effect of various doping profile on depletion region of the collecting diode.
- \* Reverse biased current characteristics by using different doping profile
- \* Capacitance voltage characteristics ALICE ITS – upgrade and O<sub>2</sub> Asian Workshop 2014 @ Pusan, South Korea