Session Program

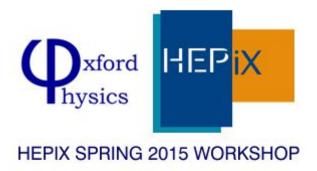**23–27 Mar 2015**

HEPiX Spring 2015 Workshop

Computing and Batch Systems

Physics Department, Oxford University, Martin Wood Lecture Theatre, Parks Road

Wednesday 25 March

14:00

Computing and Batch Systems
Session | **Location:** Physics Department, Oxford University, Martin Wood Lecture Theatre, Parks Road |
Conveners: Dr Ofer Rind, Wolfgang Friebel, Michele Michelotto

14:00–14:25 **Operation and benchmarking of a commercial datacentre**

Speaker
Peter Love

14:25–14:50 **Remote evaluation of hardware**

Speaker
Dr Tony Wong

14:50–15:15

Evaluation of Memory and CPU usage via Cgroups of ATLAS workloads running at a Tier-2

Speaker
Gang Qin

15:15–15:40

Beyond HS06: Toward a New HEP CPU Benchmark - Update March 2015

Speaker
Manfred Alef

15:40

16:05

Computing and Batch Systems: Computing and Batch Systems
Session | **Location:** Physics Department, Oxford University, Martin Wood Lecture Theatre, Parks Road |
Conveners: Wolfgang Friebel, Dr Ofer Rind, Michele Michelotto

16:05–16:30 **Looking for a fast benchmark**

Speaker
Dr Michele Michelotto

16:30–16:55 **Evaluation of low power Systems on Chip for scientific computing**

Speaker
Dr Lucia Morganti

16:55–17:20 **A look beyond x86: OpenPOWER8 & AArch64**

Speaker
Liviu Valsan

17:20–17:45 **HEPSPEC analysis across modern architectures**

Speaker
Mr David Power

17:45

Thursday 26 March

12:20

Computing and Batch Systems: Computing and Batch Systems
Session | **Location:** Physics Department, Oxford University, Martin Wood Lecture Theatre, Parks Road |
Conveners: Wolfgang Friebel, Michele Michelotto, Dr Ofer Rind

12:20–12:45

DRMAA2 - An Open Standard for Job Submission and Cluster Monitoring

Speaker

Daniel Gruber

12:45

Friday 27 March

09:00

Computing and Batch Systems: Computing and Batch Systems
Session | **Location:** Physics Department, Oxford University, Martin Wood Lecture Theatre, Parks Road |
Conveners: Wolfgang Friebel, Michele Michelotto, Dr Ofer Rind

09:00–09:25 **Batch Processing at CERN**

Speaker
Jerome Belleman

09:25–09:50 **Grid Engine at GridKa**

Speaker
Manfred Alef

09:50–10:15 **SLURM update from the Nordics**

Speaker
Erik Mattias Wadenstein

10:15–10:40 **Condor Workshop Summary**

Speaker
Mr Michel Jouvin

10:40

11:05

Computing and Batch Systems: Computing and Batch Systems
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11:05–11:30 **Two years of HTCondor at the RAL Tier-1**

Speaker
Andrew David Lahiff

11:30–11:55 **Future of Batch Processing at CERN**

Speaker
Jerome Belleman

11:55–12:20 **HTCondor within the European Grid and in the Cloud**

Speaker
Andrew David Lahiff

12:20–12:45 **DrainBoss: A Drain Rate Controller for ARC/HTCondor**

Speaker
Stephen Jones

12:45