



# HepMC Interface & nanoAODs

Alice Offline Week November 2014

# HepMC Interface

#### HepMC Interface



- Why?
  - A lot (all?) modern MC generator produce an output in HepMC format
    - EPOS (CRMC), JEWEL, Pythia, Herwig++, Sherpa
  - Implementing an interface for those generators automatically supports all of them!

#### Development

- Most of the development done by Brian Thorsbro (Summer Student 2014)
  - Slides: https://indico.cern.ch/event/291328/contribution/1
  - Report: http://cds.cern.ch/record/1751399?ln=en
- A lot of help from Alina, in preparing the grid package and debugging grid issues



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(One additional package if you run on the grid)

Includes

particles

Headers and

file HepMC ReaderHepMC ("real" or named pipe) Preferred: Root Named pipe (aka FIFO) (via RAM: very fast, simplest form of inter-Transport process communication, seen as a regular file from processes) Reconstruction Process #2 (aliroot)

#### In practice





-p3500 -P-3500 -n200 -m0 &

nc \ (number of events must be synced with sim.C)

HepMC reader as the generator

External

1 additional

package

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#### Small test production

pid	owner		first seen	last seen	subjobs				
	mfloris (383)	¢				DONE		ERRORS	
443124529		mfloris	17.11.2014 08:49	17.11.2014 16:18	100	58%	58	42%	42

20 K EPOS events requested (pp at  $\sqrt{s} = 7$  TeV, anc. to LHC10d) 40% fail (TTL: 200 evts/chunk)

Output in alien:///alice/cern.ch/user/m/mfloris/TestHepMC/TestPPLHC10d/output Quick analysis by Vytautas Vislavicious



#### Next:

- Port changes to release
- Run an official production
- Test other generators



### Clean up of the AOD/ESD interface



- Clean up of Events, Tracks, Vertices classes
  - Homogeneous ESD/AOD interfaces (as much as possible)
  - Push interface back to the **virtual classes** 
    - Benefit for nanoAOD vs AOD tasks
  - **Clarify** meaning if ambiguous members/getters (comments)
  - Remove duplications
- Required changes to [Virtual, ESD, AOD] [Events, Tracks, Vertices]
- Will have to be reconsidered when we switch to a flat AOD structure
- Changes implemented in the NanoAODdev branch (A. Festanti, R. Russo, R. Shahoyan, M. Floris), for details: JIRA <u>PWGPP-9</u>
- Merged on 27/10 in master

#### Changes to the analysis framework



- AODs now contain **2 types** of tracks/headers:
  - AliAODTrack, AliAODHeader
  - AliNanoAODTrack, AliNanoAODHeader
- Proposal (March 2014): Change AliAOD{Track, Header}\* to AliV{Track, Header}\* in AliAODEvent
  - Required patching some analysis tasks and framework classes
  - If a user can rely on the V track interface, no changes are needed in the task between processing AOD or nanoAOD format

Both derive from the

corresponding AliV classes

- If one needs specific methods, the tracks will have to be explicitly recasted.
- Problem with header found during implementation (next slide)

#### Implementation

- Tasks using AOD tracks / headers automatically patched to add a dynamic cast to AliAODtracks
- Changes merged into master on 27/10/2014

AliAODTrack \*aodtrack =dynamic\_cast<AliAODTrack\*>(event->GetTrack(i));
if(!aodtrack) AliFatal("Not processing a standard AOD");

#### AliVAODHeader



- Added AliVAODHeader, because AOD interface very different wrt ESD/Virtual
- Possibility to make interface more uniform will be considered in the future



#### Next Steps



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- Complete AliNanoAODHeader
  - Only most basic methods are implemented
- Develop PID interface
  - Basic requirement: maintain **compatibility with PID framework**
  - Idea:
    - AliDetectorPID a transient object used to cache calibrated PID information in AliAODtracks
    - Make it non-transient in AliNanoAODTracks
    - Only store the PID information which is actually requested in this object
- Clean up (coverity etc)
- Use it!
- **Development** has been **slow**: any **help** to speed it up would be appreciated
  - No show-stopper, just need time fore development/tests