

Tuesday 17 February

16:20

HV CMOS (2) and LGAD (1)

Session | Location: FBK, Trento, "Stringa" Conference Hall, Via Sommarive, 18 38123 Povo - Trento ITALY | Convener: Gregor Kramberger

16:20-16:40

Capacitively Coupled Hybrid Pixel Assemblies for the CLIC Vertex Detector

Speaker

Dr Daniel Hynds

16:40-17:00 Studies of non-irradiated and irradiated HVCMOS detectors

Speaker

Igor Mandic

17:00-17:20 Issues in the design of Ultrafast silicon detectors

Speaker

Mr Nicolo Cartiglia

17:20-17:40

Measurements of the Time Resolution of Low-Gain Avalanche Detectors (LGAD)

Hartmut Sadrozinski

17:40-18:00

Measures with strips LGAD p-type detectors fabricated at CNM-Barcelona

Speaker

18:00

Marta Baselga

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