

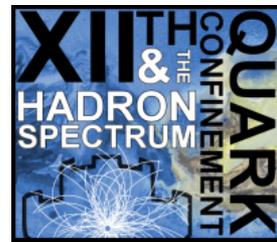
# Upgrade of the ALICE Inner Tracking System

Pasquale Di Nezza

on behalf of the ALICE Collaboration



ALICE



# ALICE upgrade strategy

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The Collaboration prepares a major upgrade for the LHC LS2 (2019-2020)

*Motivation: QGP precision studies*

High precision measurements of rare probes from low to high transverse momentum

*Requirements:*

- Excellent tracking efficiency and resolution at low  $p_T$
- Large statistics
- PID capability (even at high rate)

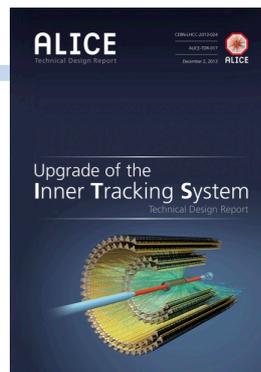
# ALICE upgrade strategy

- Target for upgrade programme (LHC Run3+Run4):
  - Pb-Pb luminosity  $> 10 \text{ nb}^{-1} \rightarrow 8 \times 10^{10}$  events
- Upgrade detectors, readout systems and online systems to:
  - readout all Pb-Pb interactions at the maximum rate of 50 kHz ( $L=6 \times 10^{27} \text{ cm}^{-2}\text{s}^{-1}$ ) with MB trigger. At present the rate is 500 Hz
- Gain a factor 100 in statistics over the originally approved programme Run1+Run2
- Significant improvement of vertexing and tracking capabilities at low  $p_T$  (150 MeV/c)

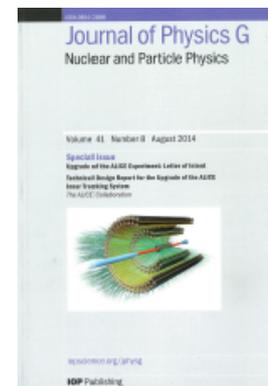
The new Inner Tracking System (ITS)

TDR endorsed by LHCC

The construction matches the LHC LS2

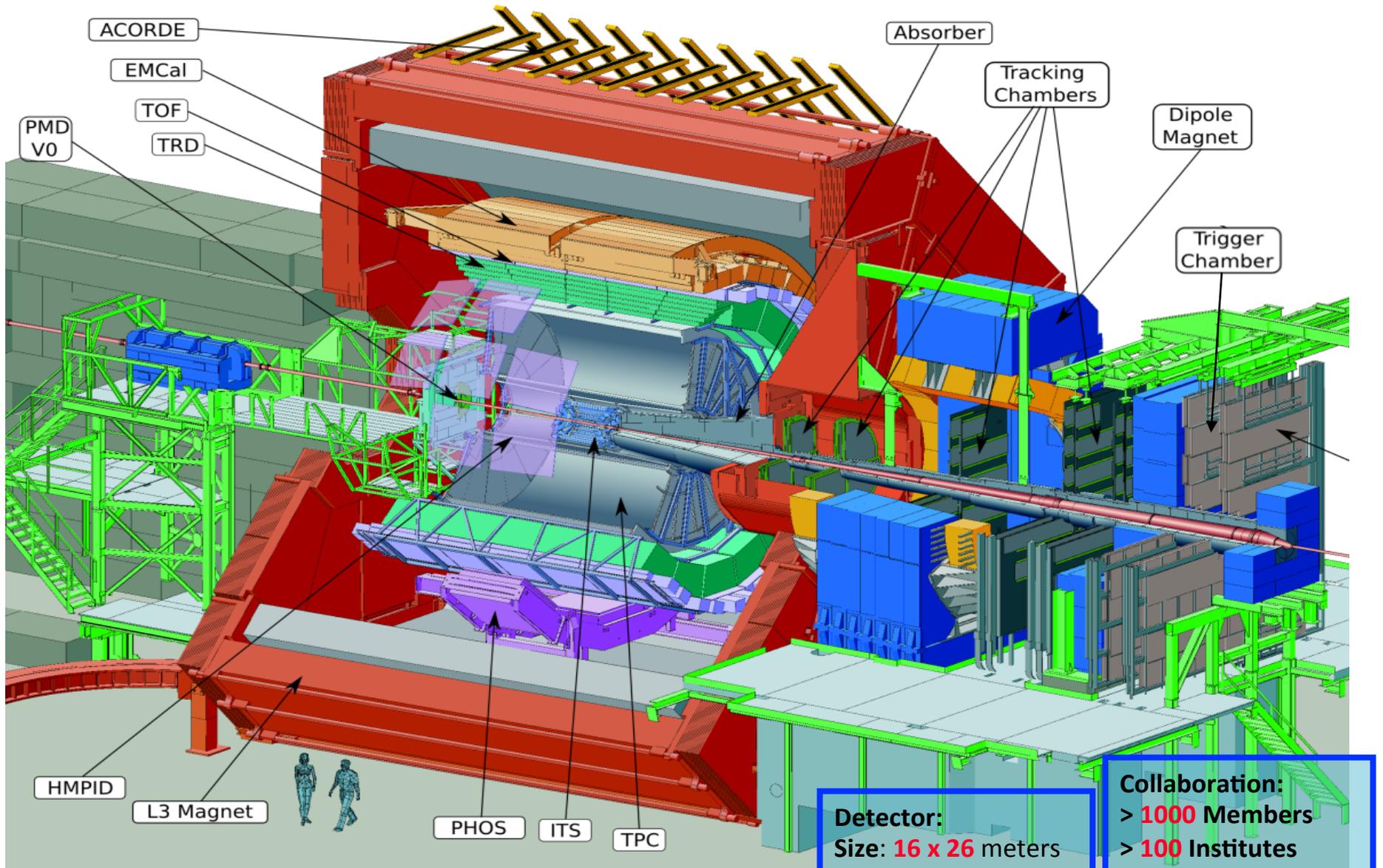


CERN-LHCC-2013-24



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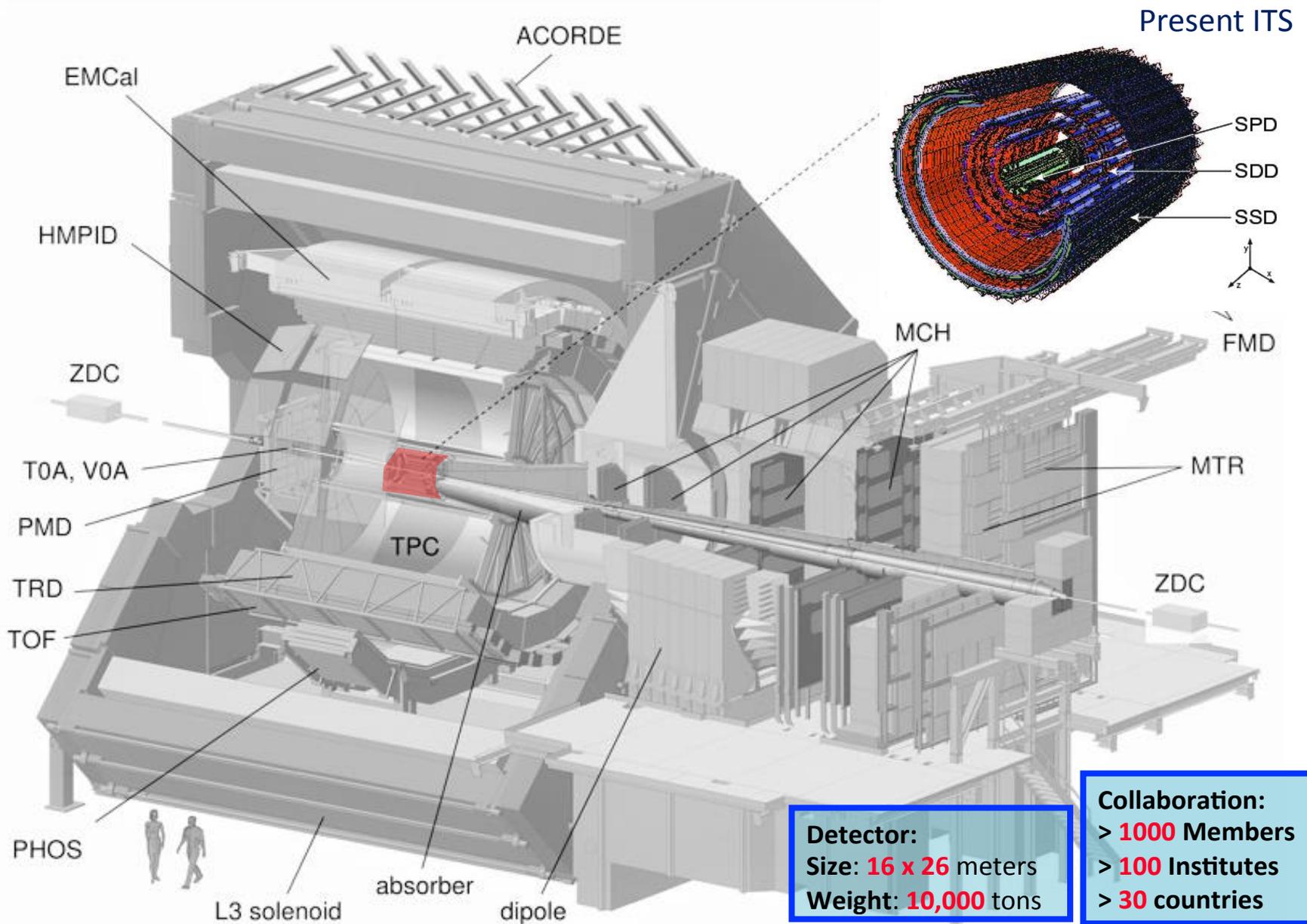
# ALICE: the current setup



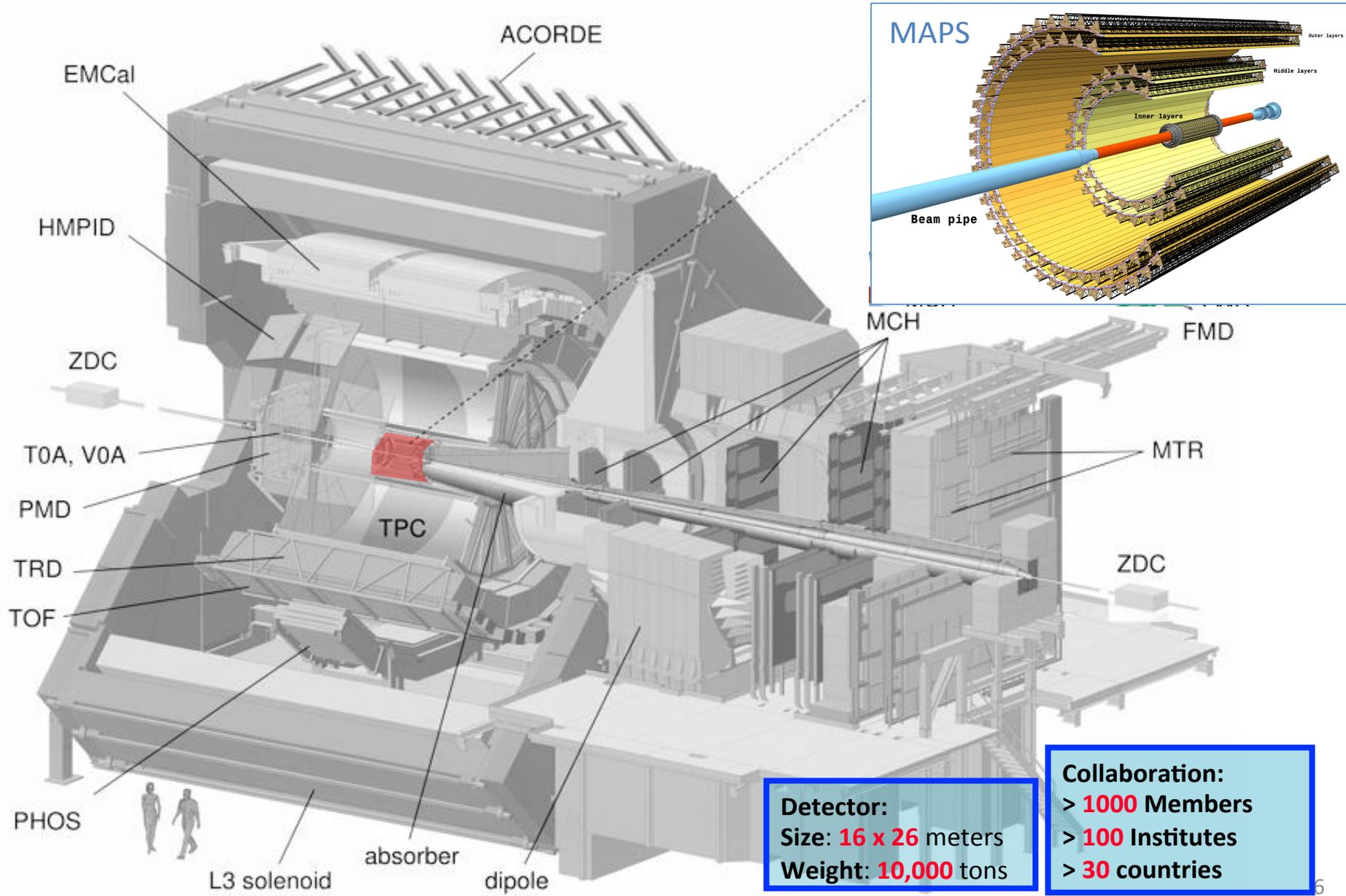
**Detector:**  
Size: 16 x 26 meters  
Weight: 10,000 tons

**Collaboration:**  
> 1000 Members  
> 100 Institutes  
> 30 countries

# ALICE: the current setup



# ALICE: the new ITS



**Detector:**  
Size: 16 x 26 meters  
Weight: 10,000 tons

**Collaboration:**  
> 1000 Members  
> 100 Institutes  
> 30 countries

# ITS upgrade design objectives

## 1. Improve impact parameter resolution by a factor of $\sim 3$

- Get closer to IP (position of first layer): 39mm  $\rightarrow$  23mm
- Reduce  $X/X_0$  /layer:  $\sim 1.14\%$   $\rightarrow$   $\sim 0.3\%$  (for inner layers)
- Reduce pixel size: currently  $50\mu\text{m} \times 425\mu\text{m}$   $\rightarrow$   $O(30\mu\text{m} \times 30\mu\text{m})$

## 2. Improve tracking efficiency and $p_T$ resolution at low $p_T$

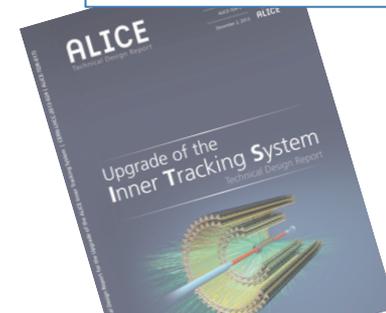
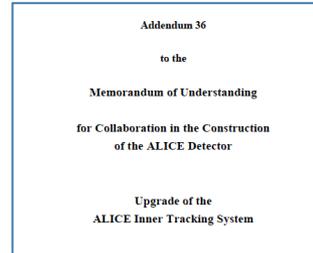
- Increase granularity:
  - 6 layers  $\rightarrow$  7 layers
  - silicon drift and strips  $\rightarrow$  pixels

## 3. Fast readout

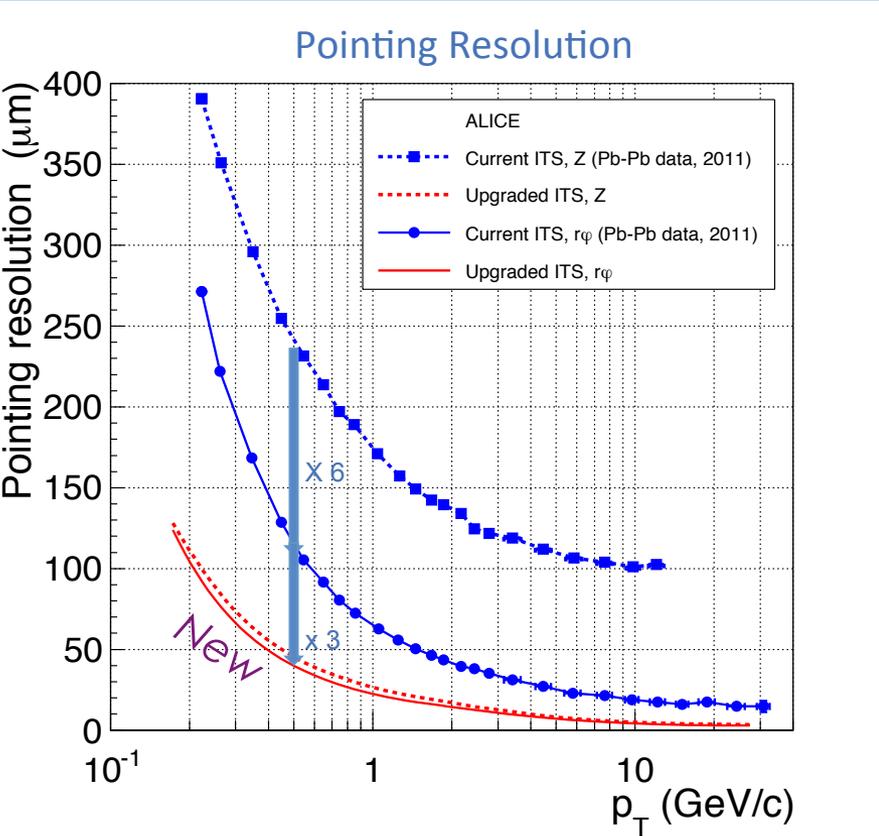
- readout Pb-Pb interactions at  $> 100$  kHz and pp interactions at  $\sim$  several  $10^5$  Hz (currently limited at 1kHz with full ITS)

## 4. Fast insertion/removal for yearly maintenance

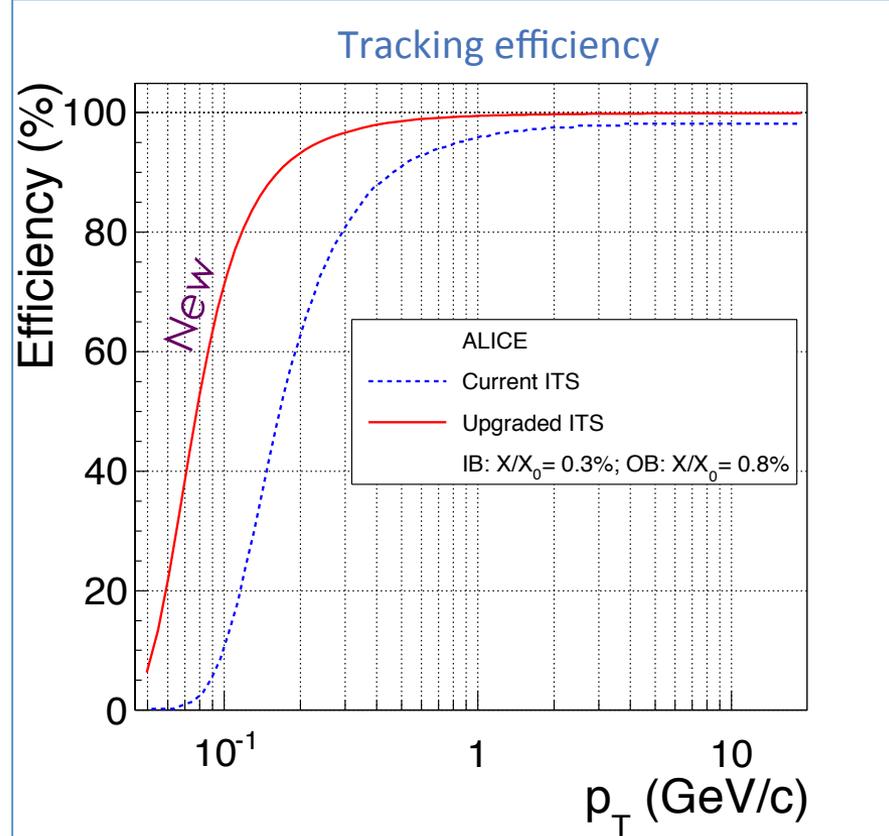
- possibility to replace non functioning detector modules during yearly shutdown



# ITS – Performance of new detector



$\sim 40 \mu\text{m}$  at  $p_T = 500 \text{ MeV}$



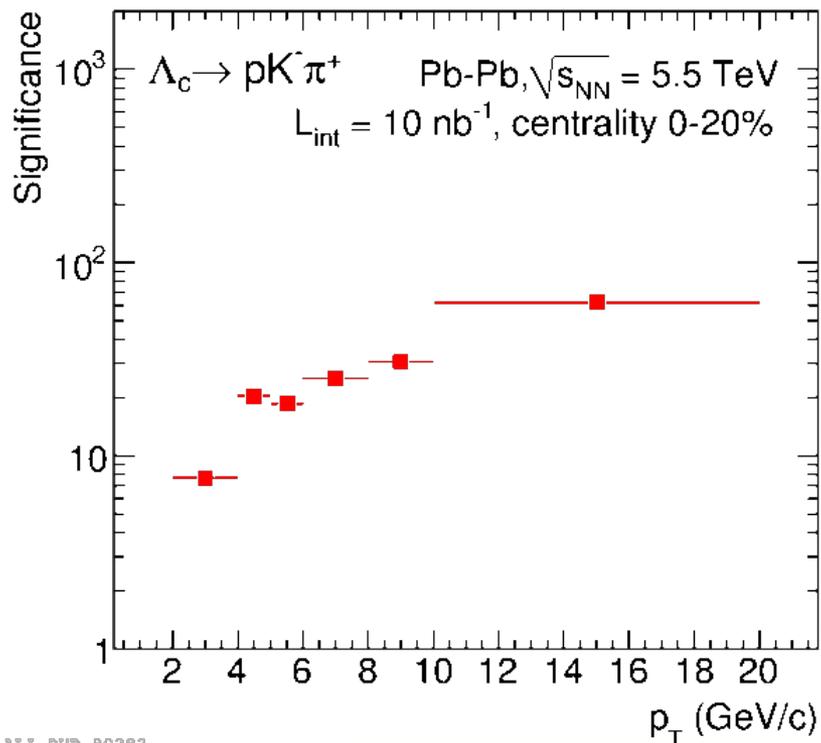
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Fundamental improvement of the impact parameter resolution (left) and tracking efficiency (right) from the old to the new ITS

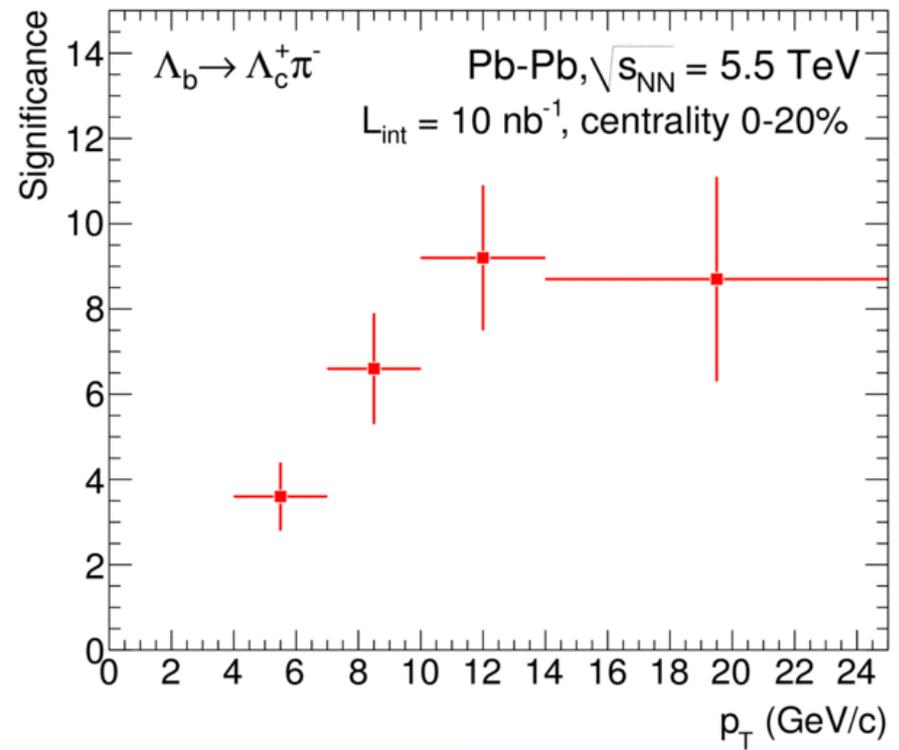
# ITS – Physics Performance

- $\Lambda_c$  ( $c\tau=60 \mu\text{m}$ ) currently inaccessible in Pb-Pb, most promising channel  $\Lambda_c \rightarrow pK^-\pi^+$ ;
- Ability to reconstruct  $\Lambda_c \rightarrow$  also  $\Lambda_b$  production can be measured from  $p_T$  of 5-7 GeV/c.
- Other decay channels and other baryon species (e.g.  $\chi_c$ ) will be investigated.

**charm**



**beauty**

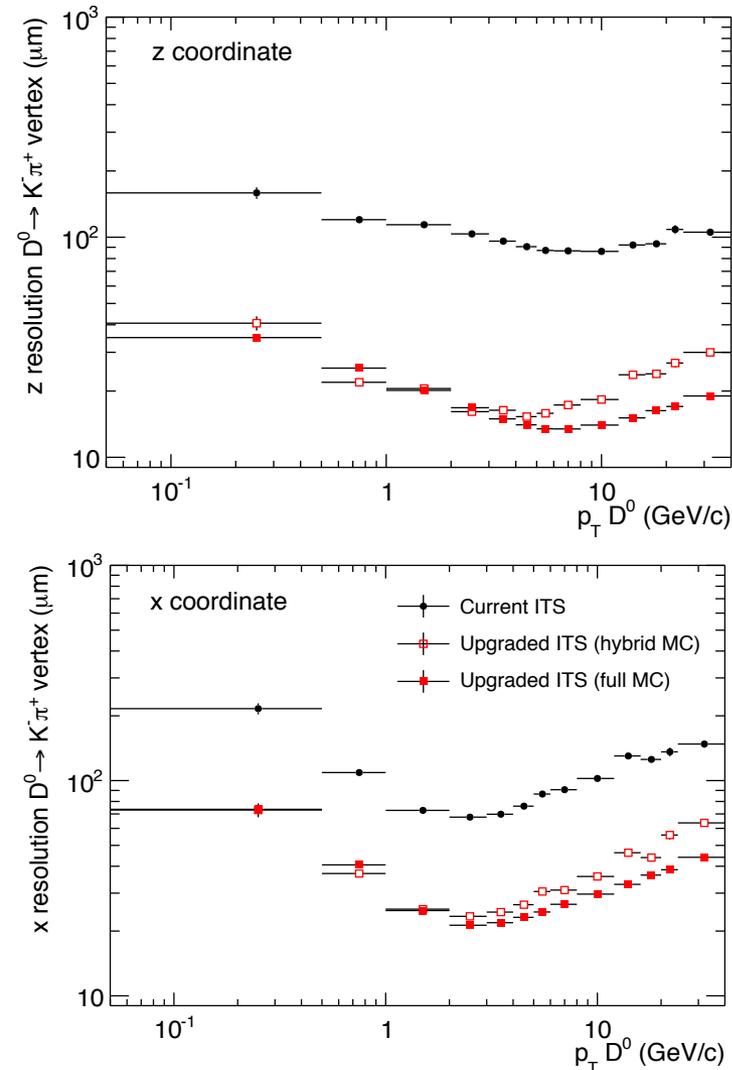


ALICE-PUB-80282

ALICE ITS upgrade TD, CERN-LHCC-2013-024 ; ALICE-TDR-017

First measurements of  $\Lambda_c$  in Pb-Pb collisions at LHC

## Example: $D^0 \rightarrow K\pi^+$

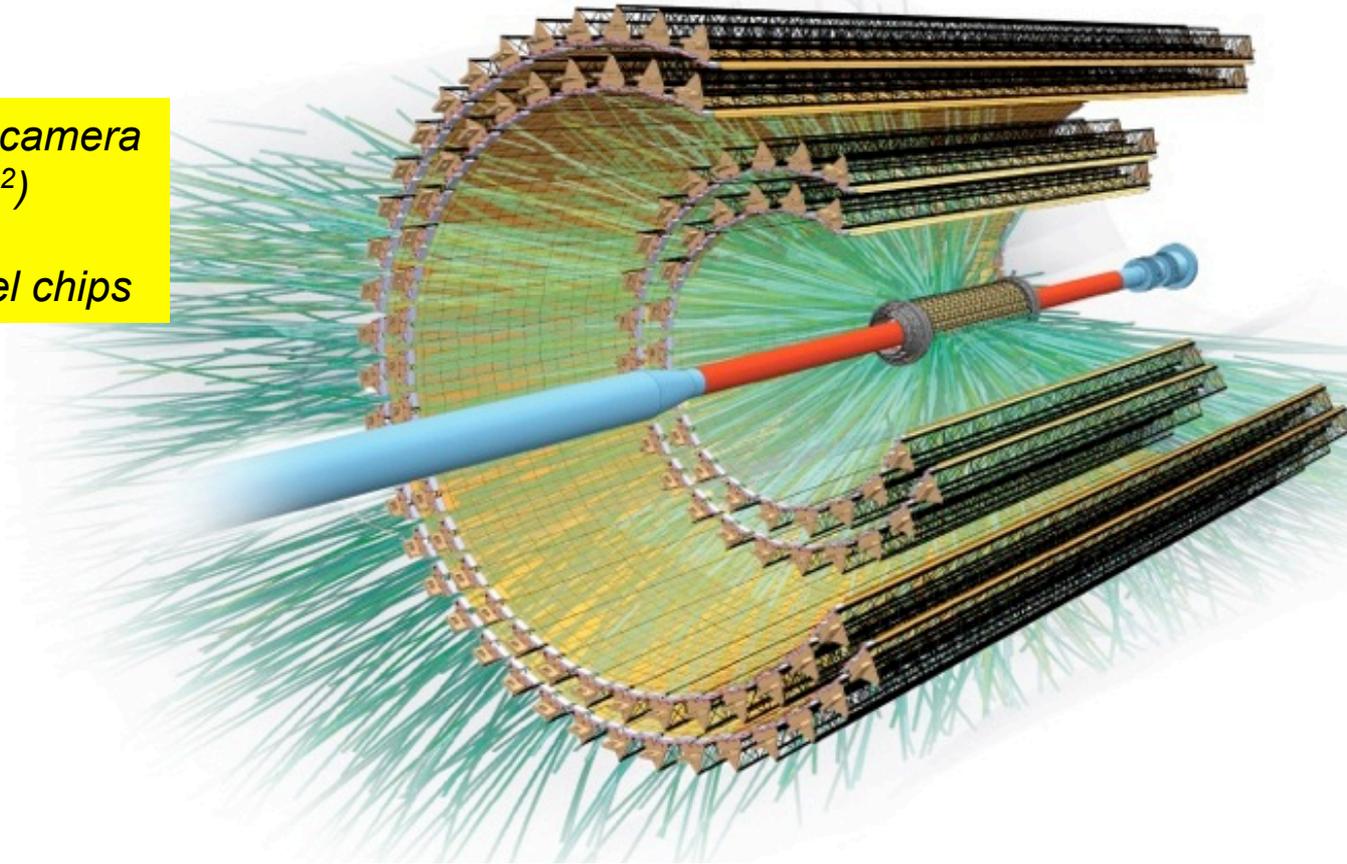


Observable	Current, $0.1 \text{ nb}^{-1}$		Upgrade, $10 \text{ nb}^{-1}$	
	$p_T^{\text{min}}$ (GeV/c)	statistical uncertainty	$p_T^{\text{min}}$ (GeV/c)	statistical uncertainty
Heavy Flavour				
D meson $R_{AA}$	1	10 %	0	0.3 %
$D_s$ meson $R_{AA}$	4	15 %	< 2	3 %
D meson from B $R_{AA}$	3	30 %	2	1 %
$J/\psi$ from B $R_{AA}$	1.5	15 % ( $p_T$ -int.)	1	5 %
$B^+$ yield	not accessible		3	10 %
$\Lambda_c$ $R_{AA}$	not accessible		2	15 %
$\Lambda_c/D^0$ ratio	not accessible		2	15 %
$\Lambda_b$ yield	not accessible		7	20 %
D meson $v_2$ ( $v_2 = 0.2$ )	1	10 %	0	0.2 %
$D_s$ meson $v_2$ ( $v_2 = 0.2$ )	not accessible		< 2	8 %
D from B $v_2$ ( $v_2 = 0.05$ )	not accessible		2	8 %
$J/\psi$ from B $v_2$ ( $v_2 = 0.05$ )	not accessible		1	60 %
$\Lambda_c$ $v_2$ ( $v_2 = 0.15$ )	not accessible		3	20 %
Dielectrons				
Temperature (intermediate mass)	not accessible			10 %
Elliptic flow ( $v_2 = 0.1$ )	not accessible			10 %
Low-mass spectral function	not accessible		0.3	20 %
Hypernuclei				
$^3\Lambda\text{H}$ yield	2	18 %	2	1.7 %

# ITS new layout

12.5 G-pixel camera  
( $\sim 10 \text{ m}^2$ )

$\sim 24.000$  pixel chips



## 7-layer barrel geometry based on MAPS

r coverage: 23 – 400 mm

$\eta$  coverage:  $|\eta| \leq 1.22$

for tracks from 90% most luminous region

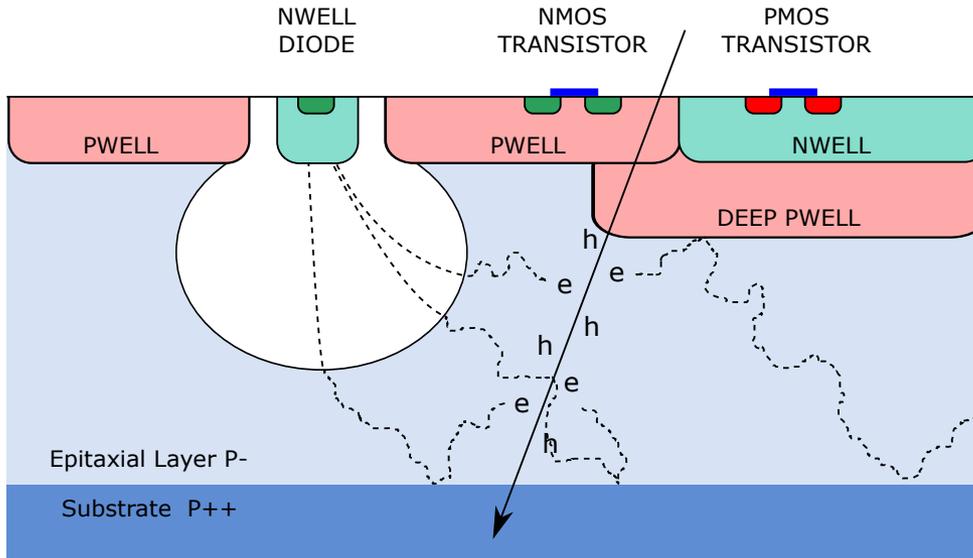
**3** Inner Barrel layers (**IB**)

**4** Outer Barrel layers (**OB**)

Material /layer :  $0.3\% X_0$  (IB),  $1\% X_0$  (OB)

# ITS Pixel Chip – Architecture Choice

## CMOS Pixel Sensor using TowerJazz 0.18 $\mu\text{m}$ CMOS Imaging Process

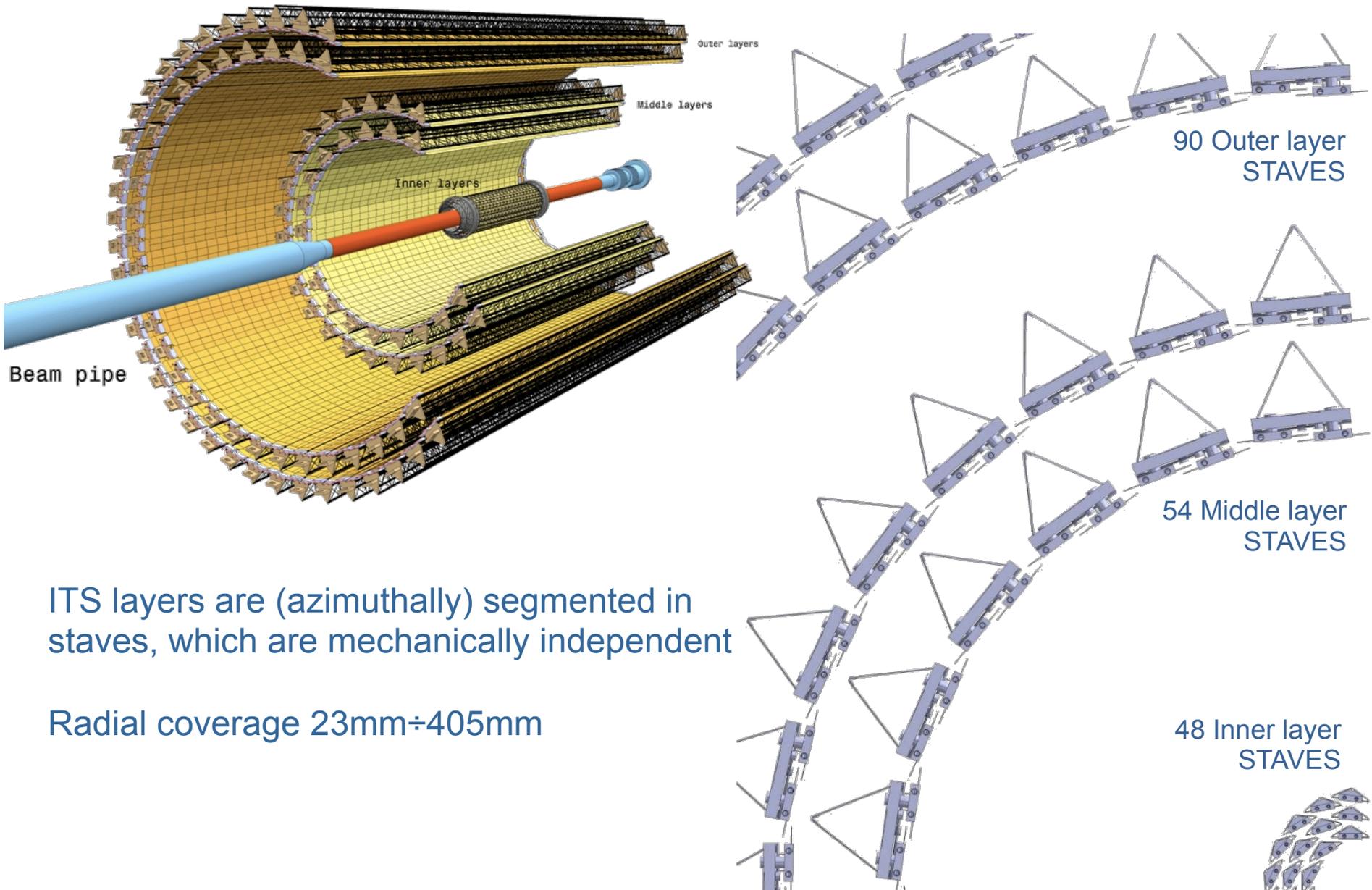


### Tower Jazz 0.18 $\mu\text{m}$ CMOS

- feature size 180 nm
- metal layers 6
- ➔ Suited for high-density, low-power
- Gate oxide 3nm
- ➔ Circuit rad-tolerant

- ▶ High-resistivity ( $> 1\text{k}\Omega\text{ cm}$ ) p-type epitaxial layer (18 $\mu\text{m}$  to 30 $\mu\text{m}$ ) on p-type substrate
- ▶ Small n-well diode (2  $\mu\text{m}$  diameter),  $\sim 100$  times smaller than pixel  $\Rightarrow$  low capacitance
- ▶ Application of (moderate) reverse bias voltage to substrate (contact from the top) can be used to increase depletion zone around NWELL collection diode
- ▶ Deep PWELL shields NWELL of PMOS transistors to allow for full CMOS circuitry within active area

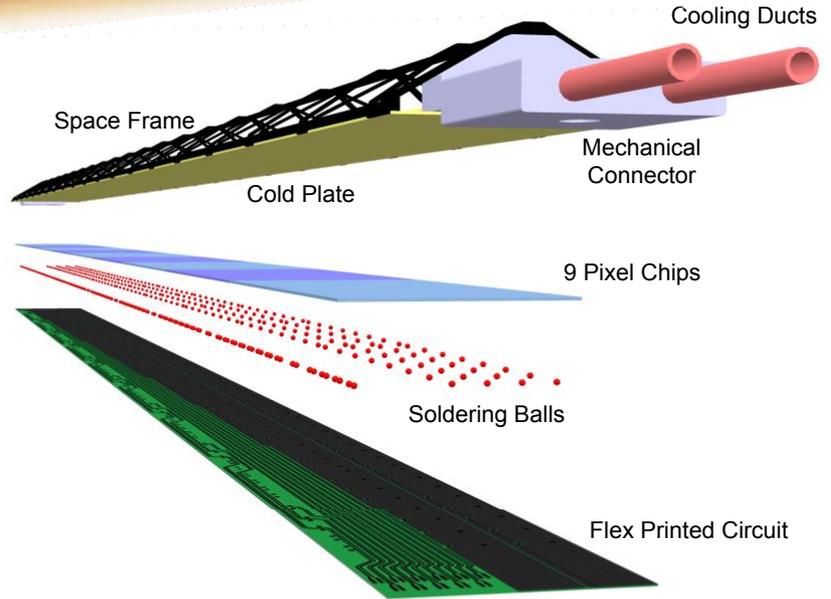
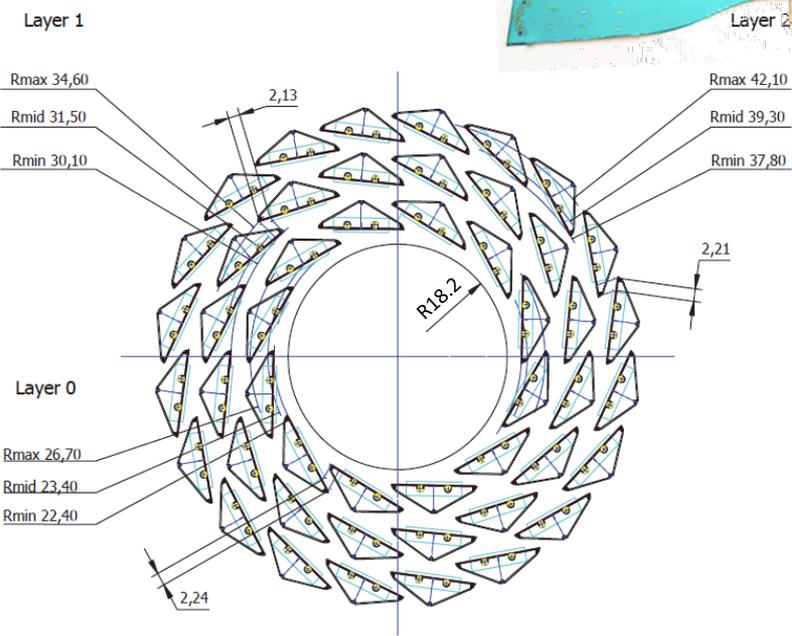
# ITS new layout



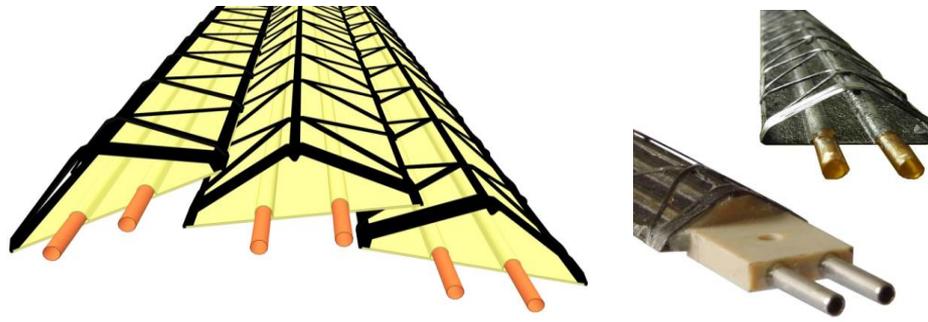
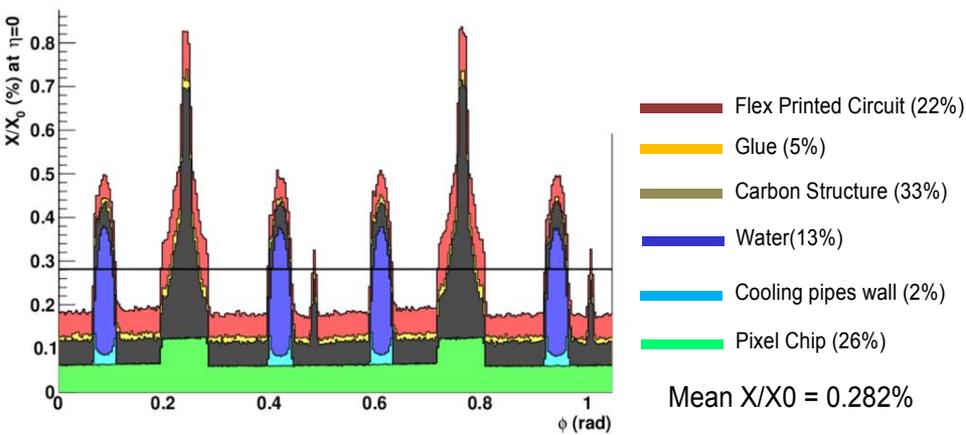
ITS layers are (azimuthally) segmented in staves, which are mechanically independent

Radial coverage  $23\text{mm} \div 405\text{mm}$

# Inner Barrel

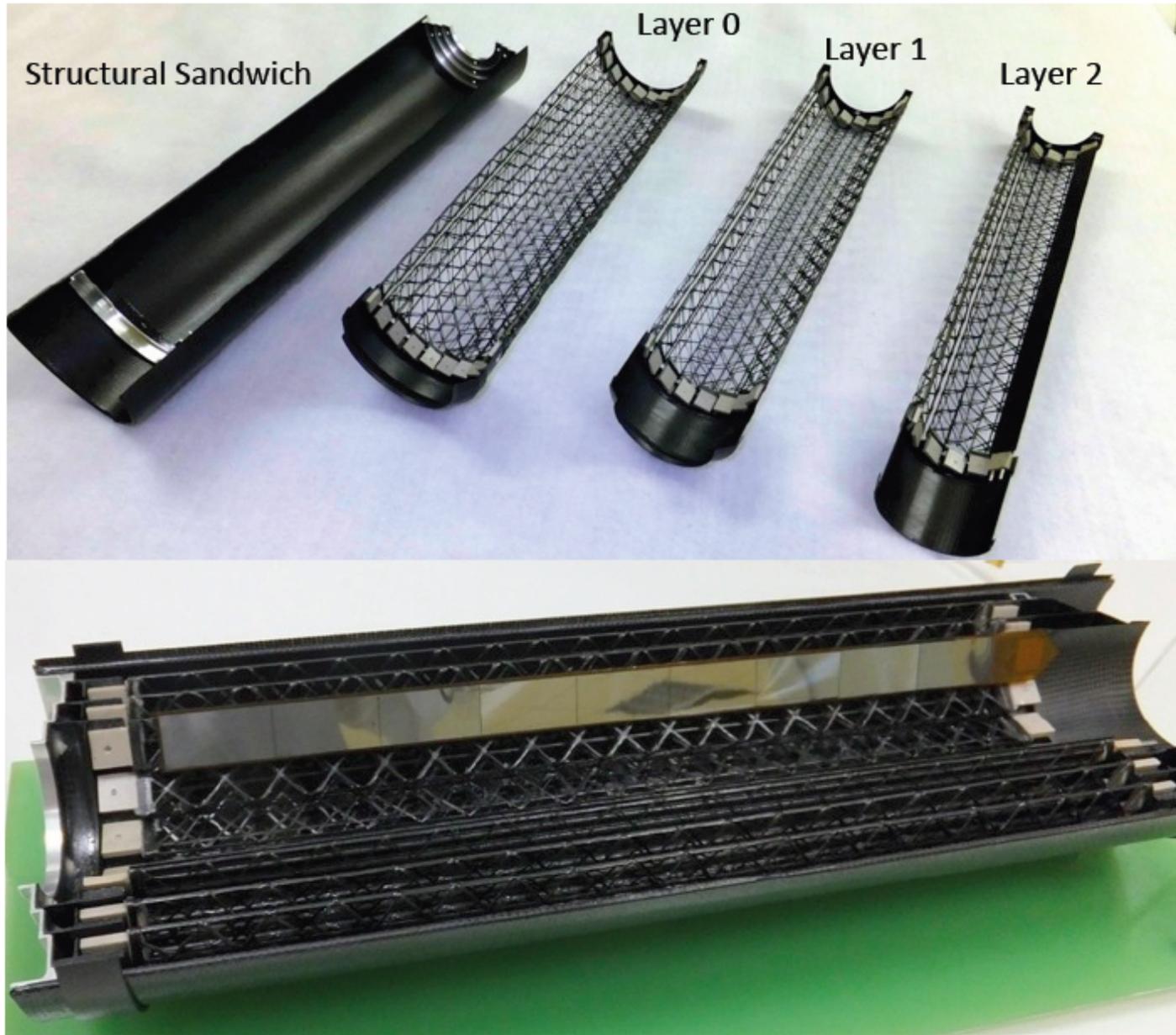


## Inner Barrel Stave

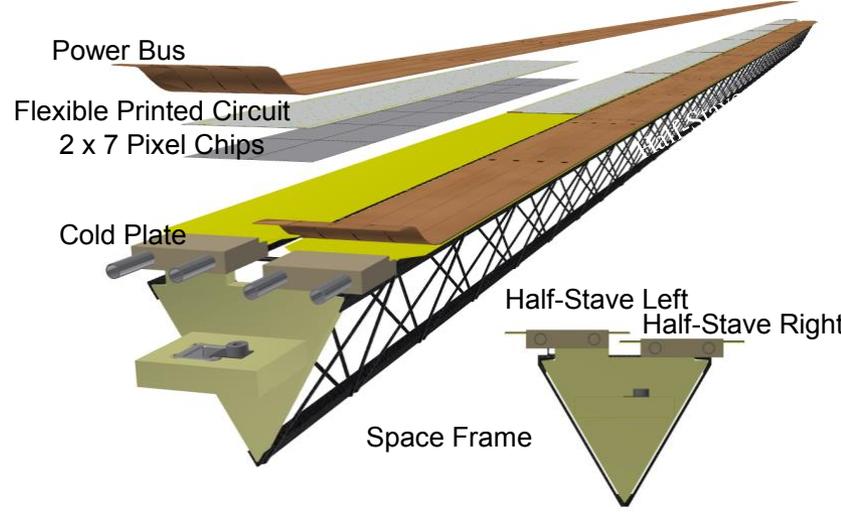
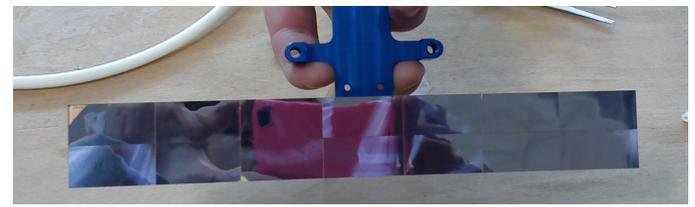
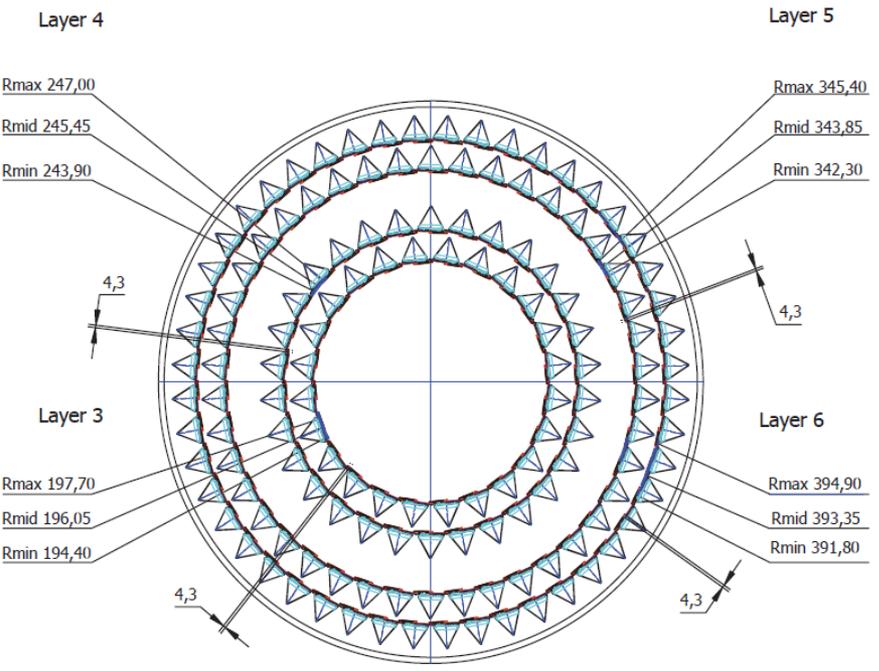


Length 290 mm  
Space Frame total weight 1.5 gr

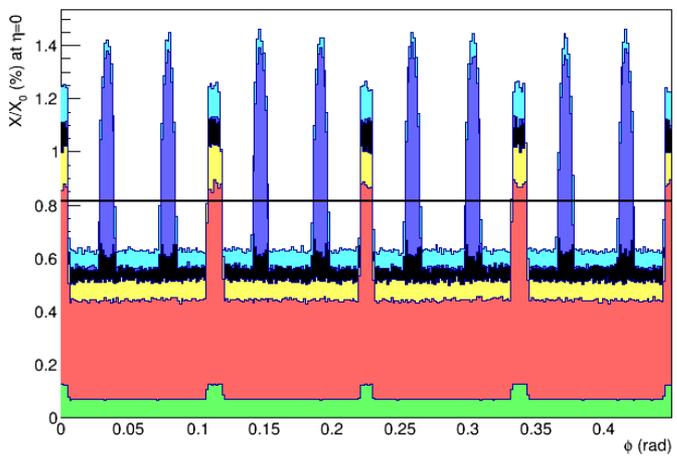
# Inner Barrel: full-scale prototype



# Outer Barrel



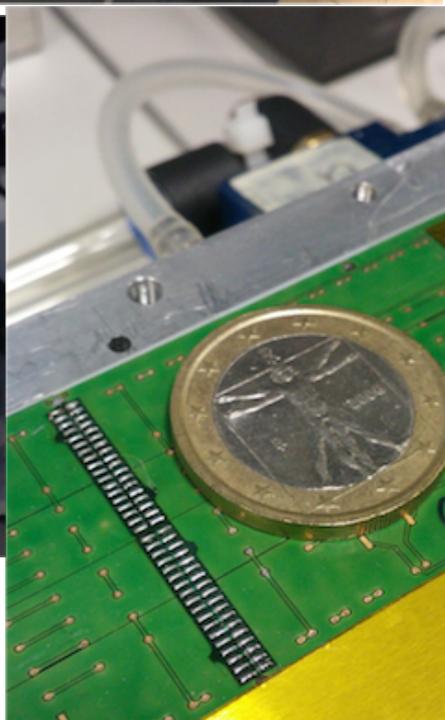
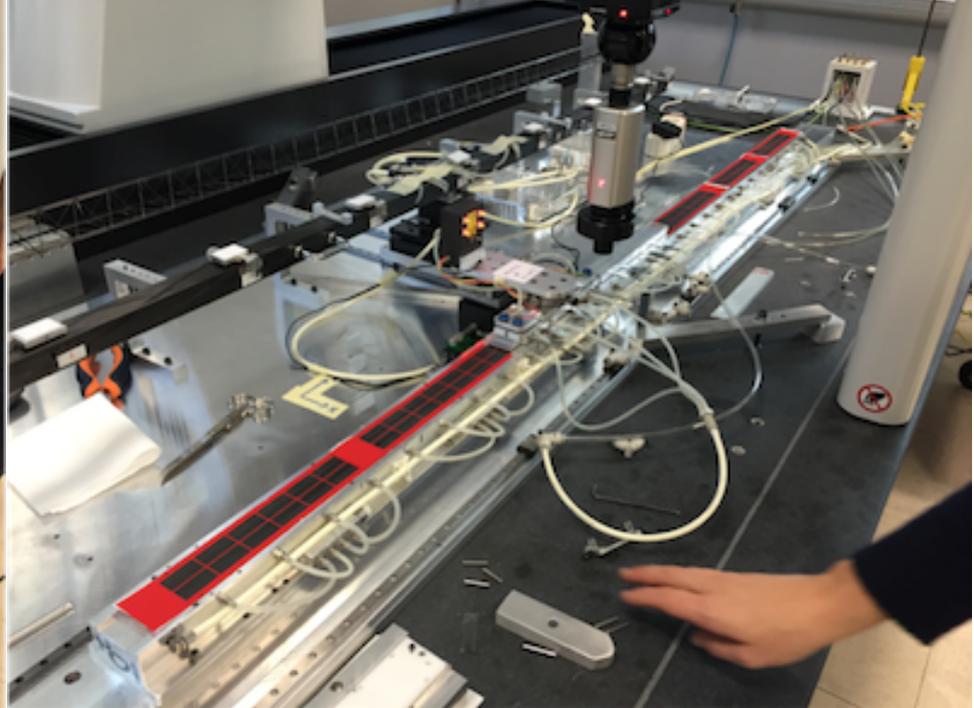
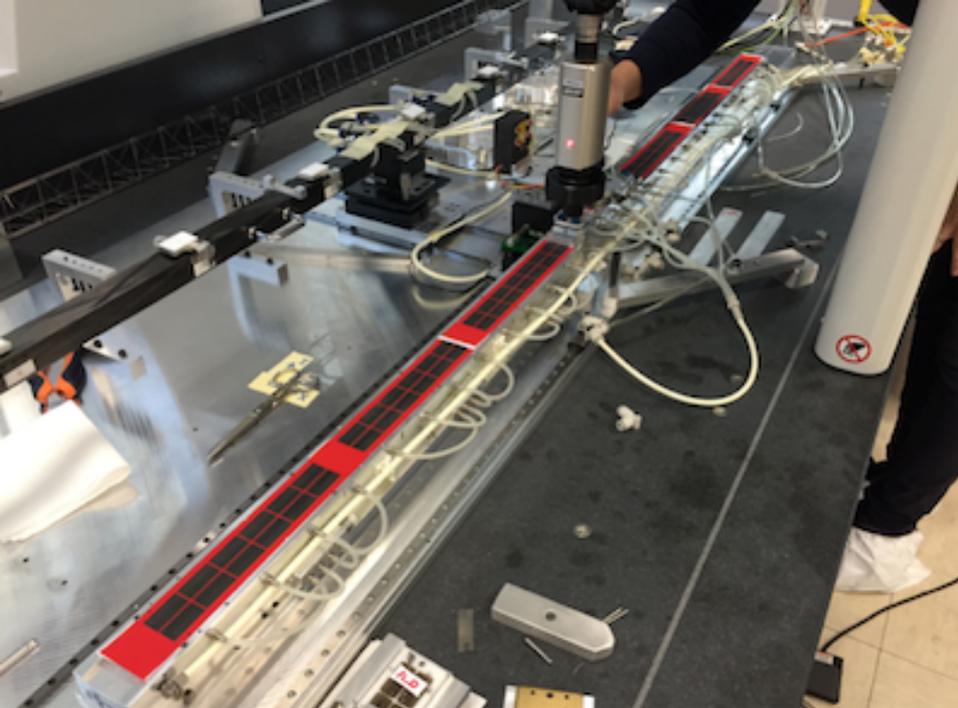
Outer Barrel Stave



- Carbon Structure (9.1%)
  - Water (14.2%)
  - Cooling Pipe Walls and ColdPlate (8.0%)
  - Glue (9.5%)
  - Flex Cable (50.1%)
  - Pixel Chip (9.2%)
- Mean  $X/X_0 = 0.816\%$*



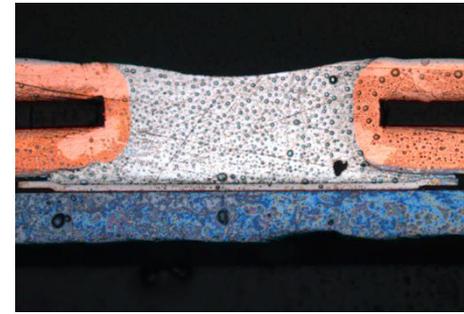
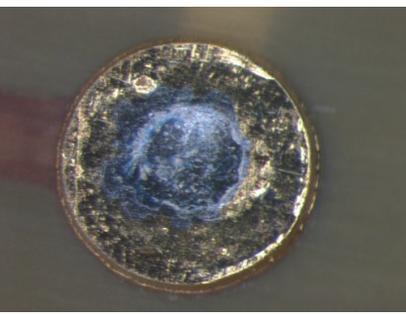
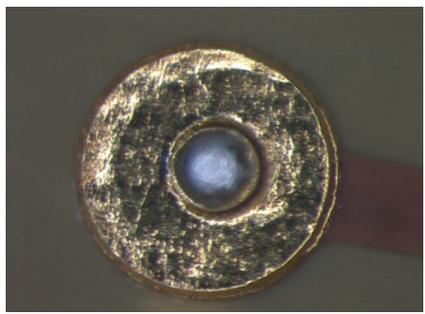
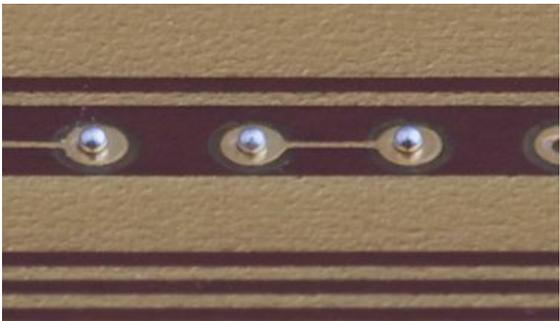
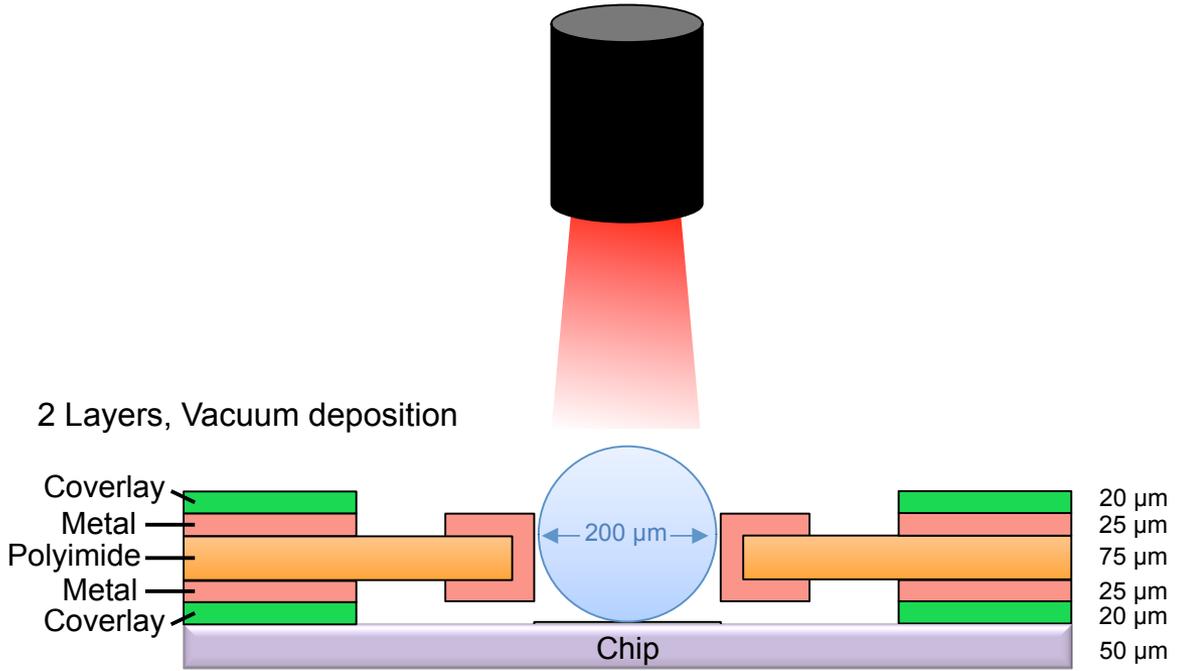
Length 1500 mm  
Space Frame total weight 30 gr



# Flexible Printed Circuit (FPC) interconnection

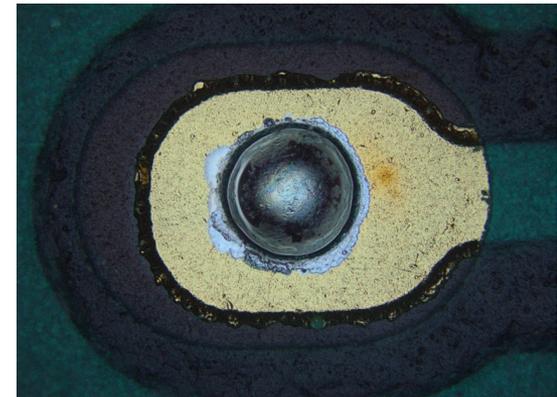
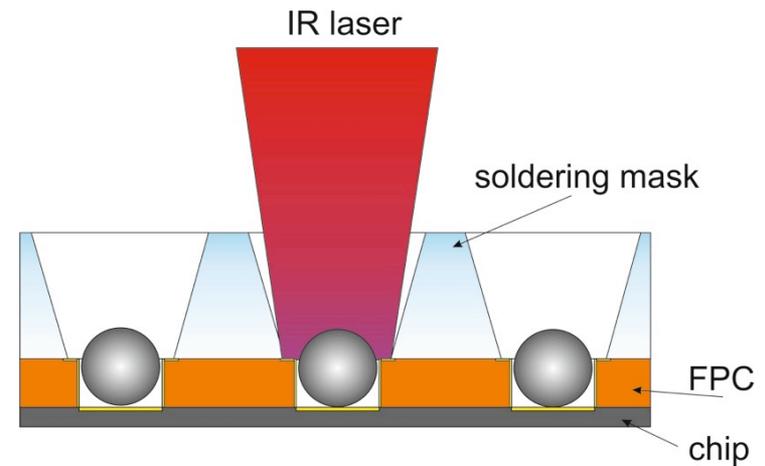


## Laser soldering

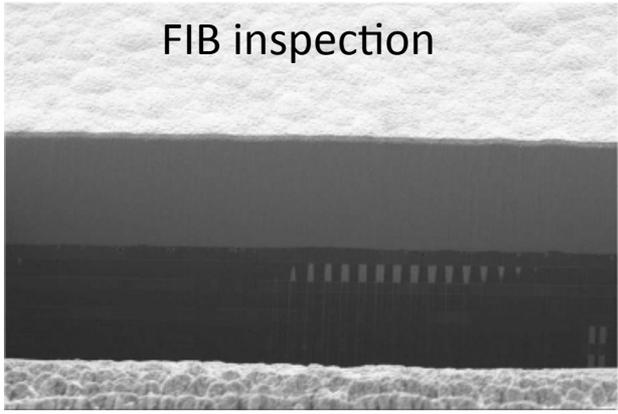
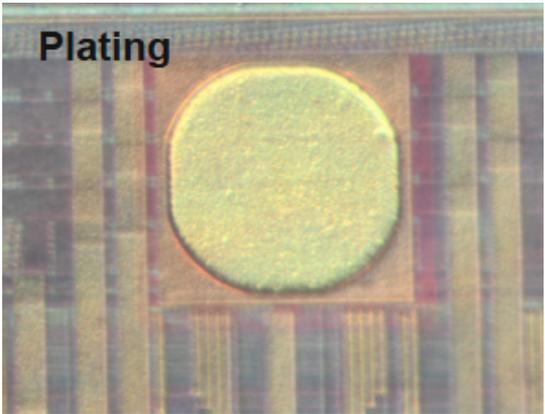


# Flexible Printed Circuit (FPC) interconnection

- Flux-less soldering of 200  $\mu\text{m}$  diameter Sn/Ag (96.5/3.5) balls (227  $^{\circ}\text{C}$  melting T) in vacuum ( $<10^{-1}$  mbar)
- IR diode laser, 976 nm, 25 W, 50  $\mu\text{m}$  focal length, 250  $\mu\text{m}$  beam spot size
- Laser power modulated by pyrometer, programmable T profile ensures precise limitation of heating
- Solder provides both electrical and mechanical connection
- Pre-melting ? Conductive glue? Wire-bonding? ... possible options



# Flexible Printed Circuit (FPC) interconnection



Optical and electrical inspection together with the quality of metallization is according to required spec

➔ Laser Soldering not mature enough to guarantee an adequate yield

Many issues were solved

- Quality of the metallization of FPC VIAs
- Excessive warping of pixel chip
- Assembly jigs out of tolerance



Yield continues to be below requirements

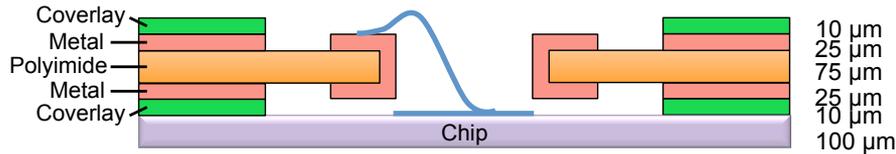
=> too often “cold” soldering or partial wetting of the pad

# Interconnection of pixel chip to flex PCB

A Large Ion Collider Experiment



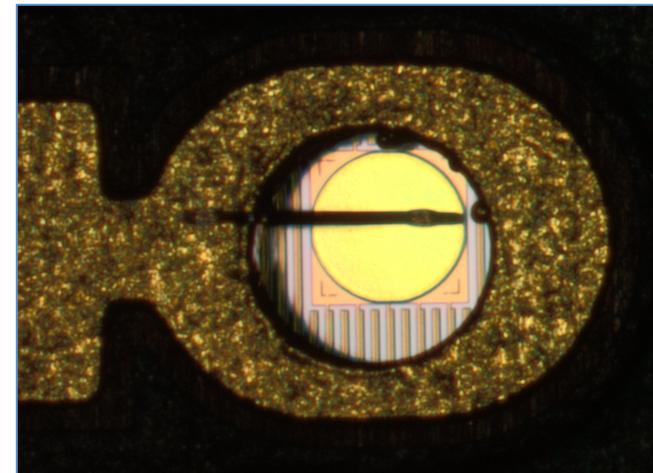
## New baseline: wire bonding



- The chip is glued on the electrical substrate (FPC)
- Electrical interconnection to the FPC using standard wedge (Al) wire bonding trough the FPC VIAs

## Tests with pALPIDE-3 single-chip HIC

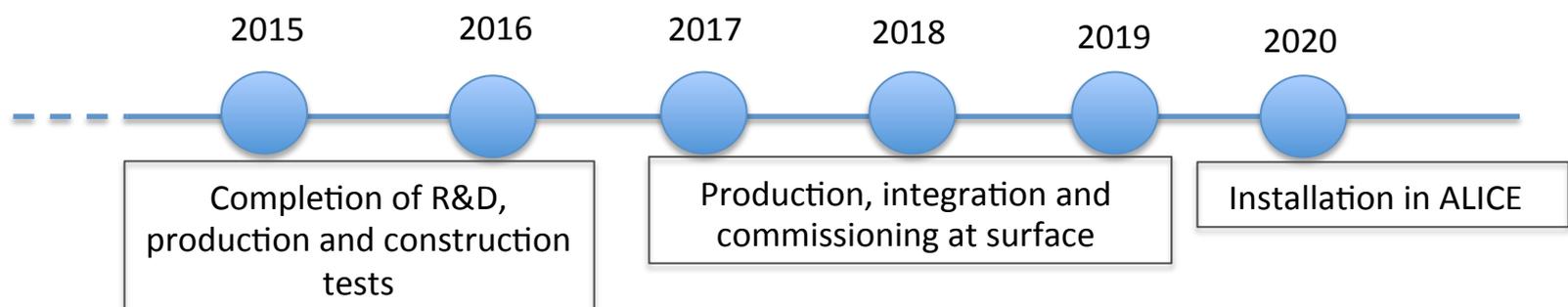
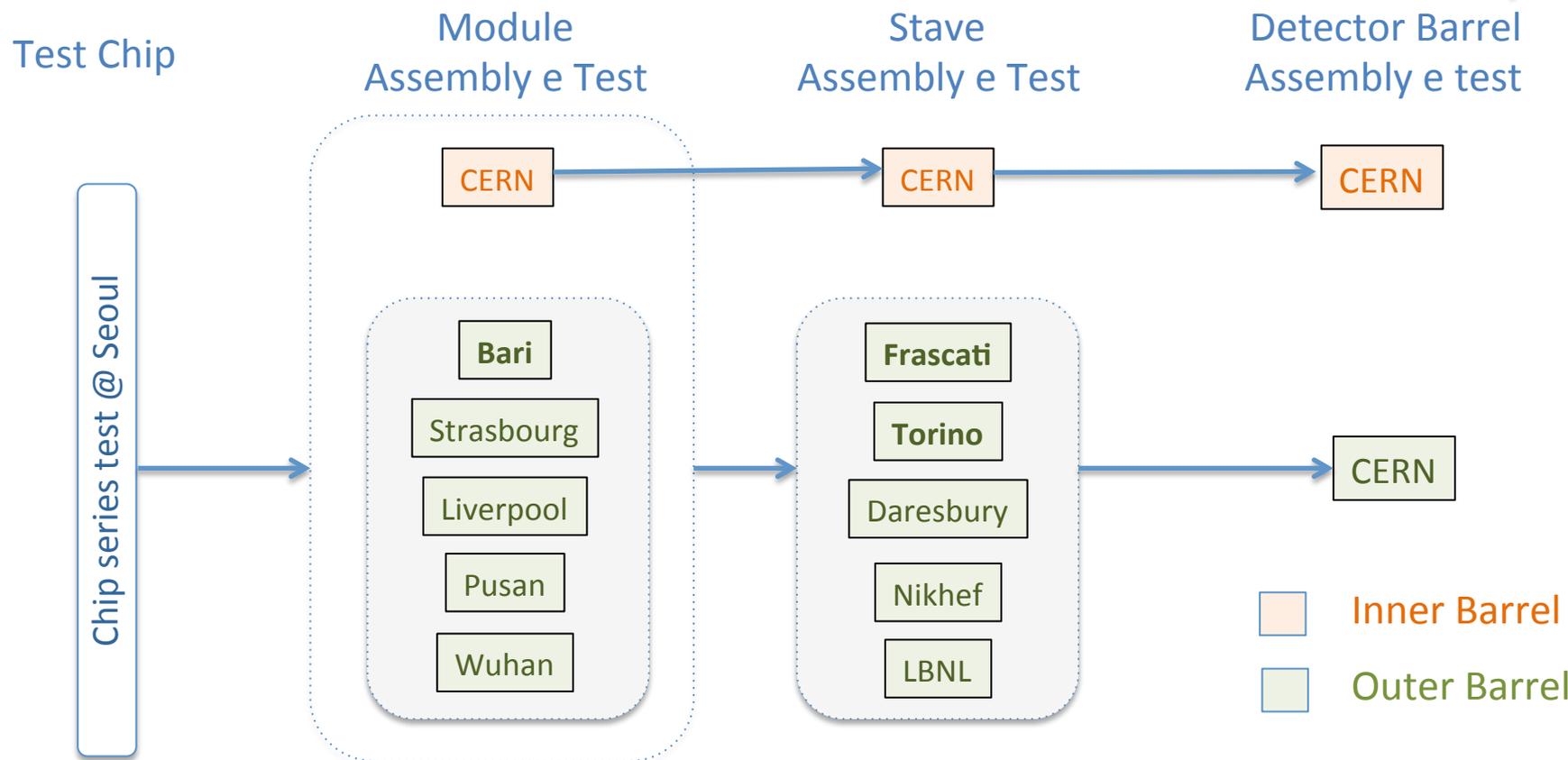
- 5 assemblies with 25 μm Al wire and standard wedge tool (Bari)
- 1 assembly with 25 μm Al wire and deep access wedge tool (Trieste)
- **Results: all working according to specs**



Wire Bonding

There will be multiple connections

# Responsibilities and schedule

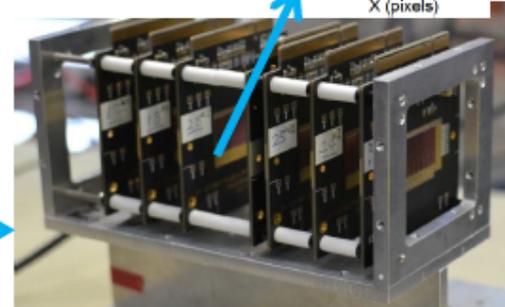
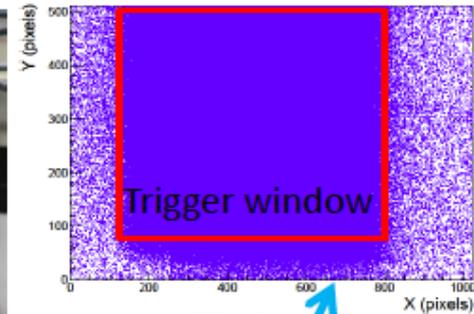
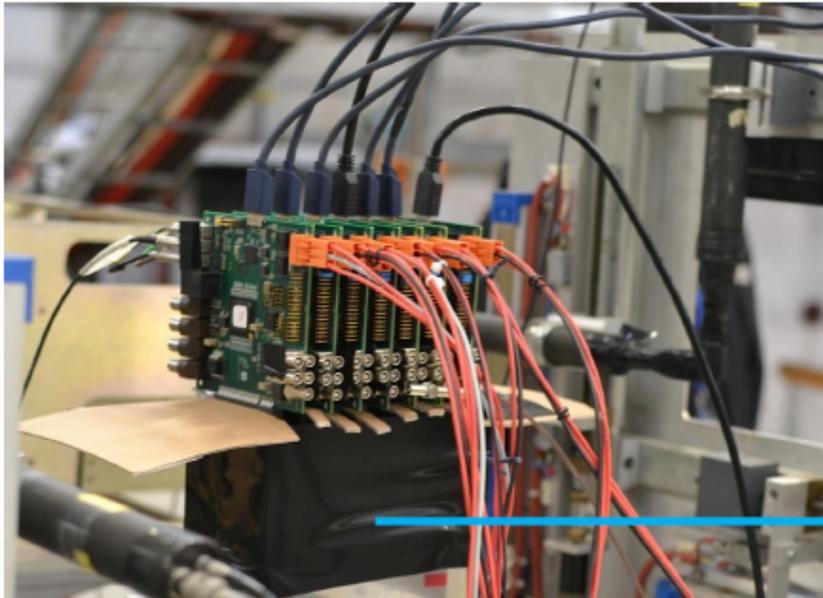


Intensive test beam campaign:

- PS: 5-7 GeV  $\pi^-$
- SPS: 120 GeV  $\pi^-$
- PAL(Korea): 60 MeV  $e^-$
- BTF (Frascati): 450 MeV  $e^-$
- DESY: 5.8 GeV  $e^+$
- SLRI(Thailand): 1.2 GeV

Scan of main parameters  $\rightarrow$   $\sim$  200 settings

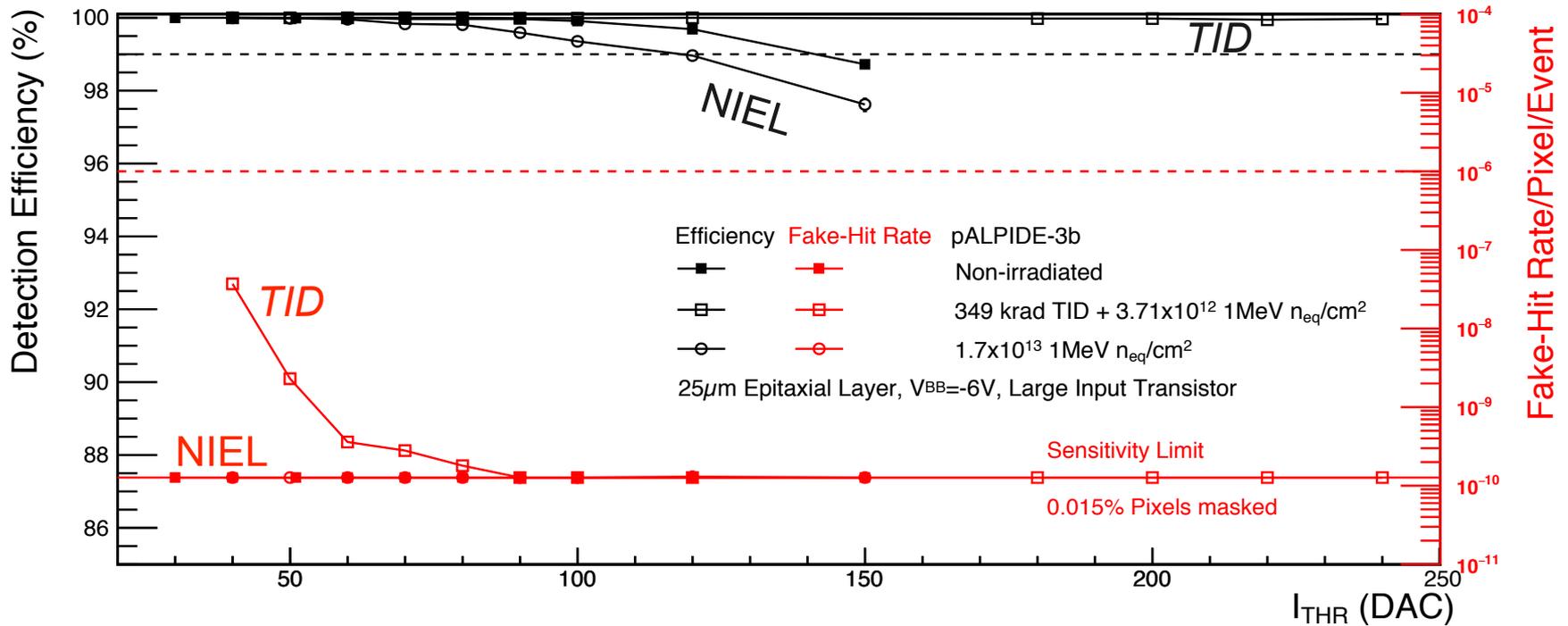
7-plane telescope based on pALPIDE chip



Irradiation studies at: Legnaro, Louvain La Neuve, Prague, PSI, LBNL

# Test Beam: ALPIDE-3

## Efficiency and fake hit rate



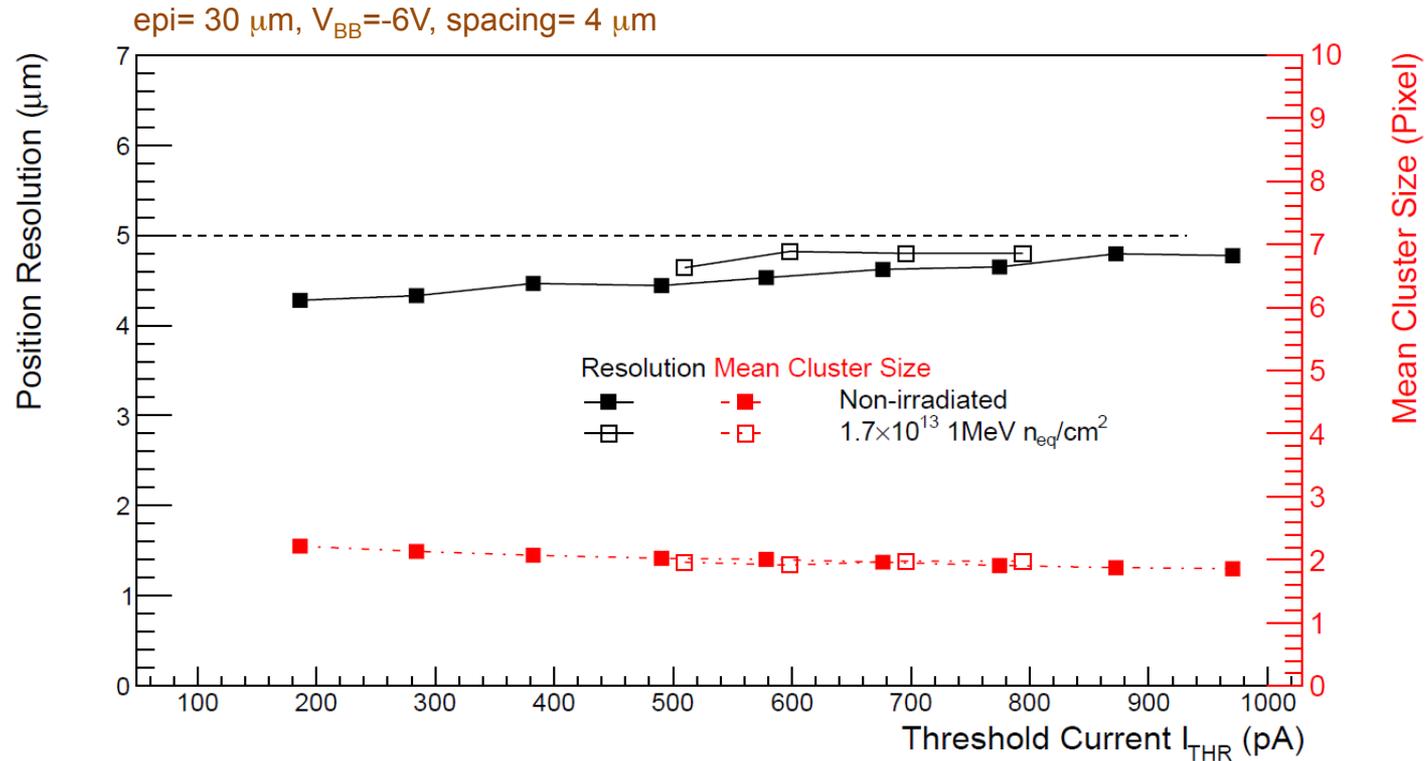
large margin over design requirements

$\lambda_{fake} \ll 10^{-5}$  / event/pixel and  $\epsilon_{det} > 99\%$  over a wide threshold range

Chip of 50  $\mu$ m thick: 3 non irradiated and 3 irradiate with neutrons to  $10^{13}$  (1MeV  $n_{eq}$ )/ $cm^2$   $\rightarrow$  excellent performance also after the irradiation

# Test Beam: ALPIDE-2

## Spatial resolution and cluster size

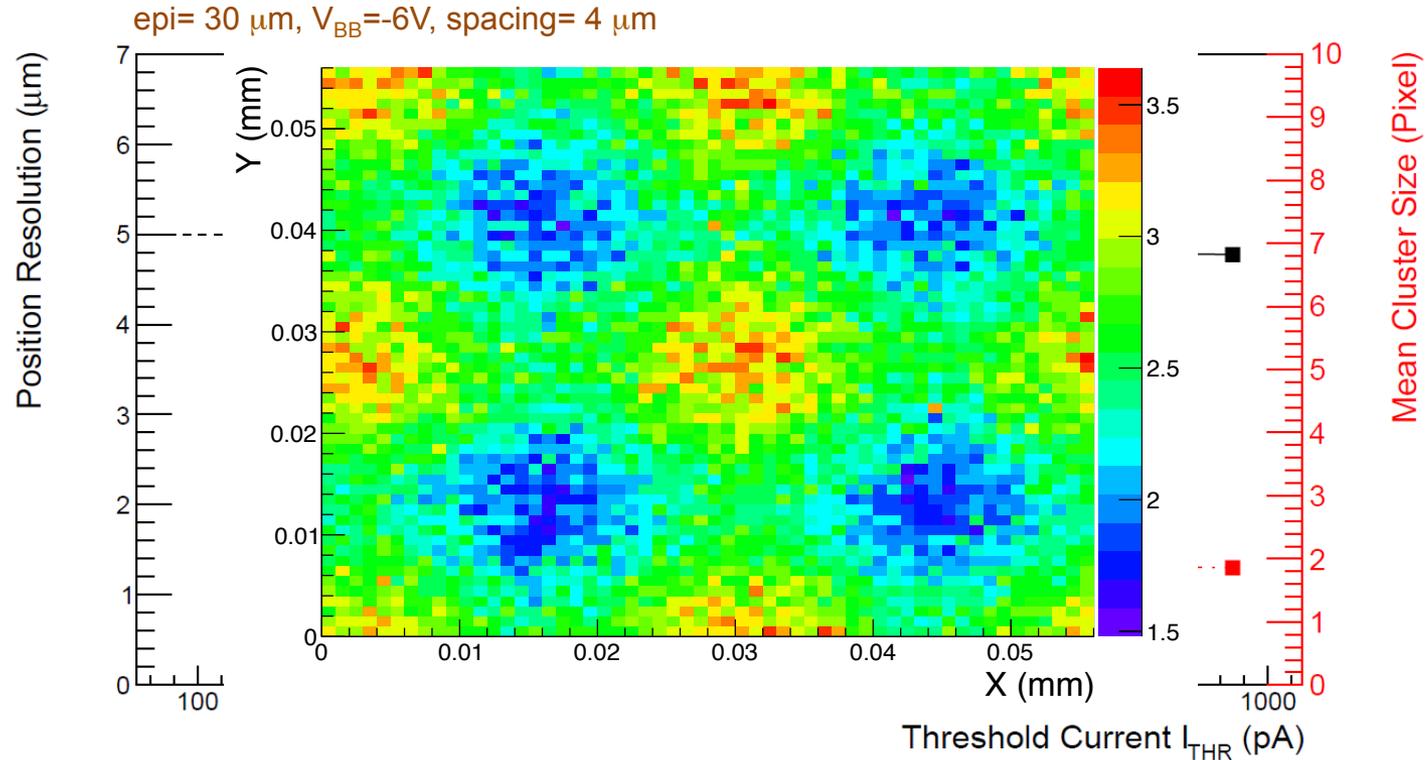


Space point resolution (including tracking error  $\sim 3 \mu\text{m}$ )  $< 5 \mu\text{m}$

Chip of 50  $\mu\text{m}$  thick: 3 non irradiated and 3 irradiate with neutrons to  $10^{13}$  (1MeV  $n_{\text{eq}}$ )/ $\text{cm}^2 \rightarrow$  excellent performance also after the irradiation

# Test Beam: ALPIDE-2

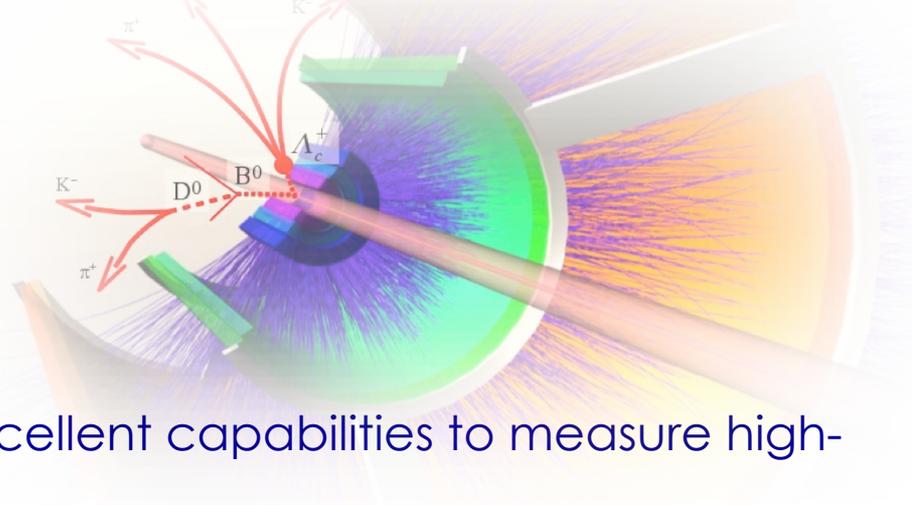
## Spatial resolution and cluster size



Space point resolution (including tracking error  $\sim 3 \mu\text{m}$ )  $< 5 \mu\text{m}$

Chip of 50  $\mu\text{m}$  thick: 3 non irradiated and 3 irradiate with neutrons to  $10^{13}$  (1MeV  $n_{\text{eq}}$ )/ $\text{cm}^2 \rightarrow$  excellent performance also after the irradiation

# Conclusions



- ✓ ALICE is going to extend his already excellent capabilities to measure high-energy nuclear collisions at the LHC
- ✓ The ITS plays a key role in the upgrade in order to achieve precision measurements of rare probes over a large kinematic range

The new ITS represents one of the most advanced solid state detectors

- ✓ 7 layers of 24.000 monolithic CMOS pixel sensors over 10 m<sup>2</sup> surface
- ✓ An intensive R&D program is presently being completed
- ✓ The production will start at the beginning of 2017. The detector will be ready for the LHC LS2 in 2019-2020