

More Computing with Less energy.

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Today's processors designs have some significant challenges in the coming years. Compute demands are projected to continue to grow at a compound aggregate growth rate of 45% per year, with seemingly no end in sight. Also, energy as well as property, plant and equipment costs continue to increase as well. Processor designers can no longer afford to trade off increasing power for increasing performance. System designers need to consider the power requirements of the entire system and not just the power associated with the processor. This talk will focus on the challenges and efforts to bring a class of performance traditionally delivered by high performance system designs with the energy efficiency in the class of today's low power processor platforms.

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Pawlowski joined Intel in 1982. In the 1980's he led the design of the first single board computers based on the 386 processor. He was a lead architect and designer for Intel's early desktop PC and high performance server products. He led the definition of several new technologies for Intel's Pentium® Pro® family of processors, the Pentium® 4 processor and the Intel® Itanium® processor. He also created and led the research for Intel's agile radio architecture for a future generation of wireless products. Prior to his current assignment, he was the director of Intel's Microprocessor Research.

Pawlowski graduated from the Oregon Institute of Technology in 1982 with bachelor's degrees in electrical engineering technology and computer systems engineering technology. He received a master's degree in computer science and engineering from the Oregon Graduate Institute in 1993.

Pawlowski holds 56 patents in the area of system and microprocessor technologies. He has received three Intel Achievement Awards.

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