

SAMPA - a new ASIC for MPGDs

Hugo Natal da Luz and Marco Bregant
University of São Paulo, Brazil
10/12/2014
RD51 Mini Week

SAMPA chip development context

ALICE looking forward for LHC RUN3 increased luminosity

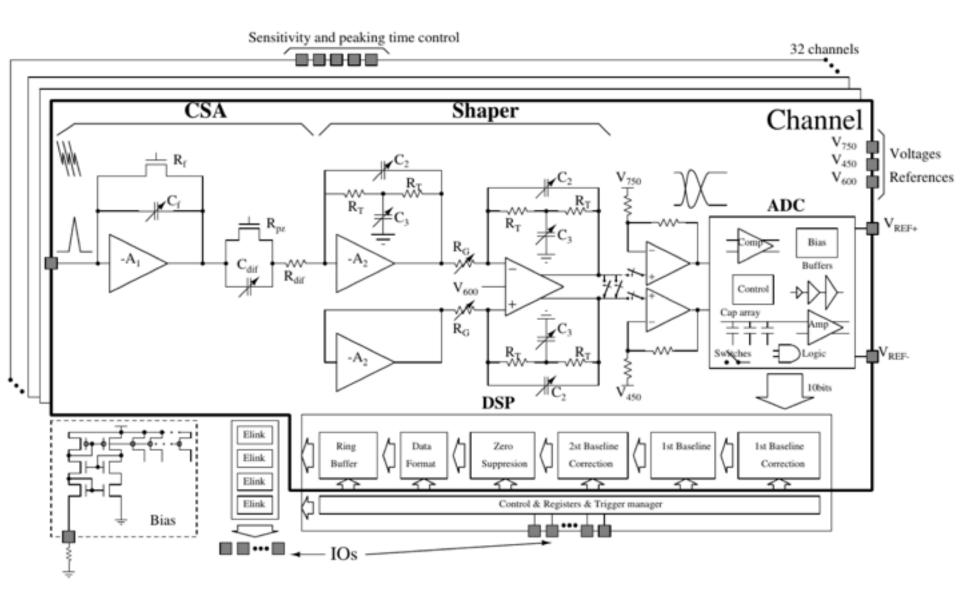
The "physics" requirements are

- interaction rate up to 50 kHz
- trigger-less readout

For the ALICE TPC, that turns in "technical" requirements:

- un-gated "sensor" -> GEMs (was MWPC)
- new front-end chip -> SAMPA

Block Diagram



Status of the project

- First Multi Project Wafer prototype received
 - PRESENTLY BEEING TESTED
- Design of the next prototype (MPW2) in progress. Aim is to have a first full 32ch prototype by summer 2015.
- In this talk: only some first results of playing with SAMPA and a GEM detector.

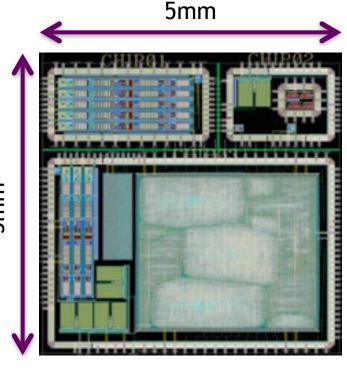
The MPW1 chip(s)

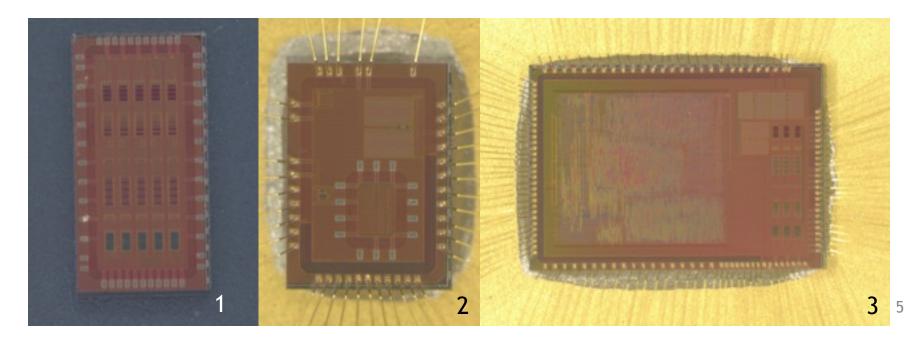
MPW1: sample split in three chips

• Chip1: analog (CSA + shapers), 5 chs =

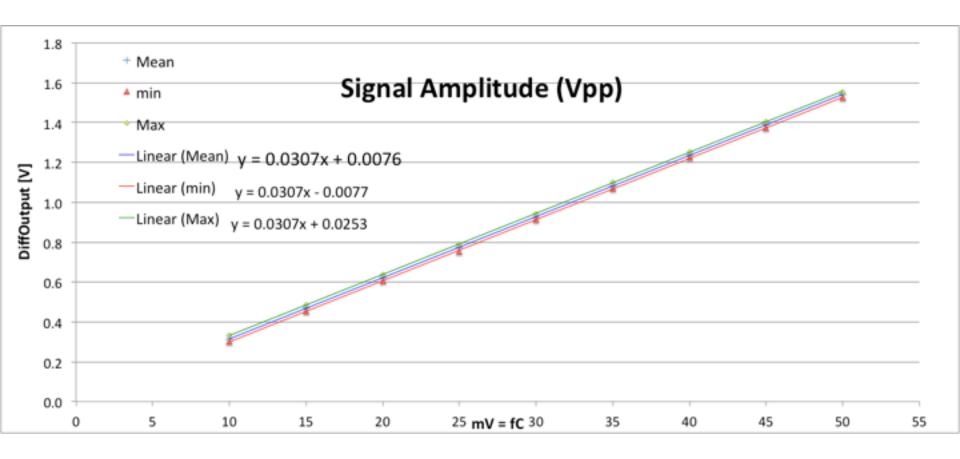
 Chip2: ADC (1 ch) + LVDS Receiver and Transmitter prototype

• Chip3: full chain (CSA-Shaper-ADC-DSP), 3chs





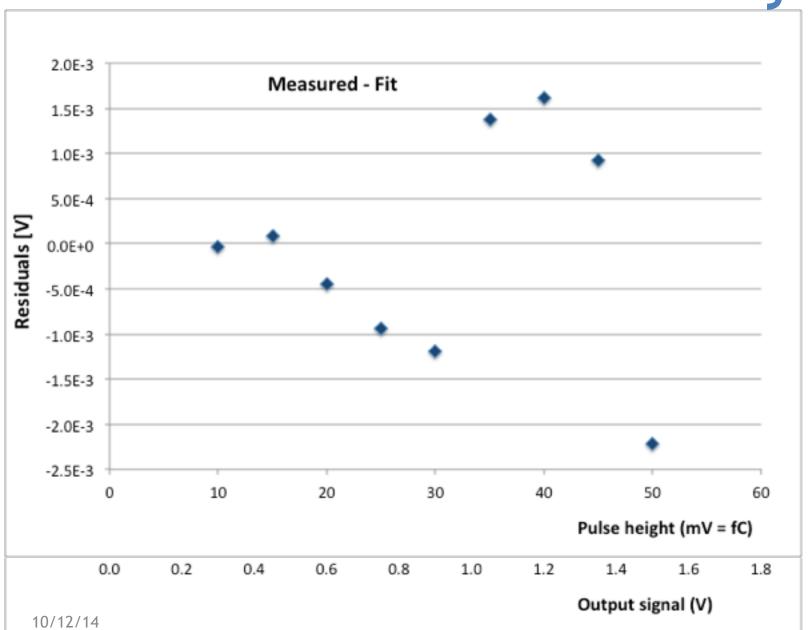
A look at the gain

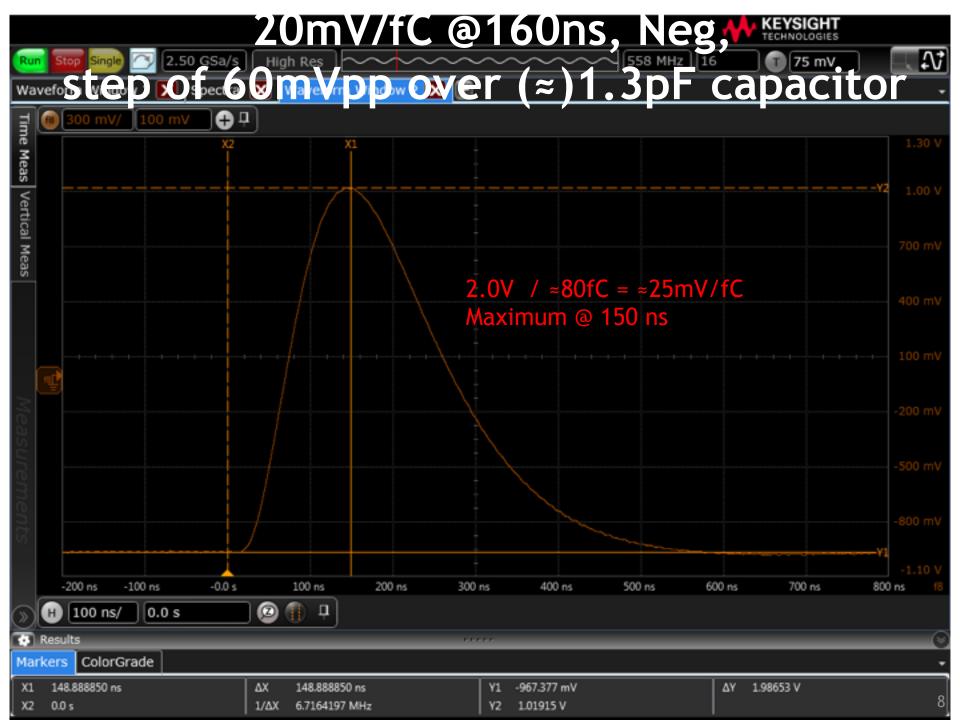


30mV/fC; 160ns Scope "measurements"

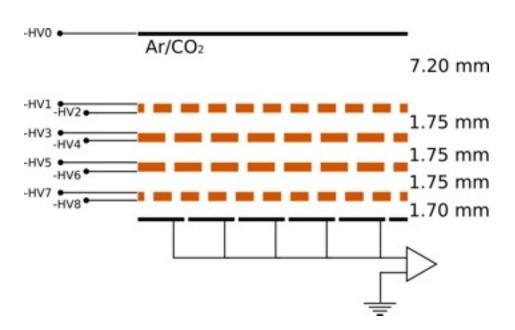
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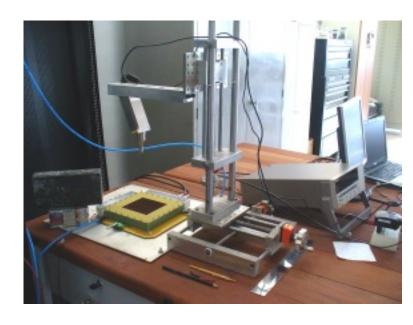
A closer look at linearity





"our" GEM



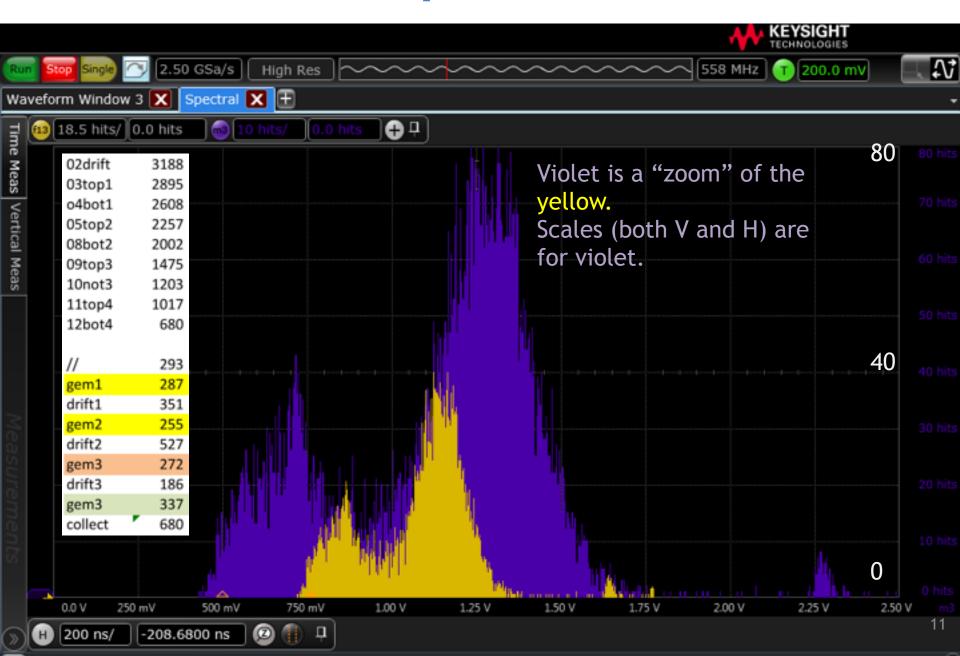


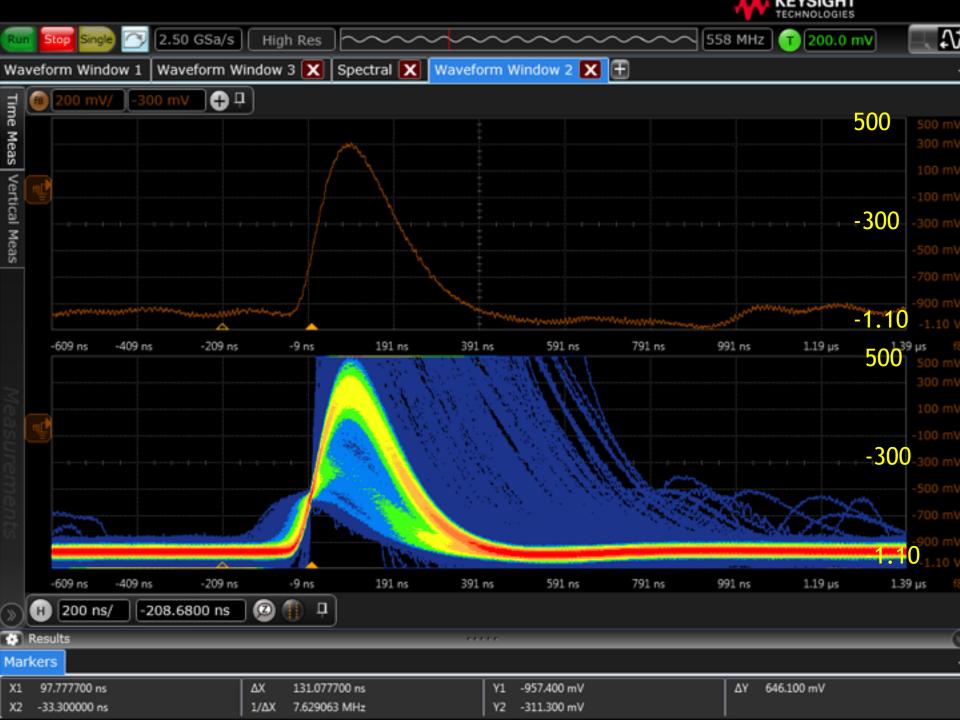
- 4 GEMs setup, with different pitches to minimize ion back-flow,
- 120 readout pads, 8.2 × 10 mm2 ,
- 9 independent HV channels,
- Ar/CO2 mixture (90/10).

setup

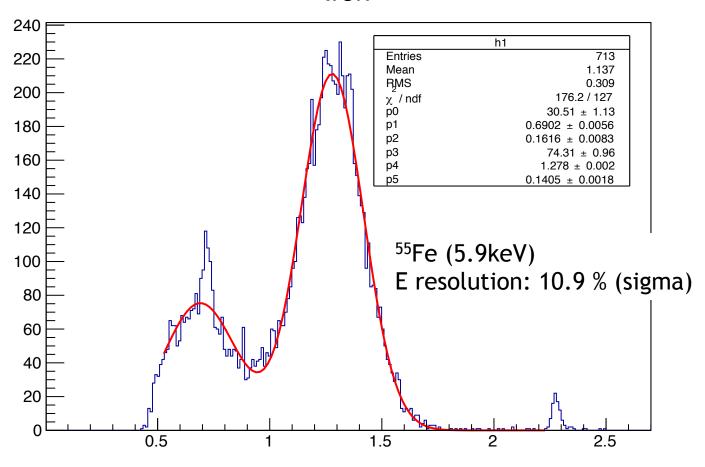
- All pads shorted, but one
- One pad connected to one of the channels of the SAMPA MPW1_chip1 ("analog")
- Looked at shaper output with a scope
- Used a Fe55 source
- GEM stack gain adjusted to match SAMPA dynamic range

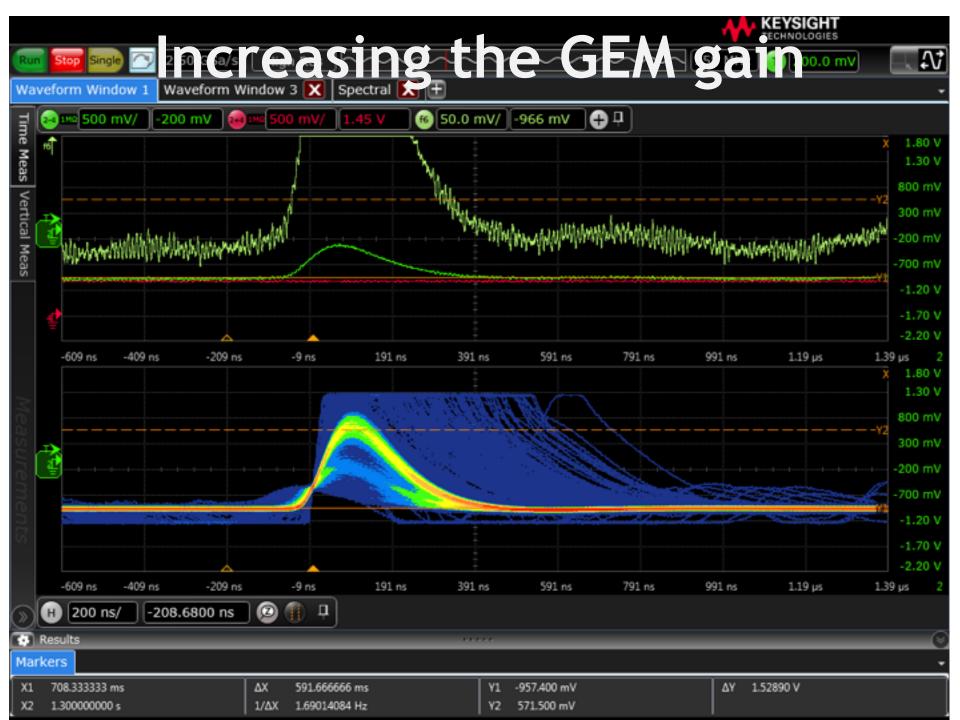
Scope histo



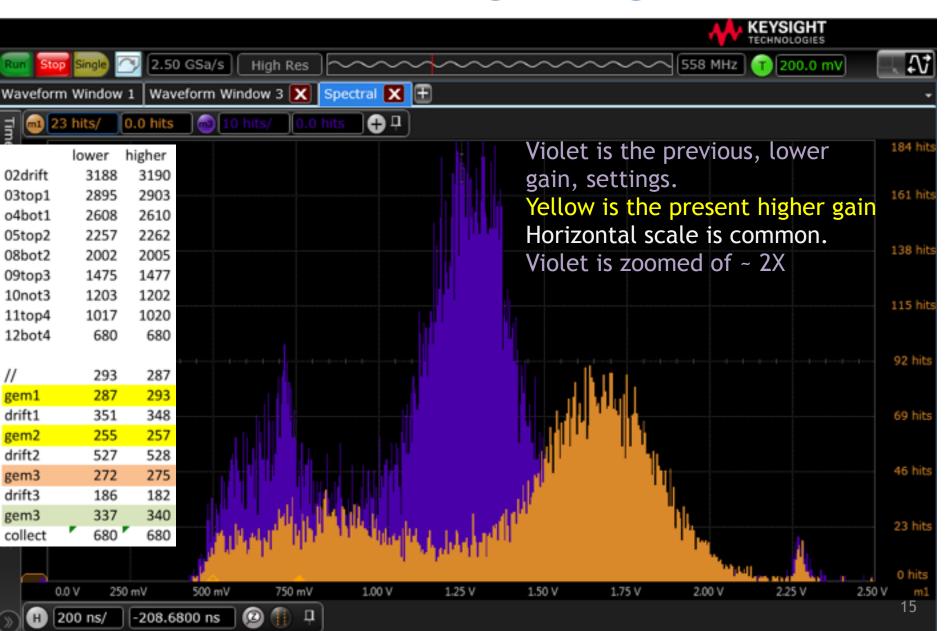




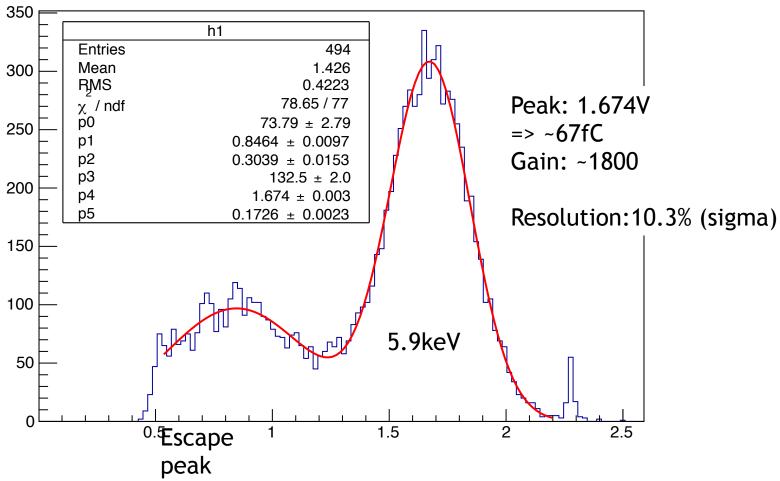




Low vs higher gain







Conclusions

- First try of SAMPA chip (analog, "PASA like", part) connected to a GEM detector.
- Setup and connection still to be cleaned up...
 but

Without a doubt, it is working!

End of Slides