



Contribution ID: 236

Type: **Plenary**

## High-Performance Analog-to-Digital Converters - Evolution and Trends

*Monday, 28 September 2015 17:15 (40 minutes)*

The Analog-to-Digital Converter (ADC) is a fundamental building block of many systems. We will address the trends and evolution of performance observed in ADC design over the last decade. Some of the solutions recently published in the literature, to improve energy efficiency and address the limitations found in advanced CMOS processes, will be discussed. Finally we disclose implementation details from two 12 bit digitally calibrated, high-speed ADCs, using the pipeline and SAR architectures.

**Presenter:** FIGUEIREDO, Pedro (Synopsys Lisbon)

**Session Classification:** Opening