

TWEPP 2015 - Topical Workshop on Electronics for Particle Physics

Tuesday 29 September 2015

Poster: Session 1 - Hall of Civil Engineering (16:30-18:30)

-Conveners: Mitchell Franck Newcomer; Ken Wyllie

time	[id] title	presenter
16:30	[9] TopMetal2-: a direct charge-collecting sensor for high energy physics and imaging in XFAB 350nm process	Mr GAO, Chaosong
16:31	[11] GEMMA and GEMINI, two dedicated mixed-signal ASICs for Triple-GEM detectors readout	PEZZOTTA, Alessandro
16:33	[18] Design of a 10-bit segmented current-steering Digital-to-Analog Converter in CMOS 65nm technology for the bias of new generation readout chips in high radiation environment	LODDO, Flavio
16:34	[19] TOFPETv2: a high-performance ASIC for time and amplitude measurements of SiPM signals in time-of-flight applications	DI FRANCESCO, Agostino
16:35	[36] A 12bits 40MSPS SAR ADC with a redundancy algorithm and digital calibration for the ATLAS LAr calorimeter readout	ZELOUFI, mohamed
16:36	[58] Ongoing studies for the control system of a serially powered ATLAS pixel detector at the HL-LHC	KERSTEN, Susanne
16:37	[64] Characterization of a Three-Side Abutable Cmos Pixel Sensor with Digital Pixel and Data Compression for Charged Particle Tracking : PIXAM	GUILLOUX, Fabrice
16:38	[71] QIE12: A New High-Performance ASIC For the ATLAS TileCal Upgrade	DRAKE, Gary
16:39	[85] A CMOS 0.18 μm 600 MHz clock multiplier PLL and a pseudo-LVDS Driver for the high speed data transmission for the ALICE Inner Tracking System front-end chip.	LATTUCA, Alessandra
16:40	[86] Performance of the new Amplifier-Shaper-Discriminator chip for the ATLAS MDT Chambers at the HL-LHC	RICHTER, Robert
16:41	[87] Design of the NSW Read Out Controller ASIC	COLIBAN, Radu Mihai
16:42	[103] A time-based front-end ASIC for the Silicon micro-strip sensors of the PANDA Micro Vertex Detector	Mr DI PIETRO, Valentino
16:53	[4] Trigger and readout electronics for the STEREO experiment.	BOURRION, Olivier Raymond
16:54	[7] Readout and data acquisition in the NEW detector based on SRS-ATCA	Dr ESTEVE, Raul
16:55	[8] The NA62 Liquid Krypton calorimeter readout system.	RYJOV, Vladimir
16:56	[16] The Giga Bit Transceiver based Expandable Front-End (GEFE) - a new radiation tolerant acquisition system for beam instrumentation	BARROS MARIN, Manoel
16:58	[24] Multi-Gigabit Wireless Data Transfer using the Millimeter Wave Band at 60 GHz	SOLTVEIT, Hans Kristian
17:02	[34] Development of a Standardized Readout System for Active Pixel Sensors in HV/HR-CMOS Technologies for ATLAS Inner Detector Upgrades	RIEGER, Julia Katharina
17:03	[50] Front-End electronics for the FAZIA project	SALOMON, Franck

17:04	[60] The SST-1m prototype camera for the Cherenkov Telescope Array	SCHIOPPA, Enrico Junior
17:05	[62] Performance of the sROD demonstrator for the ATLAS Tile Calorimeter Phase II Upgrade	CARRIO ARGOS, Fernando
17:06	[69] The Clock and Control System for the ATLAS Liquid Argon Calorimeter Phase-I Upgrade	LIU, Tiankuan
17:07	[72] Self-Triggering Readout System for the Neutron Lifetime Experiment PENELOPE	Mr GAISBAUER, Dominic
17:08	[80] New Fast Beam Conditions Monitoring (BCM1F) system for CMS.	Ms ZAGOZDZINSKA, Agnieszka
17:10	[105] The CMS Beam Halo Monitor Electronics	TOSI, Nicolo
17:11	[106] The New Front-End Electronics For the ATLAS Tile Calorimeter Phase 2 Upgrade	DA SILVA GOMES, Agostinho
17:12	[110] A Signal Distribution Board for the Timing and Fast Control Master of the CBM Experiment	MEDER, Lukas
17:23	[2] Commissioning of the Upgraded CSC Endcap Muon Port Cards at CMS	Mr MATVEEV, Mikhail
17:24	[5] The Level-0 Trigger Processor for the NA62 experiment	SOLDI, Dario
17:25	[46] Test of a demonstrator of an MDT-based first level muon trigger for HL-LHC under realistic operating conditions.	KROHA, Hubert
17:26	[61] A Pattern Recognition Mezzanine based on Associative Memory and FPGA technology for Level 1 Track Triggers for the HL-LHC upgrade	FEDI, Giacomo
17:27	[121] A High Bandwidth and versatile Advanced MC Board	DONG, Jianmeng
17:28	[126] Algorithm and implementation of muon trigger and data transmission system for barrel-endcap overlap region of the CMS detector	Dr ZABOLOTNY, Wojciech
17:29	[134] The Evolution of the Region of Interest Builder in the ATLAS experiment	RIFKI, Othmane
17:40	[23] Development of a sub-nanosecond time-to-digital converter based on field-programmable gate array	SANO, Yuta
17:41	[25] Versatile prototyping platform for Data Processing Boards for CBM experiment	Dr ZABOLOTNY, Wojciech
17:43	[152] A Fast Turn-on ADC Scheme and its Engineering Validation	WU, Jinyuan
17:45	[194] A multi-Gigabyte per Second PCI-Express Data Link for Real-Time DAQ Systems	Dr CASELLE, Michele
17:46	[202] FPGA implementation of PCI-express bifurcation for high-throughput data acquisition	DURANTE, Paolo
17:47	[210] Construction, Testing, Installation, Commissioning and Operation of the CMS Calorimeter Trigger Layer-1 CTP7 Cards	SVETEK, Ales
17:58	[21] Design of Si-Photonic structures and evaluation of their radiation hardness dependence on design parameters	ZEILER, Marcel
17:59	[66] High Speed Data Transmission on Small Gauge Cables for the ATLAS Pixel Upgrade	GRILLO, Alex
18:00	[107] A Silicon Photonic Wavelength Division Multiplex System for High-Speed Data Transmission in Detector Instrumentation	SKWIERAWSKI, Piotr
18:01	[183] Board-mount miniature optical transmitters and transceivers for detector readout in particle physics experiments	GONG, Datao
18:12	[13] Transmission Lines Implementation on HDI Flex Circuits for the CMS Tracker Upgrade	KOVACS, Mark Istvan

18:13	[53] Low-Cost Bump-Bonding Process for High Energy Physics Pixel Detectors	Dr CASELLE, Michele
18:14	[195] Polyurethane spray coating of aluminum wire bonds to prevent corrosion and suppress resonant oscillations	Prof. IZEN, Joseph

Wednesday 30 September 2015

Poster: Session 2 - Hall of Civil Engineering (16:30-18:30)

-Conveners: Mitchell Franck Newcomer; Ken Wyllie

time	[id] title	presenter
16:30	[17] Design of a Constant Fraction Discriminator for the VFAT3 front-end ASIC of the CMS GEM detector	LODDO, Flavio
16:30	[115] Development and experimental study of the Read-out ASIC for Muon Chambers of the CBM Experiment	Mr MALANKIN, Evgeny
16:31	[122] High speed readout solution for single-pixel-photon counting ASICs	SZCZYGIEL, Robert
16:32	[144] A 12-bit 60-MS/s 36-mW SHA-less Opamp-Sharing Pipeline ADC in 130nm CMOS	Prof. CHEN, Jinghong
16:33	[150] A front-end ASIC for ionising radiation monitoring with femto-amp capabilities	Mrs VOULGARI, Evgenia
16:34	[153] Pixel front-end with synchronous discriminator and fast charge measurement for the upgrades of HL-LHC experiments	MONTEIL, Ennio
16:35	[162] LOCx2, a low-latency, low-overhead, 2 × 5.12-Gbps serializer ASIC for the ATLAS Liquid Argon Calorimeter trigger upgrade	GONG, Datao
16:36	[164] Simulation of Digital Pixel Readout Chip Architectures for the LHC Phase 2 Upgrades with a SystemVerilog-UVM Verification Environment	CONTI, Elia
16:37	[188] A 12b Rad-Hard Digital Calibrated Single Slope ADC for LHC environment	Dr VERGINE, Tommaso
16:38	[193] Front-end electronics for Micro Pattern Gas Detectors with integrated input protection against discharges	Dr FIUTOWSKI, Tomasz
16:39	[205] SALT, a dedicated readout chip for strip detectors in the LHCb Upgrade experiment	SWIENTEK, Krzysztof Piotr
16:40	[206] Comparison of two fast, ultra-low power 10-bit SAR ADCs in CMOS 130 nm A and B technologies	MORON, Jakub
16:41	[207] FE65_P2: Prototype Pixel Readout Chip in 65nm for HL-LHC Upgrades	HEMPEREK, Tomasz
16:42	[217] First large volume characterization of the QIE10/11 custom front-end integrated circuits	HARE, Daryl
16:43	[227] Radiation hard Regulator circuits for the ALICE ITS Upgrade	GAJANANA, Deepak
16:53	[123] 65k pixel X-Ray camera module of 75µm pixel size	MAJ, Piotr
16:55	[129] Status Of The Central Logic Board Of The KM3NeT Neutrino Telescope	CALVO, David
16:56	[137] Design and Electronics of the CBM Micro-Vertex-Detector	Mr WIEBUSCH, Michael
16:57	[156] ATLAS Transition Radiation Tracker (TRT) Electronics Operation Experience at High Rates	MISTRY, Khilesh Pradip
16:58	[166] Evaluation of a commercial AdvancedTCA board management controller solution	MENDEZ, Julian Maxime
16:59	[169] The ALICE HLT Readout Upgrade for Run2	ENGEL, Heiko
17:01	[174] Preparing the hardware of the CMS Electromagnetic Calorimeter control and safety systems for LHC Run 2	HOLME, Oliver
17:02	[179] LHCb RICH Upgrade: an overview on the photon detector and the electronics system.	CASSINA, Lorenzo

17:03	[182] Instrument Readout for the European Spallation Source	Dr KOLYA, Scott Daniel
17:04	[187] Development and performance studies of TORCH readout electronics using custom MCPs in a test-beam	GAO, Rui
17:05	[191] Commissioning of the on-detector electronics of a novel GEM-based detector for the CMS experiment	YANG, Yifan
17:06	[196] Development of the 40 MHz readout for the upgraded LHCb VELO	HENNESSY, Karol
17:07	[197] GBT Link testing and performance measurement on PCIe40 and AMC40 custom design FPGA boards	MITRA, Jubin KHAN, Shuaib Ahmad
17:08	[214] Hardware evaluation of Xilinx High Level Synthesis for building data readout systems – a CMS ECAL Data Concentrator Card case	HUSEJKO, Michal
17:09	[222] Performance of the prototype readout system for the CMS endcap hadron calorimeter upgrade	PASTIKA, Nathaniel Joseph
17:19	[155] The CMS Level-1 Trigger Barrel Track Finder	LOUKAS, Nikitas
17:20	[180] Processing of the Liquid Xenon Calorimeter's signals for timing measurements.	Mr EPSHTEYN, Leonid
17:21	[212] NaNet-10: a 10GbE Network Interface Card for the GPU-based Low-Level Trigger of the NA62 RICH Detector.	BIAGIONI, Andrea
17:23	[224] Pulsar IIb Design, System Integration and Next-Generation Full Mesh ATCA Backplane Test Results	OLSEN, Jamieson
17:24	[225] A New Way to Implement High Performance Pattern Recognition Associative Memory in Modern FPGAs	OLSEN, Jamieson
17:25	[226] The Level-0 Trigger of the NA62 Liquid Krypton Calorimeter and its performance during first data-taking activities in 2015.	DE SIMONE, Nicola
17:26	[230] The upgrade of the CMS Global Trigger	WITTMANN, Johannes
17:27	[47] Results from longevity studies of the on-detector readout of the CMS Electromagnetic calorimeter	PLANER, Michael
17:28	[81] Upgrade of the ALICE TPC FEE online radiation monitoring system	ZHAO, Chengxin
17:29	[114] Development of a Radiation-Tolerant Component for the Quench Protection System	BITTERLING, Oliver
17:31	[204] COTS ADC for the Accelerator Radiation Environment	TAKAI, Helio
17:32	[250] Triggering on electron, jets and tau leptons with the CMS upgraded calorimeter trigger for the LHC RUN II	ZABI, Alexandre
17:42	[37] Power Distribution for the ATLAS LAr Trigger Digitizer Board	LAZZARONI, Massimo
17:43	[40] Performances of a Remote High Voltage Power Supply for the Phase II Upgrade of the ATLAS Tile Calorimeter	VAZEILLE, Francois
17:44	[45] Low Voltage Power for the ATLAS New Small Wheel Muon Detector	EDGAR, Ryan Christopher
17:45	[54] Rad-Hard Vertical JFET switch for the HV-MUX system of the ATLAS upgrade ITk	Dr FERNANDEZ MARTINEZ, Pablo
17:46	[108] Experience from design, prototyping and production of a DC-DC conversion powering scheme for the CMS Phase-1 Pixel Upgrade	KLEIN, Katja
17:57	[76] High dynamic range diamond detector acquisition system for beam wire scanner applications	SIRVENT BLASCO, Jose Luis
17:58	[99] Standardization of automated industrial test equipment for mass production of control systems	VOTO, Adriana