

TWEPP 2015 - Topical Workshop on Electronics for Particle Physics

Tuesday, September 29, 2015

Poster: Session 1 - Hall of Civil Engineering (4:30 PM - 6:30 PM)

-Conveners: Mitchell Franck Newcomer; Ken Wyllie

time	[id] title	presenter
4:30 P	[10] TopMetal2-: a direct charge-collecting sensor for high energy physics and imaging in XFAB 350nm process	Mr GAO, Chaosong
4:31 P	[11] GEMMA and GEMINI, two dedicated mixed-signal ASICs for Triple-GEM detectors readout	PEZZOTTA, Alessandro
4:33 P	[18] Design of a 10-bit segmented current-steering Digital-to-Analog Converter in CMOS 65nm technology for the bias of new generation readout chips in high radiation environment	LODDO, Flavio
4:34 P	[19] TOFPETv2: a high-performance ASIC for time and amplitude measurements of SiPM signals in time-of-flight applications	DI FRANCESCO, Agostino
4:35 P	[16] A 12bits 40MSPS SAR ADC with a redundancy algorithm and digital calibration for the ATLAS LAr calorimeter readout	ZELOUFI, mohamed
4:36 P	[18] Ongoing studies for the control system of a serially powered ATLAS pixel detector at the HL-LHC	KERSTEN, Susanne
4:37 P	[14] Characterization of a Three-Side Abutable Cmos Pixel Sensor with Digital Pixel and Data Compression for Charged Particle Tracking : PIXAM	GUILLOUX, Fabrice
4:38 P	[11] QIE12: A New High-Performance ASIC For the ATLAS TileCal Upgrade	DRAKE, Gary
4:39 P	[15] A CMOS 0.18 μm 600 MHz clock multiplier PLL and a pseudo-LVDS Driver for the high speed data transmission for the ALICE Inner Tracking System front-end chip.	LATTUCA, Alessandra
4:40 P	[16] Performance of the new Amplifier-Shaper-Discriminator chip for the ATLAS MDT Chambers at the HL-LHC	RICHTER, Robert
4:41 P	[17] Design of the NSW Read Out Controller ASIC	COLIBAN, Radu Mihai
4:42 P	[103] A time-based front-end ASIC for the Silicon micro-strip sensors of the PANDA Micro Vertex Detector	Mr DI PIETRO, Valentino
4:53 P	[1] Trigger and readout electronics for the STEREO experiment.	BOURRION, Olivier Raymond
4:54 P	[1] Readout and data acquisition in the NEW detector based on SRS-ATCA	Dr ESTEVE, Raul
4:55 P	[1] The NA62 Liquid Krypton calorimeter readout system.	RYJOV, Vladimir
4:56 P	[16] The Giga Bit Transceiver based Expandable Front-End (GEFE) - a new radiation tolerant acquisition system for beam instrumentation	BARROS MARIN, Manoel
4:58 P	[14] Multi-Gigabit Wireless Data Transfer using the Millimeter Wave Band at 60 GHz	SOLTVEIT, Hans Kristian
5:02 P	[14] Development of a Standardized Readout System for Active Pixel Sensors in HV/HR-CMOS Technologies for ATLAS Inner Detector Upgrades	RIEGER, Julia Katharina
5:03 P	[10] Front-End electronics for the FAZIA project	SALOMON, Franck
5:04 P	[10] The SST-1m prototype camera for the Cherenkov Telescope Array	SCHIOPPA, Enrico Junior

5:05 P	M02] Performance of the sROD demonstrator for the ATLAS Tile Calorimeter Phase II Upgrade	CARRIO ARGOS, Fernando
5:06 P	M09] The Clock and Control System for the ATLAS Liquid Argon Calorimeter Phase-I Upgrade	LIU, Tiankuan
5:07 P	M2] Self-Triggering Readout System for the Neutron Lifetime Experiment PENELOPE	Mr GAISBAUER, Dominic
5:08 P	M0] New Fast Beam Conditions Monitoring (BCM1F) system for CMS.	Ms ZAGOZDZINSKA, Agnieszka
5:10 P	M05] The CMS Beam Halo Monitor Electronics	TOSI, Nicolo
5:11 P	M06] The New Front-End Electronics For the ATLAS Tile Calorimeter Phase 2 Upgrade	DA SILVA GOMES, Agostinho
5:12 P	M10] A Signal Distribution Board for the Timing and Fast Control Master of the CBM Experiment	MEDER, Lukas
5:23 P	M] Commissioning of the Upgraded CSC Endcap Muon Port Cards at CMS	Mr MATVEEV, Mikhail
5:24 P	M] The Level-0 Trigger Processor for the NA62 experiment	SOLDI, Dario
5:25 P	M6] Test of a demonstrator of an MDT-based first level muon trigger for HL-LHC under realistic operating conditions.	KROHA, Hubert
5:26 P	M1] A Pattern Recognition Mezzanine based on Associative Memory and FPGA technology for Level 1 Track Triggers for the HL-LHC upgrade	FEDI, Giacomo
5:27 P	M21] A High Bandwidth and versatile Advanced MC Board	DONG, Jianmeng
5:28 P	M26] Algorithm and implementation of muon trigger and data transmission system for barrel-endcap overlap region of the CMS detector	Dr ZABOLOTNY, Wojciech
5:29 P	M34] The Evolution of the Region of Interest Builder in the ATLAS experiment	RIFKI, Othmane
5:40 P	M3] Development of a sub-nanosecond time-to-digital converter based on field-programmable gate array	SANO, Yuta
5:41 P	M5] Versatile prototyping platform for Data Processing Boards for CBM experiment	Dr ZABOLOTNY, Wojciech
5:43 P	M52] A Fast Turn-on ADC Scheme and its Engineering Validation	WU, Jinyuan
5:45 P	M94] A multi-Gigabyte per Second PCI-Express Data Link for Real-Time DAQ Systems	Dr CASELLE, Michele
5:46 P	M02] FPGA implementation of PCI-express bifurcation for high-throughput data acquisition	DURANTE, Paolo
5:47 P	M10] Construction, Testing, Installation, Commissioning and Operation of the CMS Calorimeter Trigger Layer-1 CTP7 Cards	SVETEK, Ales
5:58 P	M1] Design of Si-Photonic structures and evaluation of their radiation hardness dependence on design parameters	ZEILER, Marcel
5:59 P	M6] High Speed Data Transmission on Small Gauge Cables for the ATLAS Pixel Upgrade	GRILLO, Alex
6:00 P	M07] A Silicon Photonic Wavelength Division Multiplex System for High-Speed Data Transmission in Detector Instrumentation	SKWIERAWSKI, Piotr
6:01 P	M83] Board-mount miniature optical transmitters and transceivers for detector readout in particle physics experiments	GONG, Datao
6:12 P	M3] Transmission Lines Implementation on HDI Flex Circuits for the CMS Tracker Upgrade	KOVACS, Mark Istvan
6:13 P	M3] Low-Cost Bump-Bonding Process for High Energy Physics Pixel Detectors	Dr CASELLE, Michele

6:14 PM	[195] Polyurethane spray coating of aluminum wire bonds to prevent corrosion and suppress resonant oscillations
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Prof. IZEN, Joseph

Wednesday, September 30, 2015

Poster: Session 2 - Hall of Civil Engineering (4:30 PM - 6:30 PM)

-Conveners: Mitchell Franck Newcomer; Ken Wyllie

time	[id] title	presenter
4:30 P	M17] Design of a Constant Fraction Discriminator for the VFAT3 front-end ASIC of the CMS GEM detector	LODDO, Flavio
4:30 P	M15] Development and experimental study of the Read-out ASIC for Muon Chambers of the CBM Experiment	Mr MALANKIN, Evgeny
4:31 P	M22] High speed readout solution for single-pixel-photon counting ASICs	SZCZYGIEL, Robert
4:32 P	M44] A 12-bit 60-MS/s 36-mW SHA-less Opamp-Sharing Pipeline ADC in 130nm CMOS	Prof. CHEN, Jinghong
4:33 P	M50] A front-end ASIC for ionising radiation monitoring with femto-amp capabilities	Mrs VOULGARI, Evgenia
4:34 P	M53] Pixel front-end with synchronous discriminator and fast charge measurement for the upgrades of HL-LHC experiments	MONTEIL, Ennio
4:35 P	M62] LOCx2, a low-latency, low-overhead, 2 × 5.12-Gbps serializer ASIC for the ATLAS Liquid Argon Calorimeter trigger upgrade	GONG, Datao
4:36 P	M64] Simulation of Digital Pixel Readout Chip Architectures for the LHC Phase 2 Upgrades with a SystemVerilog-UVM Verification Environment	CONTI, Elia
4:37 P	M88] A 12b Rad-Hard Digital Calibrated Single Slope ADC for LHC environment	Dr VERGINE, Tommaso
4:38 P	M93] Front-end electronics for Micro Pattern Gas Detectors with integrated input protection against discharges	Dr FIUTOWSKI, Tomasz
4:39 P	M05] SALT, a dedicated readout chip for strip detectors in the LHCb Upgrade experiment	SWIENTEK, Krzysztof Piotr
4:40 P	M06] Comparison of two fast, ultra-low power 10-bit SAR ADCs in CMOS 130 nm A and B technologies	MORON, Jakub
4:41 P	M07] FE65_P2: Prototype Pixel Readout Chip in 65nm for HL-LHC Upgrades	HEMPEREK, Tomasz
4:42 P	M17] First large volume characterization of the QIE10/11 custom front-end integrated circuits	HARE, Daryl
4:43 P	M27] Radiation hard Regulator circuits for the ALICE ITS Upgrade	GAJANANA, Deepak
4:53 P	M23] 65k pixel X-Ray camera module of 75µm pixel size	MAJ, Piotr
4:55 P	M29] Status Of The Central Logic Board Of The KM3NeT Neutrino Telescope	CALVO, David
4:56 P	M37] Design and Electronics of the CBM Micro-Vertex-Detector	Mr WIEBUSCH, Michael
4:57 P	M56] ATLAS Transition Radiation Tracker (TRT) Electronics Operation Experience at High Rates	MISTRY, Khilesh Pradip
4:58 P	M66] Evaluation of a commercial AdvancedTCA board management controller solution	MENDEZ, Julian Maxime
4:59 P	M69] The ALICE HLT Readout Upgrade for Run2	ENGEL, Heiko
5:01 P	M74] Preparing the hardware of the CMS Electromagnetic Calorimeter control and safety systems for LHC Run 2	HOLME, Oliver
5:02 P	M79] LHCb RICH Upgrade: an overview on the photon detector and the electronics system.	CASSINA, Lorenzo

5:03 P	[M82] Instrument Readout for the European Spallation Source	Dr KOLYA, Scott Daniel
5:04 P	[M87] Development and performance studies of TORCH readout electronics using custom MCPs in a test-beam	GAO, Rui
5:05 P	[M91] Commissioning of the on-detector electronics of a novel GEM-based detector for the CMS experiment	YANG, Yifan
5:06 P	[M96] Development of the 40 MHz readout for the upgraded LHCb VELO	HENNESSY, Karol
5:07 P	[M97] GBT Link testing and performance measurement on PCIe40 and AMC40 custom design FPGA boards	MITRA, Jubin KHAN, Shuaib Ahmad
5:08 P	[M14] Hardware evaluation of Xilinx High Level Synthesis for building data readout systems – a CMS ECAL Data Concentrator Card case	HUSEJKO, Michal
5:09 P	[M22] Performance of the prototype readout system for the CMS endcap hadron calorimeter upgrade	PASTIKA, Nathaniel Joseph
5:19 P	[M55] The CMS Level-1 Trigger Barrel Track Finder	LOUKAS, Nikitas
5:20 P	[M80] Processing of the Liquid Xenon Calorimeter's signals for timing measurements.	Mr EPSHTEYN, Leonid
5:21 P	[M12] NaNet-10: a 10GbE Network Interface Card for the GPU-based Low-Level Trigger of the NA62 RICH Detector.	BIAGIONI, Andrea
5:23 P	[M24] Pulsar IIb Design, System Integration and Next-Generation Full Mesh ATCA Backplane Test Results	OLSEN, Jamieson
5:24 P	[M25] A New Way to Implement High Performance Pattern Recognition Associative Memory in Modern FPGAs	OLSEN, Jamieson
5:25 P	[M26] The Level-0 Trigger of the NA62 Liquid Krypton Calorimeter and its performance during first data-taking activities in 2015.	DE SIMONE, Nicola
5:26 P	[M30] The upgrade of the CMS Global Trigger	WITTMANN, Johannes
5:27 P	[M7] Results from longevity studies of the on-detector readout of the CMS Electromagnetic calorimeter	PLANER, Michael
5:28 P	[M1] Upgrade of the ALICE TPC FEE online radiation monitoring system	ZHAO, Chengxin
5:29 P	[M14] Development of a Radiation-Tolerant Component for the Quench Protection System	BITTERLING, Oliver
5:31 P	[M04] COTS ADC for the Accelerator Radiation Environment	TAKAI, Helio
5:32 P	[M50] Triggering on electron, jets and tau leptons with the CMS upgraded calorimeter trigger for the LHC RUN II	ZABI, Alexandre
5:42 P	[M7] Power Distribution for the ATLAS LAr Trigger Digitizer Board	LAZZARONI, Massimo
5:43 P	[M0] Performances of a Remote High Voltage Power Supply for the Phase II Upgrade of the ATLAS Tile Calorimeter	VAZEILLE, Francois
5:44 P	[M5] Low Voltage Power for the ATLAS New Small Wheel Muon Detector	EDGAR, Ryan Christopher
5:45 P	[M4] Rad-Hard Vertical JFET switch for the HV-MUX system of the ATLAS upgrade ITk	Dr FERNANDEZ MARTINEZ, Pablo
5:46 P	[M08] Experience from design, prototyping and production of a DC-DC conversion powering scheme for the CMS Phase-1 Pixel Upgrade	KLEIN, Katja
5:57 P	[M6] High dynamic range diamond detector acquisition system for beam wire scanner applications	SIRVENT BLASCO, Jose Luis
5:58 P	[M9] Standardization of automated industrial test equipment for mass production of control systems	VOTO, Adriana