

TWEPP 2015 - Topical Workshop on Electronics for Particle Physics

Tuesday, 29 September 2015

Programmable Logic, design tools and methods - Sala 02.1 (09:50 - 10:40)

-Conveners: Jose Carlos Rasteiro Da Silva

time	[id] title	presenter
09:50	[98] Implementation of the Timepix chip in the Scalable Readout System	LUPBERGER, Michael
10:15	[186] A Novel Approach for Pulse Width Measurements in an FPGA	UGUR, Cahit

Programmable Logic, design tools and methods - Sala 02.1 (11:10 - 11:35)

-Conveners: Magnus Hansen

time	[id] title	presenter
11:10	[151] A framework for porting the NeuroBayes machine learning algorithm to FPGAs	BAEHR, Steffen