Session Program

28 September 2015 to 2 October 2015

TWEPP 2015 - Topical Workshop on Electronics for Particle Physics

Programmable Logic, design tools and methods

Lisbon
IST (Instituto Superior Técnico) Alameda Campus Av. Rovisco Pais, 1 1049-001 Lisboa Portugal

Tuesday 29 September

09:50

Programmable Logic, design tools and methods
Session | **Location:** Sala 02.1 | **Convener:** Jose Carlos Rasteiro Da Silva

09:50–10:15   **Implementation of the Timepix chip in the Scalable Readout System**

Speaker
Michael Lupberger

10:15–10:40   **A Novel Approach for Pulse Width Measurements in an FPGA**

Speaker
Cahit Ugur

10:40

11:10

Programmable Logic, design tools and methods
Session |
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Convener: Magnus Hansen

11:10–11:35

A framework for porting the NeuroBayes machine learning algorithm to FPGAs

Speaker
Steffen Baehr

11:35