

# PLC development session planning

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Jerónimo ORTOLA - January 2014

Activity	Time	Time Schedule
Participants arrival	10 min	8:30 - 8:40
Session presentation and introduction	10 min	8:40 - 8:50
Presentation. PLC Basics	10 min	8:50 - 9:00
Presentation. PLC UNICOS architecture	40 min	9:00 - 9:40
Hands-on. Logic importation and check	20 min	9:40 - 10:00
Coffee	20 min	10:15 - 10:20
Hands-on. PLC coding exercise in Step7 generated functions Graph coding of DN1CT and DL of one of the Field objects	50 min	10:20 - 11:10
Presentation. Automatic code generation	10 min	11:10 - 11:20
Presentation. Jython methods	20 min	11:20 - 11:40
Hands-On. Logic Templates check in the UAB folder	30 min	11:40 - 12:10
Hands-On. Jython methods exercise	20 min	12:10 - 12:30
Lunch	1H 30 min	12:30 - 14:00
Hands-on. Logic Templates exercises	60 min	14:00 - 15:00
Hands-on. Generic Stepper creation	60 min	15:00 - 16:20
Coffee	20 min	15:30 - 15:50
Hands-on. Importation and compilation	40 min	16:20 - 17:00
Presentation. Versioning	20 min	17:00 - 17:20