

Developments in CMOS for strip detectors

ATLAS is currently studying the use of CMOS MAPS devices as a replacement for the baseline silicon strip sensors for the Phase-II Strip Tracker Upgrade. One of the key aspects is to establish whether the radiation hardness is suitable for the HL-LHC environment. Two different technologies are being studied: High-Voltage CMOS and High-Resistivity CMOS. Several test chips have already been manufactured. We present the latest results from non-irradiated and irradiated sensors including test beam results and give an outlook on the next steps.

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