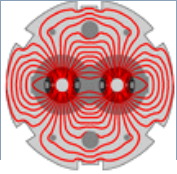


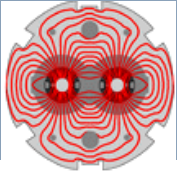
BIS operational experience in a pulsed machine and reliability aspects

the SPS case

J. Wenninger / BE-OP-LHC

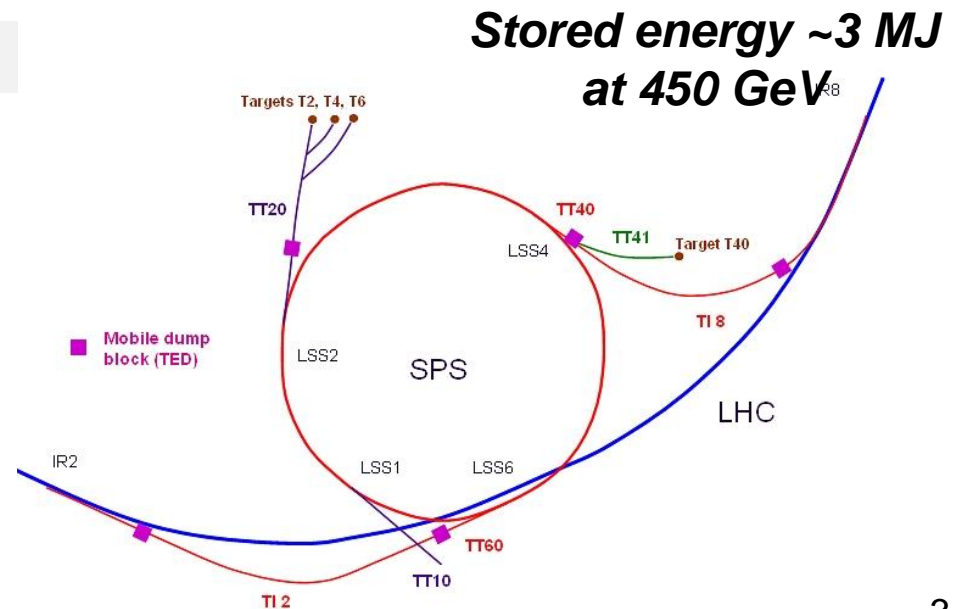
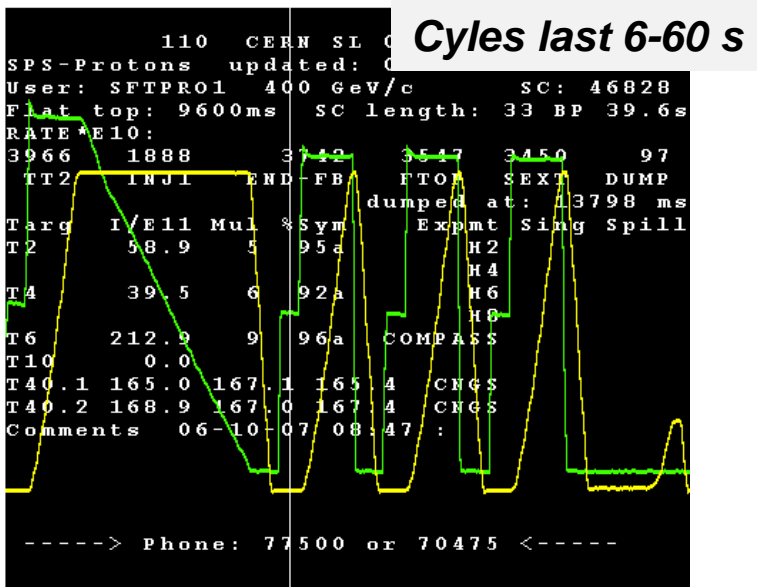


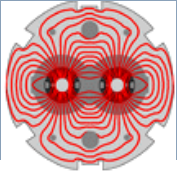
- ❑ The initial BIS was foreseen for the LHC – a collider with a slow cycle of many hours.
 - *Infrequent beam dumps with ample time to diagnose interlocks, rearm the interlock system etc.*
- ❑ During the period when the LHC BIS was designed (early 2000's) work was initiated to transform the Super Proton Synchrotron (SPS) into a much more flexible machine, able to safely provide beams to various destinations and change the beam mixture on the time scale of a minute or so.
 - *The core control system and many 'auxiliary' systems were not flexible enough to rapidly switch mode and beams.*
 - One of those systems was the interlock system.
- ❑ The SPS requirements triggered a study to investigate the possibility to reuse or adapt the future LHC BIS for the SPS.



- ❑ The Super Proton Synchrotron (SPS) is a 6.7 km long accelerator, injector for the LHC.
 - *In operation since ~1976.*
- ❑ In the period 2006-now, it provided beams (protons & ions) to:
 - *The 2 LHC rings (2 extractions),*
 - *The CNGS (CERN Neutrino to Gran Sasso) target,*
 - *The slow extraction experiments,*
 - *The HiRadMat material test facility.*

**Typical SPS
efficiency ~80%**





□ The outcome:

EUROPEAN LABORATORY FOR PARTICLE PHYSICS
CERN — A&B DIVISION

CERN-AB-2003-010-OP

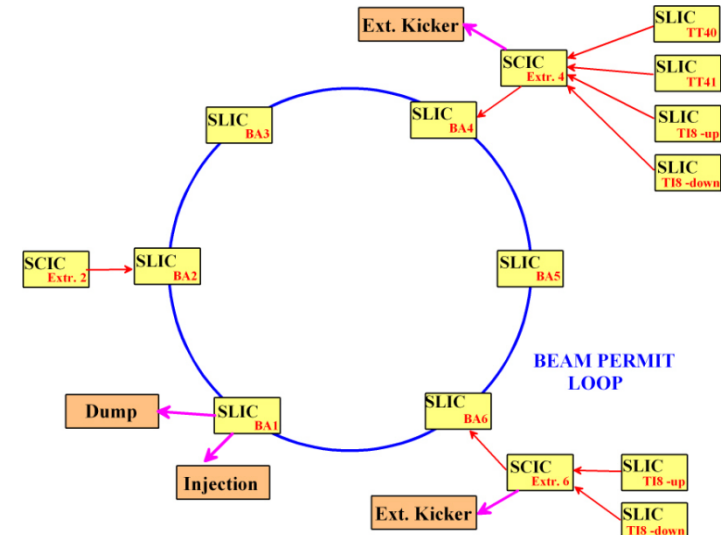
Architecture of the SPS Beam and Extraction Interlock Systems

R. Giachino, B. Puccio, R. Schmidt, J. Wenninger

Abstract

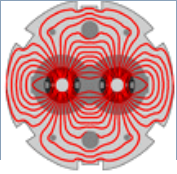
For the machine start-up in 2007, the SPS must deliver beams for fixed target experiments, to the LHC and for the neutrino beam to Gran Sasso with the highest possible efficiency. Fast changes of the SPS cycle will be required, which has many implications for the SPS control system. In particular the existing software and hardware interlock systems that protect the SPS machine against beam induced damage due to failures must be upgraded. This report presents the requirements and architecture of the new hardware interlock systems.

Architecture - all ingredients of current SPS BIS are present.

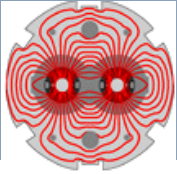


**SLIC (SPS Local Interlock Controller)
is now a standard BIC.**

- Decision to use the LHC BIS at the SPS: first controllers (BICs) were installed in 2006 in the SPS → became a test-bed for the LHC BIC/BIS.
 - *Within 2 years the SPS was equipped with new BIS systems.*

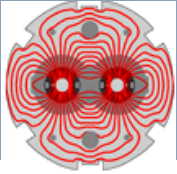


- ❑ The SPS has two '**types**' of beam interlock systems.
- ❑ The main ring system is similar to the LHC – a permit loop with 6 BICs that acts on a beam dump.
 - *One loop for one beam.*
- ❑ The fast extraction (less than one turn [23 us]) interlock systems act on extraction kickers (permit to fire – kick the beam into the transfer line).
 - *They protect the beam transfer path to the destination.*
 - *Permit only when all interlocks associated to the path are TRUE.*
 - *If the permit is not given, the beam remains in the machine and is dumped on the ring beam dump (see above).*
 - *Very similar to a LINAC (but the energy does not change !).*
- ❑ The slow extraction is very poorly protected - can only dump the beam in the ring (not a single 'actuator' that can be stopped).

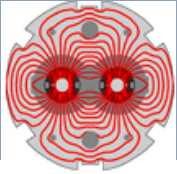


- ❑ The BIS provides the interface between clients (interlock providers) and the kickers (dump or extraction).
- ❑ The overall machine protection quality depends evidently also on the clients and on the actuators / kickers.
 - *Coverage of failures,*
 - *Reliability and safety of the systems.*
- ❑ At the SPS, the protection of the ring and the older transfers is far from optimal. There are a number of known protection loopholes – there are incidents every few years (accepted by the hierarchy...).
 - *Beam loss monitor system for example is too slow (reaction time). A new system is foreseen, but only in a few years from now !*
 - *Between 2006-2013: 2 incidents with damage, 2 near misses.*
- ❑ Only the transfers to LHC and to CNGS (stopped) have excellent protection systems with complete failure coverage and appropriate reaction times – designed after 2000 together with LHC.
 - *No incidents in those lines.*

Pulsing or not – just the same?

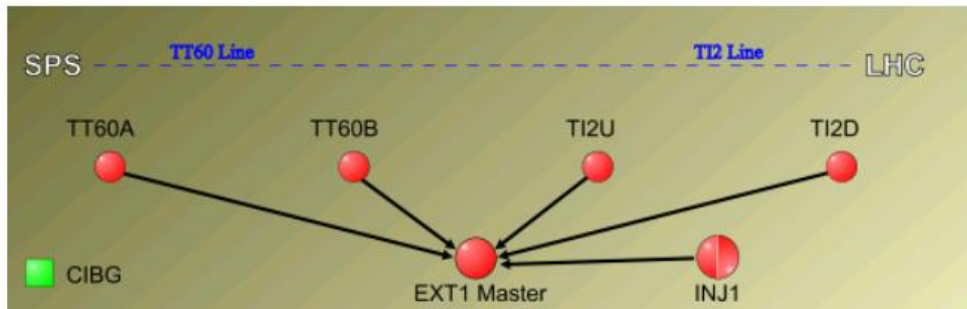


- ❑ The hardware of the SPS BIS is essentially the same as the one of the LHC.
- ❑ Some differences:
 - *As soon as all interlocks are cleared (inputs back to TRUE) the BIS loop is re-armed automatically to avoid blocking beams that are not affected by a fault (e.g. beam loss).*
 - At the LHC the system is manually re-armed.
 - *The SBF is forced to TRUE for the SPS ring – accepted risk.*
- ❑ The pulsing environment is very dynamic:
 - *Different beams every few seconds,*
 - *For extractions the permit given to the kickers lasts ~ 4 milliseconds.*
 - Need a fast post-mortem (end of cycle) analysis as diagnostics.
- ❑ The conditions for extractions are more complex than just AND gates – involves movable dumps & beam intensities.

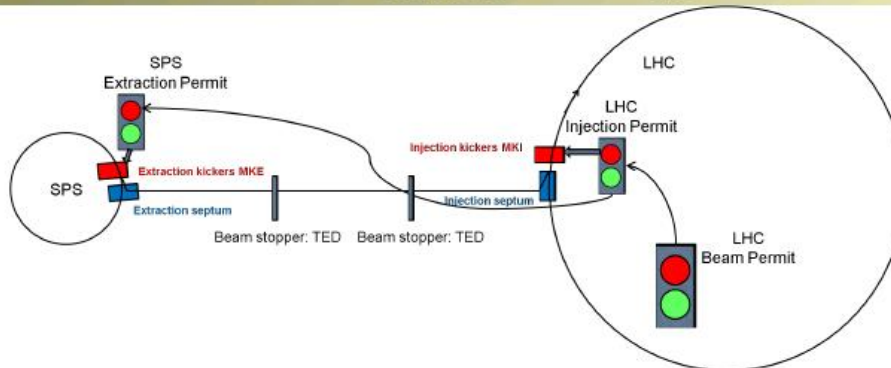


- Transfer from one machine to another or from a machine to a target are conceptually similar.
- Interlocks are grouped by geographical regions (logical segments of lines or accelerators) – merged at the level of a master controller.

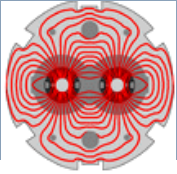
Interlocking hierarchy: LHC → LHC Injection → SPS Extraction



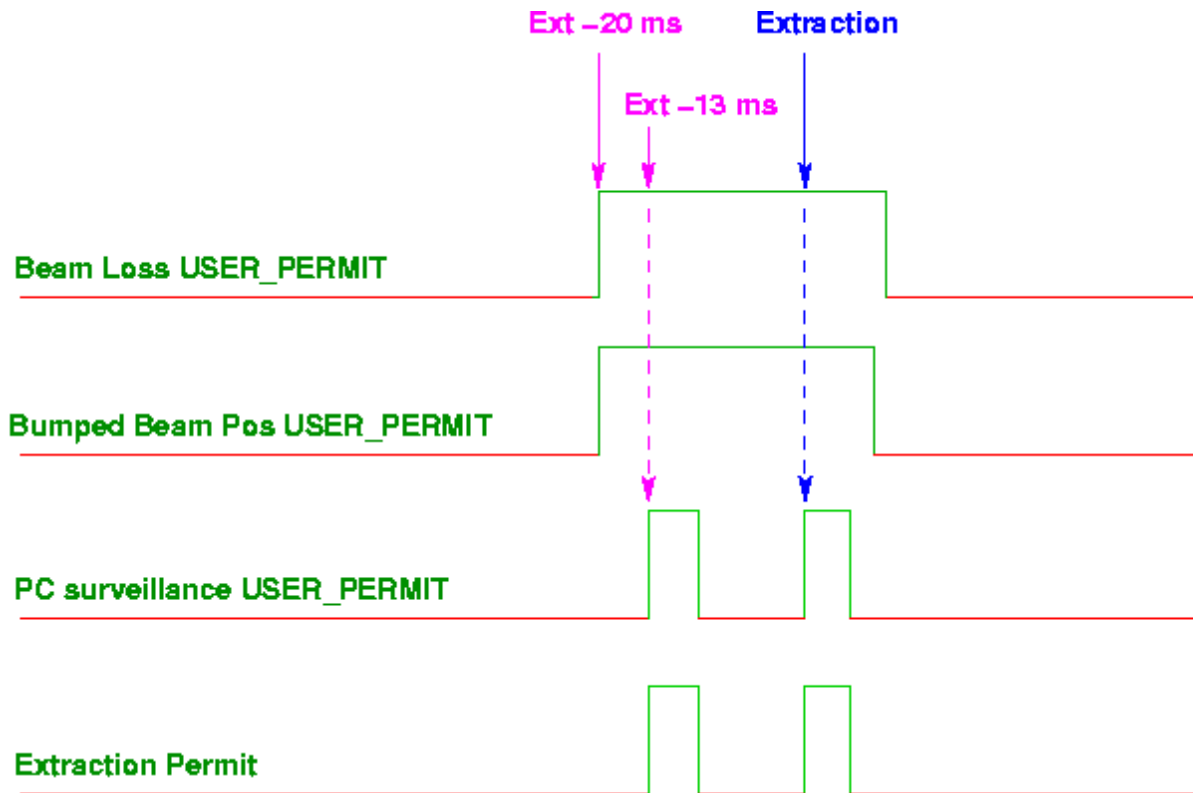
Concept for SPS to LHC transfer protection



Courtesy V. Kain

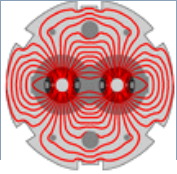


- By the nature of the client inputs, where the conditions are evaluated as close as possible to the time of extraction → the BIS signal to the extraction kickers is very short !



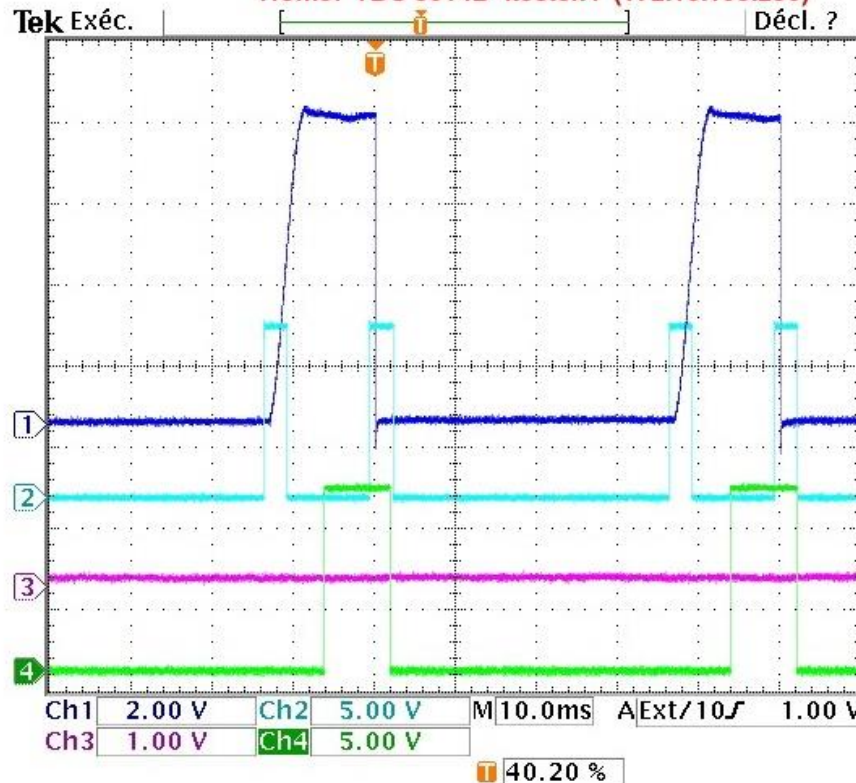
First pulse charges the kicker, the second triggers the current pulse

Pulse width ~3-4 ms, 2 pulses spaced by 13 ms



- Signal details for the extraction kickers.
 - *PFN: Pulse Forming Networks (bank of capacitors & inductances to shape the magnet pulse).*
 - *BETS: Beam Energy Tracking System surveying the beam energy (main magnet current) and the voltage on the PFNs.*

Home: TDS 3014B kestek4 (172.18.195.250)



PFN voltage

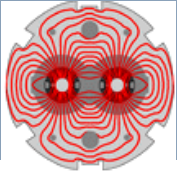
Extraction interlock permit

LHC BETS

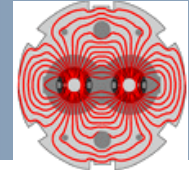
CNGS BETS

24 Sep 2007
12:06:19

Intermittent failure handling

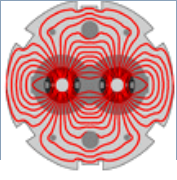


- ❑ The SPS handles multiple beams in parallel and beam loss leading to a dump can occur on one beam and not on the others.
 - *Applies to other beam observables.*
- ❑ At the level of the ring such interlocks are reset automatically after the cycle – latched only after 3 consecutive losses on the same beam.
 - *Beam loss is frequent at injection – beam parameters from the injectors !!*
- ❑ For the transfer lines such interlocks are latched – require manual reset by the operator.
 - *Transfer are very clean, beam loss triggering an interlock was only observed*
 - twice in 5 years on CNGS beams,
 - never on a LHC beam.
 - *So we can afford to be strict without machine availability issue.*

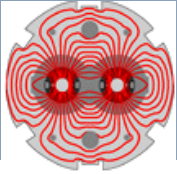


Year	No. extractions (10^6)	Intensity on T40 (10^{19} p)
2008	0.99	1.78
2009	1.82	3.53
2010	2.14	4.04
2011	2.55	4.84
2012	2.40	3.80
Sum	9.90	17.9

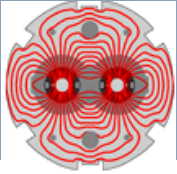
- Close to 10 million beam transfers without damage.
 - *One near miss during an intervention (late evening repair) on the extraction kicker internal protection → combined mistake of the expert and of the operation team (try right away with high intensity !).*



- Operational experience with the current BIS hardware is excellent. Interventions and failures are rare – I cannot remember any.
 - *Issues with the BIS account for 'negligible downtime at the SPS.'*



- ❑ The standard BIC/BIS supervision, based on a device based approach, proved insufficient for proper diagnostics in operation.
 - *To slow, to complicated to navigate across the screens and plots – want simple diagnostics within second after end of a cycle.*
- ❑ Dedicated diagnostics software was therefore build by operation for operation with a system approach (system = one extraction or the ring) – top to bottom philosophy !
 - *The same information available in the standard BIS software, but organized to provide a simple view for operation crews.*



- ❑ Very compact and simple overview for ring and extraction.
- ❑ 4 systems fit on one screen.
- ❑ History of the last 16 cycles.
 - *With filters on types.*

Extraction permit

SPS BIS Monitor V4.99.99

File BIC Details MKE & BETS LTIM & Prepulse Inits & Resets Help

LHC B1 RBA: jwenning Timing LHCPILOT >> SPS_DUMP # 5872

Extraction Overview MKE6 Status BIC Overview Active Intlks Masks

Extraction Status LHC B1

Time	User	Ring BCT	Extr BCT	BETS	Extr BIS
15:28:46	LHCPILOT	11	0		
15:28:14	LHCPILOT	13	0		
15:27:42	LHCPILOT	11	0		
15:27:09	LHCPILOT	13	0		
15:26:37	LHCPILOT	11	0		
15:26:04	LHCPILOT	11	0		
15:25:32	LHCPILOT	11	2		
15:25:00	LHCPILOT	11	6		
15:24:27	LHCPILOT	11	0		
15:23:55	LHCPILOT	11	0		
15:23:22	LHCPILOT	11	0		
15:22:50	LHCPILOT	11	0		
15:22:18	LHCPILOT	11	0		
15:21:45	LHCPILOT	13	7		
15:21:13	LHCPILOT	11	0		

Open Scope Display

Interlock Channel List

- CIB.BA6.TT60A In 3 : OP Switch
- CIB.BA6.TT60A In 4 : MKE6 Status
- CIB.BA6.TT60A In 5 : MSE/MST Girder & Magnet
- CIB.BA6.TT60A In 8 : PC FEI TT60
- CIB.BA6.TT60A In 9 : PC FEI MSE/MST/LSS6 Bumpers
- CIB.BA6.TT60A In 10 : PC FEI MBR TT60

SPS BIS Monitor V4.99.99

File BIC Details Economy Inits & Resets Help

SPS-RING RBA: jwenning Timing SFTION3 >> SPS_DUMP # 5843

BIC Overview Active Intlks Masks

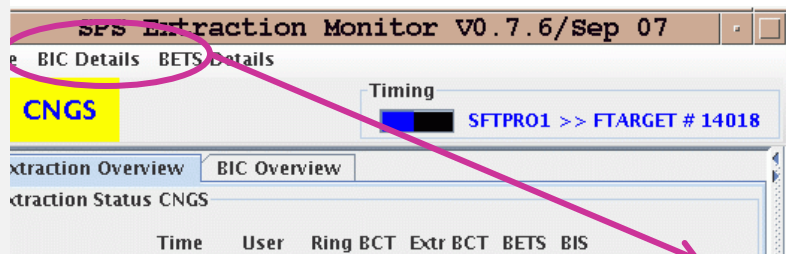
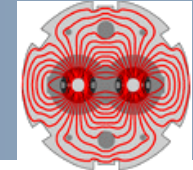
BIC Permit Status SPS-RING

Time	User	Eco	BA1	BA2	BA3	BA4	BA5	BA6
15:13:39	LHCPILOT							
15:13:15	SFTION3							
15:13:07	LHCPILOT							
15:12:43	SFTION3							
15:12:34	LHCPILOT							
15:12:10	SFTION3							
15:12:02	LHCPILOT							
15:11:38	SFTION3							
15:11:30	LHCPILOT							
15:11:06	SFTION3							
15:10:57	LHCPILOT							
15:10:33	SFTION3							
15:10:25	LHCPILOT							
15:10:01	SFTION3							
15:09:52	LHCPILOT							

Interlock Channel List

No active interlocks

Active interlocks



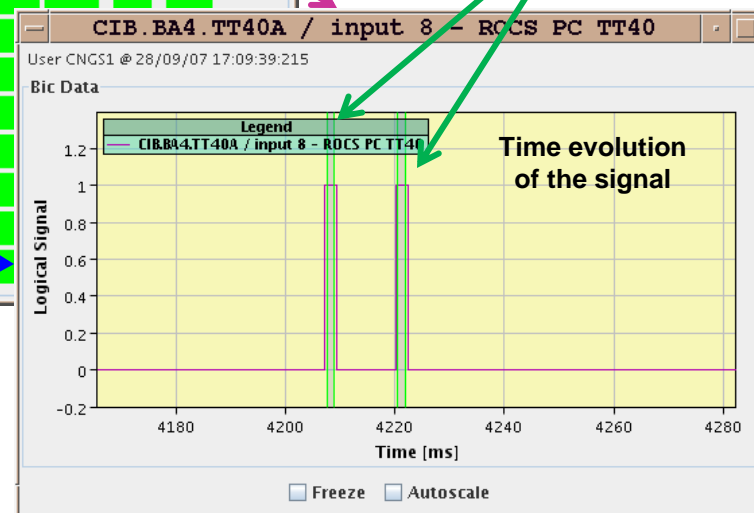
For the extractions the software knows the short windows where the inputs must be *TRUE* to fire the kickers

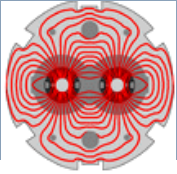
BIC detail

Summary CIB.BA4.TT41B

Time	User	In1	In2	In3	In4	In5	In6	In7	In8	In9	In10	In11	In12	In13	In14	Sw	Out
16:47:59	CNGS1	Green	Green	Blue	Green	Blue	Blue	Blue	Green	Green	Green	Green	Green	Green	Green	Green	Green
16:47:36	CNGS1	Green	Green	Blue	Green	Blue	Blue	Blue	Green	Green	Green	Green	Green	Green	Green	Green	Green
16:47:13	CNGS1	Green	Green	Blue	Green	Blue	Blue	Blue	Green	Green	Green	Green	Green	Green	Green	Green	Green
16:46:50	CNGS1	Green	Green	Blue	Green	Blue	Blue	Blue	Green	Green	Green	Green	Green	Green	Green	Green	Green
16:46:27	CNGS1	Green	Green	Blue	Green	Blue	Blue	Blue	Green	Green	Green	Green	Green	Green	Green	Green	Green
16:46:05	CNGS1	Green	Green	Blue	Green	Blue	Blue	Blue	Green	Green	Green	Green	Green	Green	Green	Green	Green
16:45:42	CNGS1	Green	Green	Blue	Green	Blue	Blue	Blue	Green	Green	Green	Green	Green	Green	Green	Green	Green
16:45:19	CNGS1	Green	Green	Blue	Green	Blue	Blue	Blue	Green	Green	Green	Green	Green	Green	Green	Green	Green
16:44:56	CNGS1	Green	Green	Blue	Green	Blue	Blue	Blue	Green	Green	Green	Green	Green	Green	Green	Green	Green
16:44:33	CNGS1	Green	Green	Blue	Green	Blue	Blue	Blue	Green	Green	Green	Green	Green	Green	Green	Green	Green
16:44:11	CNGS1	Green	Green	Blue	Green	Blue	Blue	Blue	Green	Green	Green	Green	Green	Green	Green	Green	Green
16:43:48	CNGS1	Green	Green	Blue	Green	Blue	Blue	Blue	Green	Green	Green	Green	Green	Green	Green	Green	Green
16:43:25	CNGS1	Green	Green	Blue	Green	Blue	Blue	Blue	Green	Green	Green	Green	Green	Green	Green	Green	Green
16:43:02	CNGS1	Green	Green	Blue	Green	Blue	Blue	Blue	Green	Green	Green	Green	Green	Green	Green	Green	Green
16:42:39	CNGS1	Green	Green	Blue	Green	Blue	Blue	Blue	Green	Green	Green	Green	Green	Green	Green	Green	Green

The green/shadowed regions indicate where the signal must be = 1 (TRUE) to be OK (and to give status = green !)





- ❑ Historically the SPS always had many interlocks in software that were applied (summary) to the beam dump once per cycle.
- ❑ This concept was modernized and better embedded into the control system (JAVA server) with the 2005 renovation.
- ❑ Currently the majority of interlock tests at the SPS are performed in this system – evaluation once per beam cycle (end).
 - *Can handle complex logic involving different system that do not communicate by HW.*
 - *Quick to implement as a fix to cover unexpected issues.*
 - *Anticipation of dangerous configurations of parameters – ‘big brother’.*
- ❑ The same system exists at the LHC, with 2000 tests evaluated in the SIS software layers.