

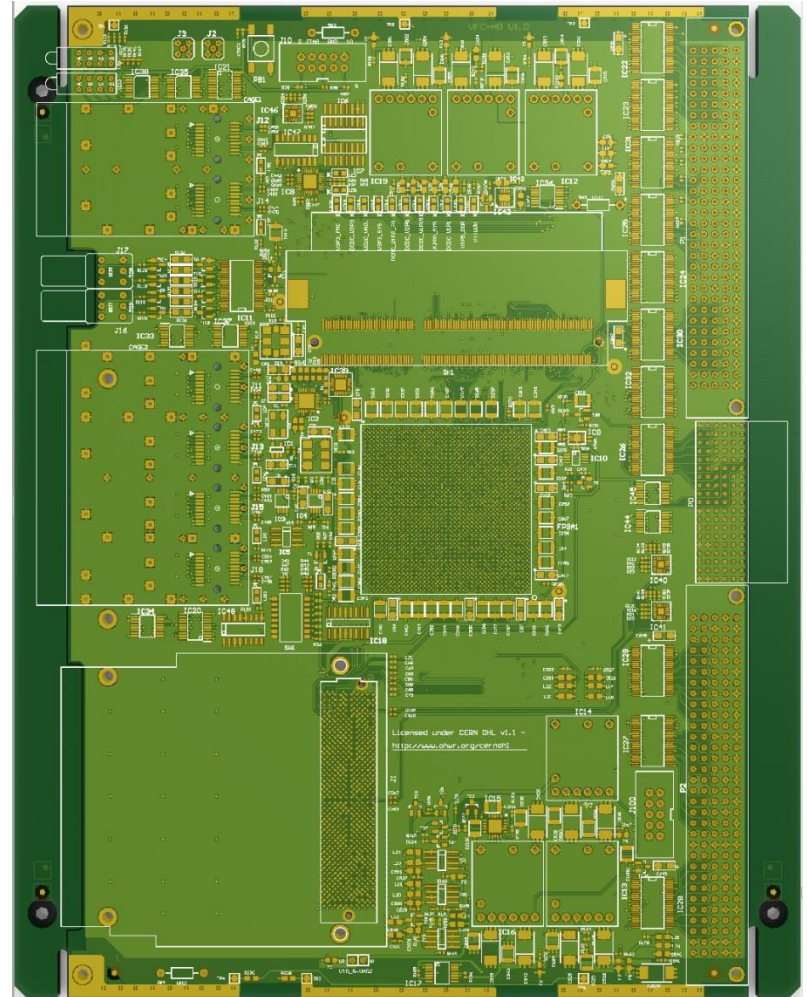
VFC-HD

Status @ 18-02-2015

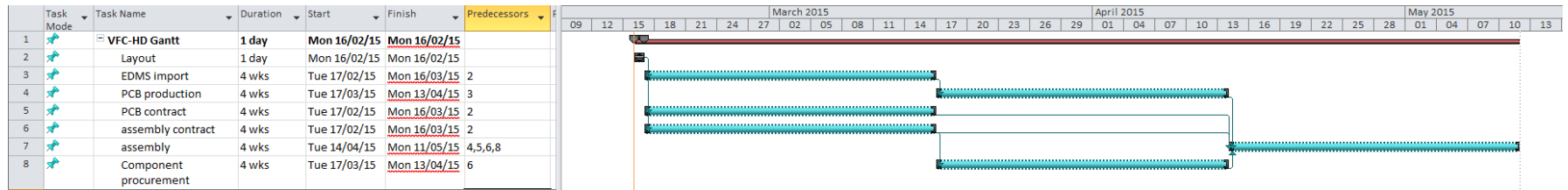
Andrea, Christos, Manoel, Tom, William

Status

- Layout finished
- Import to EDMS will take max 4 weeks
 - Minor changes to the silkscreen
 - Removal of some antennas
- OHWR project created:
<http://www.ohwr.org/projects/vfc-hd/wiki>
- CERN mailing list for users: VFC-HPCusers@cern.ch
- FPGA procurement: Done for a pre-production of up to 21 VFC-HD



Pre-series timeline estimation



- Layout: Norcott => done
- EDMS import: Andrea&B.d'E.=> 4 weeks (16 March)
- Pre-Production contracts: ?? => 4weeks (16 March)
- PCB production: ?? => 4 weeks (13 April)
- Procurements: ?? => 4 weeks (13 April)
- PCB assembly (1st VFC-HD): ?? => 4 weeks (11 May)

Projections...

11th May 1st prototypes at CERN

1st June Validation (?) of the design and call for tender

January 2016 start of the contract for the production

July 2016 Production and test

Needs

ASAP (1st batch): 13 VFC-HD (4 for QP, 4 for BL, 3 for PI, 2 for PM)

Later in the year: **45+??** more VFC-HD (6 for MIM by June and 5 for MOPOS, 10 for AWAKE and 4 for FBCT by December, 20 for Motor control by February 2016)

What next....

1. Organization of the development of a automated test bench (student request?) – inside QP
2. Development of the FW infrastructure and support for developers – inside QP
3. How many PCBs we produce and how many we assemble for series and prototypes (**series will only come mid 2016**)
4. To be decided who prepares the contracts with finance
5. To be decided who takes care of the production and test