

Speeding-up Large-Scale Storage with Non-Volatile Memory

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about DSI



vision

Founded in 1992, DSI' vision is to be a vital node in a global community of knowledge generation and innovation, nurturing research talents and capabilities for world class R&D in next generation technologies.

mission

To establish Singapore as an R&D center of excellence in data storage technologies.



Core Competencies

HARD DISK DRIVE TECHNOLOGIES

NON-VOLATILE MEMORIES

DATA CENTER
TECHNOLOGIES

ADVANCED CONCEPT & NANOFABRICATION TECHNOLOGIES



- 10Tb/in² areal density technologies
- Thin Hybrid HDD (0.5TB 2.5", 5mm, hybrid HDD)
- STT-MRAM
- ReRAM
- Signal Processing & Error Correction
- IC Design
- NVM System
- Active Hybrid Storage System
- Big Data Analytics Platform
- Data & Storage Security
- Nanofabrication
- Spintronics
- Plasmonics
- Photo-Electronics
- Metamaterials and Small Particle Physics Research

Massive Data Key Challenge for Data Center

Performance

Scalability

Security

Energy Consumption

Space

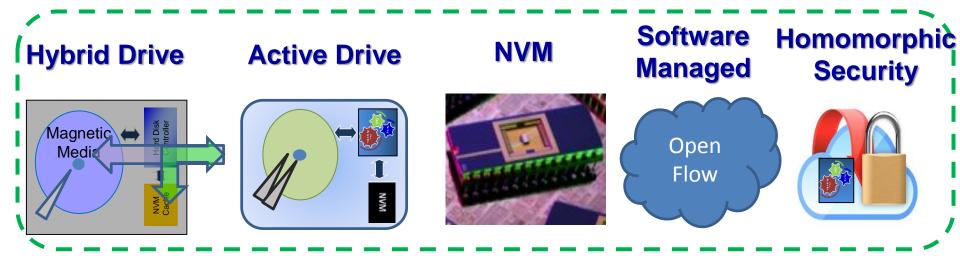
Manageability

- CAPEX cost for additional IT equipment - servers, networks and storage
- Driving the energy costs
- Larger footprint and space required
- Increasingly challenging and costly to scale and deliver performance
- Increasing complexity in operating and managing the data center
- Providing data protection and security for massive amount of data



Future Data Center Architecture with Emerging Technologies

Integration of



Performance, scalable, secured, energy and cost efficient



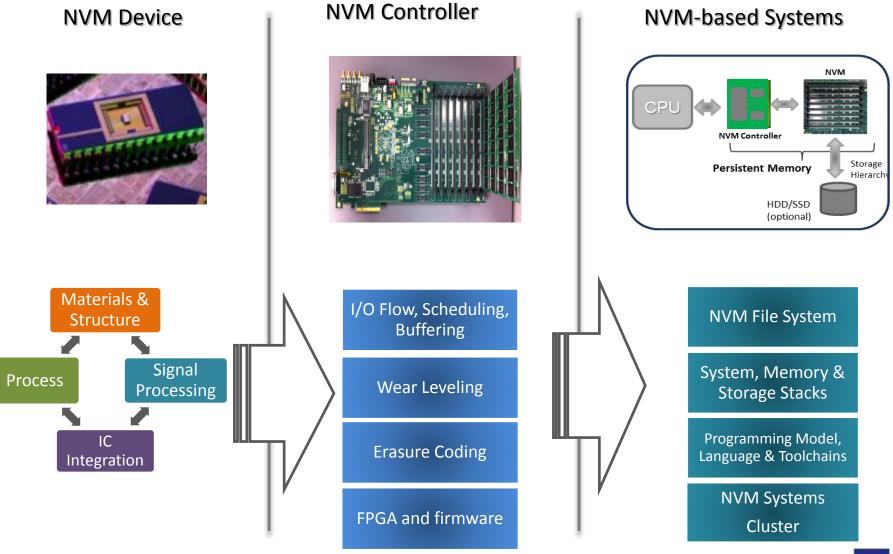
Next Generation Non Volatile Memory (NVM)

Characteristics of next generation NVM:

- + high speed ~ DRAM like
- + data persistent against power loss
- + byte-addressable (vs 4KB- 512KB blocks)
- + endurance (~DRAM like) >>> Flash
- + no refresh cycles/energy

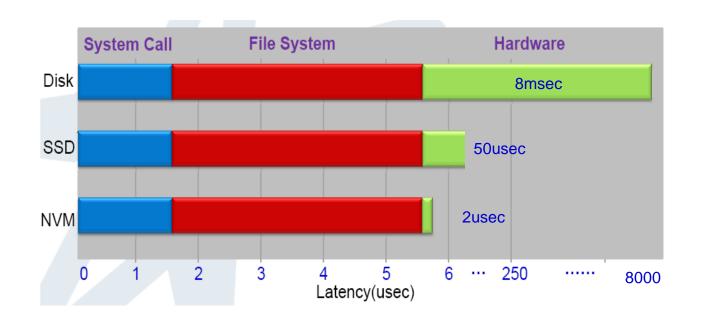
Technology	Read	Write	Endurance Cycle	Read (V)	Write (V)	Maturity
HDD (15KRPM)	6000µs	6000µs	NA	5V, 12V	5V,12V	Product
SLC Flash	25µs	200µs/1.5ms (Program/Erase)	10 ⁵ (1000x for MLC)	2	15	Product
DRAM	<10ns	<10ns	10 ¹⁶	1.8	2.5	Product
STT-MRAM	2-20ns	2-20ns	10 ¹⁵	0.7	+1	Advanced Development

NVM Research in DSI: Device to System





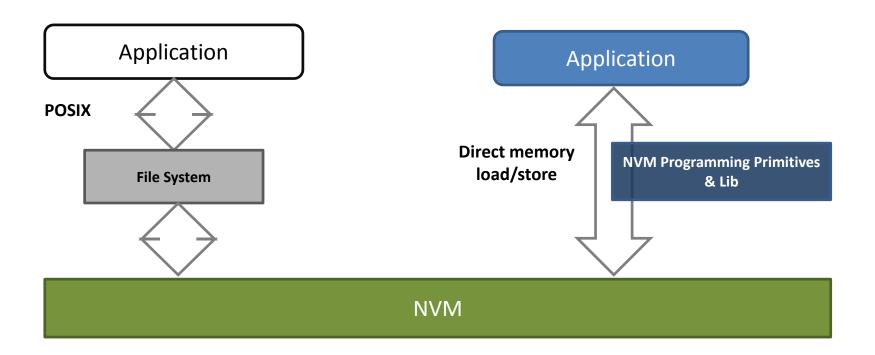
Next Generation Non-Volatile Memory



To fully exploit its performance, the hardware architecture and OS stacks including programming model — applications, languages, compilers/VMs, run-time libraries, middleware,... — must change



NVM Software Programming Model

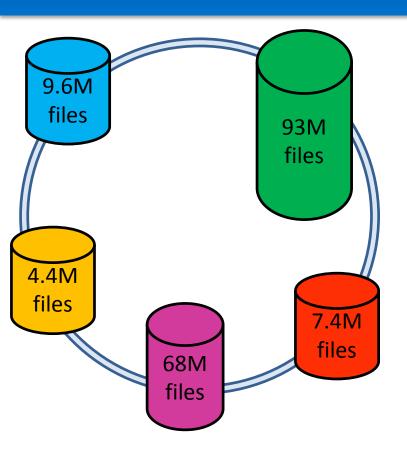


New programming model for NVM provides data persistence integrated into the application programs:

- Byte-addressable
- Load/Storage access without demand paging
- Memory performance



CERN EOS NameSpace

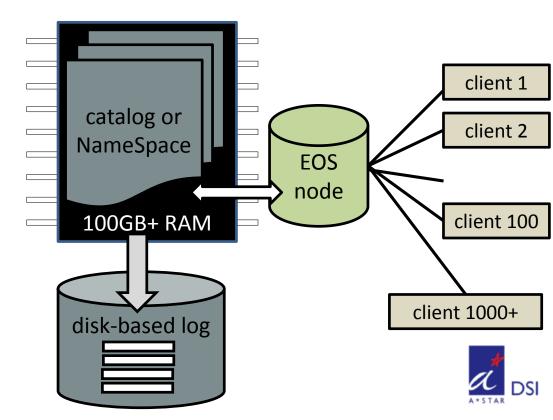


50+ PB experimental data in 150M+ files across 5 experiments (nodes): ATLAS, CMS, LHCB, ALICE...

Node availability critical for the continued operation of thousands of clients

Metadata operations (create, rename, move, delete etc.) are sped-up by in-memory NameSpace, with a growing RAM footprint of 100+ GBs

Disk-based logs enable consistent reconstruction of NameSpace to recover after any hw & sw faults

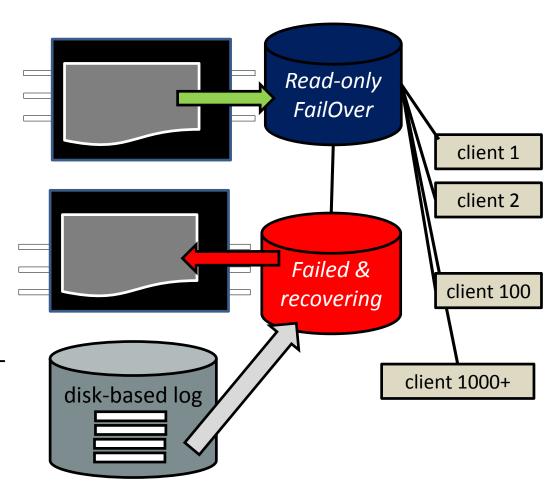


Challenges: Availability and Consistency

One of the challenges is the consistent journaling of metadata updates between memory and disk logs; but also across failures of the NS service, the hardware or power.

Reconstructing a 100GB+ Catalog can take even 10 minutes, disrupting client's work.

Reconstruction is not IO-bound but CPU-bound because data structures trade-off *lookup* speed against *insert* speed.





Proposed Solution: EOS Catalog in Non-Volatile Memory

Store the instance of the EOS Catalog in Non-Volatile Memory. NVM-based Catalog is persistent, fault-tolerant, and always consistent. No more slow reconstructions from logs

