

HV-CMOS device fabrication

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There are several on-going device submissions in HV-CMOS technology from groups working on ATLAS pixel and strip systems. The main objective of these submissions is device evaluation as a possible alternative to the sensor modules in pixels and strips. Along with functional evaluation of these monolithic devices, the basic questions regarding sensor radiation hardness need to be addressed for the relevant fluences. Some RD50 member groups already started working on this topic. The new submissions give the option of devices fabricated on intermediate resistivity wafers. We will present the test structure options and test possibilities that are available.

Authors: CASSE, Gianluigi (University of Liverpool (GB)); FADEYEV, Vitaliy (University of California, Santa Cruz (US))

Presenter: CASSE, Gianluigi (University of Liverpool (GB))

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