



# AIDA-2020 Kick-off meeting

## Thursday, June 4, 2015

### WP4 - 503/1-001 - Council Chamber (2:00 PM - 6:00 PM)

-Conveners: Valerio Re; Valerio Re; Christophe De La Taille

time	[id] title	presenter
2:00 P	[M5] Introduction	DE LA TAILLE, Christophe RE, Valerio
2:20 P	[M6] CERN (65 nm CMOS)	CAMPBELL, Michael
2:30 P	[M7] INFN contribution to WP4 (65 nm and interconnections)	RE, Valerio
2:40 P	[M8] INFN-Milano (65 nm CMOS)	LIBERALI, Valentino
2:50 P	[M9] CPPM (65 nm and interconnections)	ROZANOV, Alexandre
3:00 P	[M0] AGH (65 nm and SiGe)	DE LA TAILLE, Christophe IDZIK, Marek RE, Valerio
3:10 P	[M1] Bonn (65 nm CMOS and interconnections)	HUEGGING, Fabian HUEGGING, Fabian
3:20 P	[M2] OMEGA (SiGe)	MARTIN CHASSARD, Gisele
3:30 P	[M4] IPNL (SiGe)	MATHEZ, Herve
3:40 P	[M37] DESY & UHeidelberg	SEFKOW, Felix
3:50 P	[M5] Coffee break	
4:10 P	[M6] INFN Genova (interconnections)	DARBO, Nanni
4:20 P	[M7] LAL (interconnections)	LOUNIS, Abdenour PERNEGGER, Heinz
4:30 P	[M8] Uppsala (interconnections)	BRENNER, Richard
4:40 P	[M9] Glasgow (interconnections)	BUTTAR, Craig BATES, Richard BATES, Richard
4:50 P	[M00] HLL-MPP (interconnections)	ANDRICEK, Laci RE, Valerio
5:00 P	[M01] Discussion: WP4 milestones and deliverables	