



# HEPiX Fall 2015 Workshop

## Thursday 15 October 2015

**Computing and Batch Services - Bldg. 510 - Physics Department Large Seminar Room (16:30 - 17:30)**

time	[id] title	presenter
16:30	[10] NVM Express (NVMe): An Overview and Performance Study	HOLLOWELL, Christopher
16:50	[26] Status of DESY Batch Infrastructures	FINNERN, Thomas
17:10	[75] Update on benchmarking	MEINHARD, Helge MICHELOTTO, Michele

# Friday 16 October 2015

## Computing and Batch Services - Bldg. 510 - Physics Department Large Seminar Room (09:00 - 10:20)

time	[id] title	presenter
09:00	[19] An Easy HTCondor Configuration to Support all Workloads (for some definition of all)	STRECKER-KELLOGG, William Edward STRECKER-KELLOGG, William
09:20	[72] Upgrade to UGE 8.2 : Positives effects at IN2P3-CC	HAMAR, Vanessa
09:40	[60] HTCondor Recent Enhancement and Future Directions	TANNENBAUM, Todd
10:00	[11] Non-traditional workloads at the RACF	STRECKER-KELLOGG, William Edward STRECKER-KELLOGG, William

## Computing and Batch Services - Bldg. 510 - Physics Department Large Seminar Room (10:50 - 11:10)

time	[id] title	presenter
10:50	[52] Future of Batch Processing at CERN	BELLEMAN, Jerome