

CRU Weekly Meetings

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Recent News

1. GBT-FPGA Altera Arria 10 version

- LHCb shared their preliminary Arria 10 version

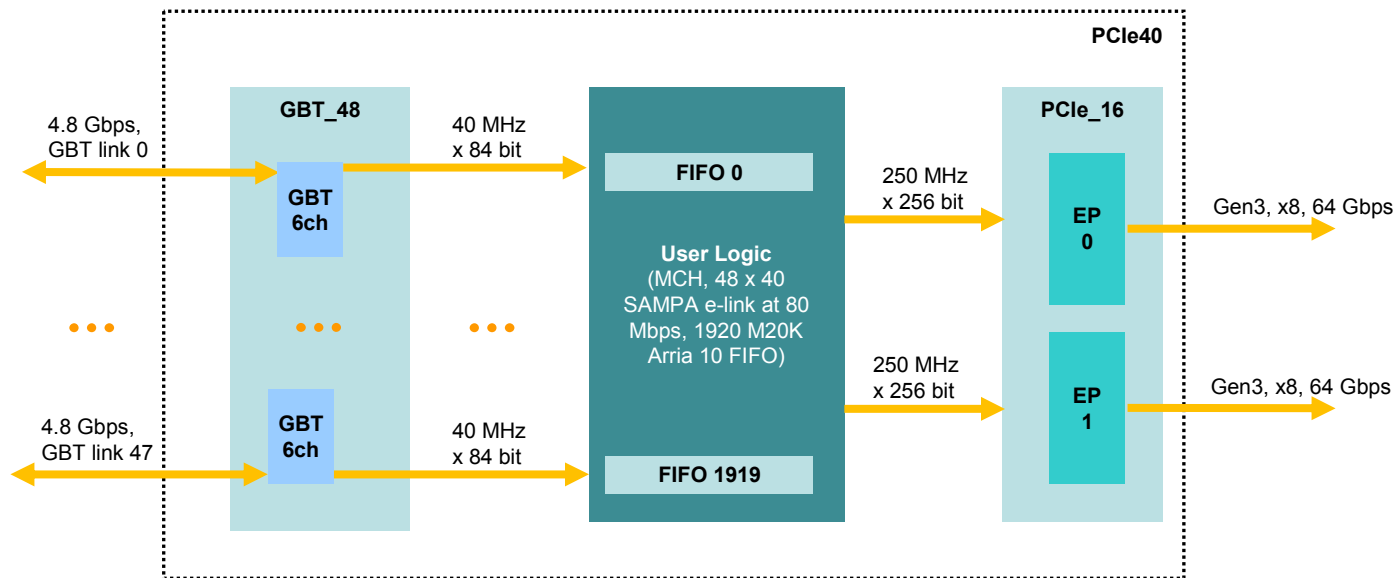
2. MiniDAQ (AMC40 + credit card PC)

- Yesterday we received the first MiniDAQ setup from Marseille (it is located in Wigner/Budapest)

3. Twiki

- We started to populate the ALICE->CRU Twiki topic
- <https://twiki.cern.ch/twiki/bin/viewauth/ALICE/CRU>
- It is in progress, please use WikiWords beginning with Cru for topic names (like CruSomething) to avoid conflicts with already existing pages

Arria 10 Complexity Challenge – First approach



- Currently the three modules tested separately:

- GBT_48 – based on the LHCb design, it is able to P&R
- User Logic – just 1920 FIFO (single M20K), it is able to P&R
- PCIe_16 – based on the LHCb design, not able to P&R (TX[8] at wrong pin, needs more investigation)

- Next steps:

- Functional improvements (std. / lat. opt. GBT, more realistic user logic, working PCIe, ...)
- Integration

Simulation of internal CRU logic for MCH (without transceivers)

