



FRIB

Facility for Rare Isotope Beam (FRIB) PLC Control Systems Introduction

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U.S. DEPARTMENT OF
ENERGY

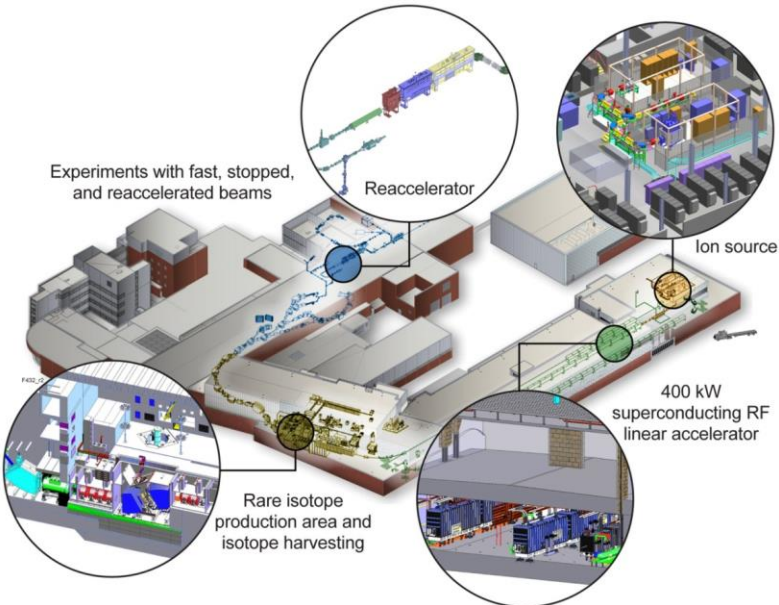
Office of
Science

Outline

- FRIB project introduced
- PLC scope and application defined
- PLC Architecture defined
- Control Engineering Process defined
- Management of change workflow defined

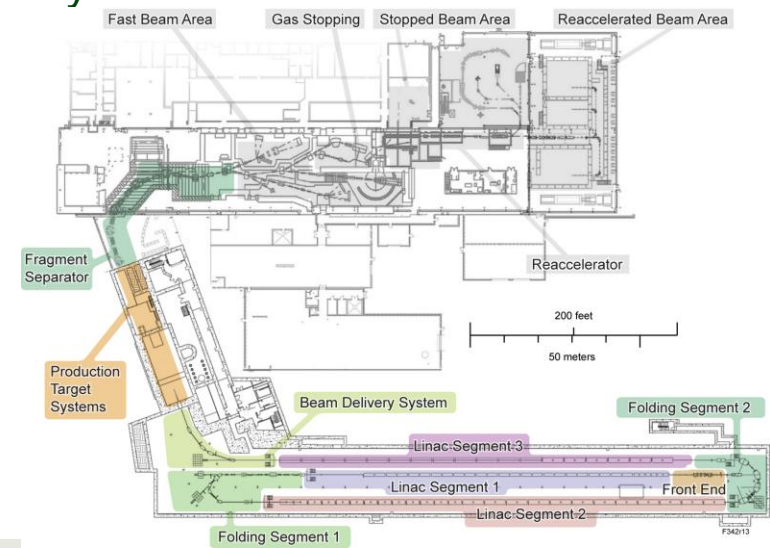


FRIB Introduction



- The FRIB, is a new heavy ion accelerator facility currently under construction at Michigan State University
 - Replaces existing coupled cyclotrons in the National Superconducting Cyclotron Lab
- It is being built to provide intense beams of rare isotopes
- Funded by DOE–SC Office of Nuclear Physics with contributions and cost share from Michigan State University

- Serving over 1,400 users
- Key feature is 400 kW beam power for all ions, from H to ^{238}U with energies of no less than 200 MeV/u
- Provides separation of rare isotopes in-flight
 - Supplying fast, stopped, and reaccelerated beams to experimenters
- Scheduled completion, 2022



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FRIB Low Level Controls Scope and Application

- Based on EPICS and industrial PLC control systems
 - (Items in green are within the scope of Low Level Controls)

- FRIB will have ~50 ControlLogix PLCs
 - NSCL has a similar number with ~12K I/O points
 - Includes control of Cryoplant process, cryodistribution and beamline systems
- PLC Controls used for hardwired control signals, interlocks and analog read and process controls

Operator stations: Displays

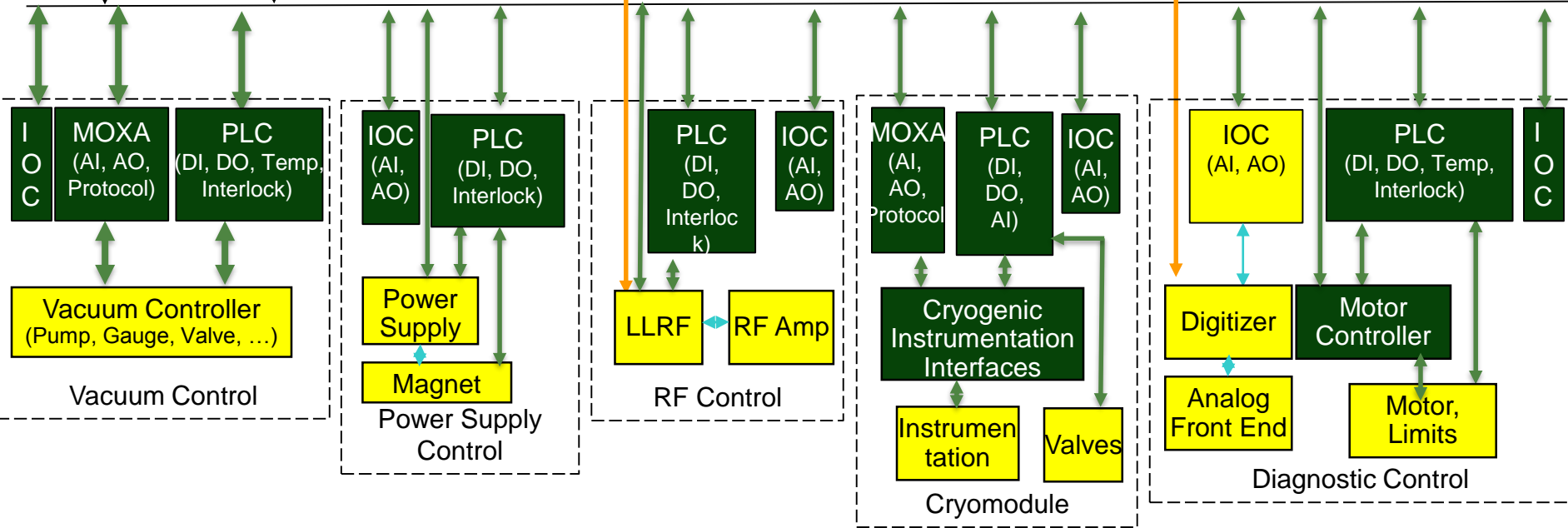


Archiving, Alarm Management, Strip charts, Save/Restore Utility



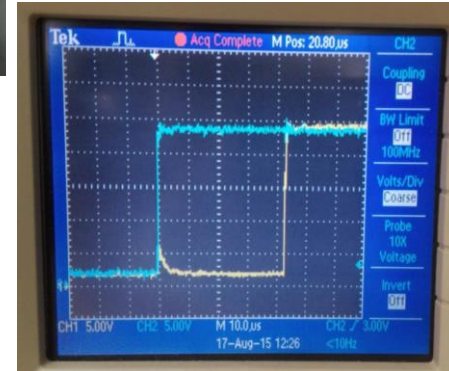
Fiber Timing Network

Ethernet – EPICS Channel Access, Precision Time Protocol (PTP)/Network Time Protocol (NTP) Timing



FRIB PLC Architecture Defined [1]

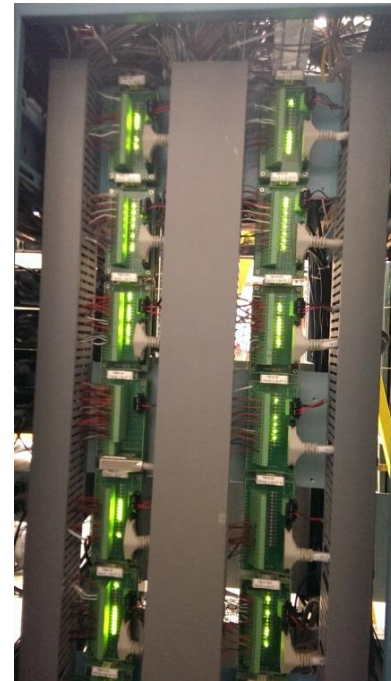
- Allen Bradley (AB) ControlLogix PLC
 - Large installed base in local industry
 - Already deployed and established as standard for NSCL projects since 2004
 - Used at several other National labs
 - Existing in-house expertise
 - Excellent local support from manufacturer
- Provides the direct interface to the process devices via modular I/O
 - Interfaces with discrete logic signals via 24 VDC
 - Interfaces with analog signals via 4..20 mA current loops or +/- 10 VDC
 - Other signaling levels can be translated through appropriate signal conditioning
- Remote I/O
 - Ethernet connected chassis house I/O modules for host controllers
 - Use of off-chassis I/O transparent to the programmer
 - Programmable failure modes for loss-of-communication faults
- Use AB AssetCentre for version control
 - Nightly compares verifies no unauthorized changes



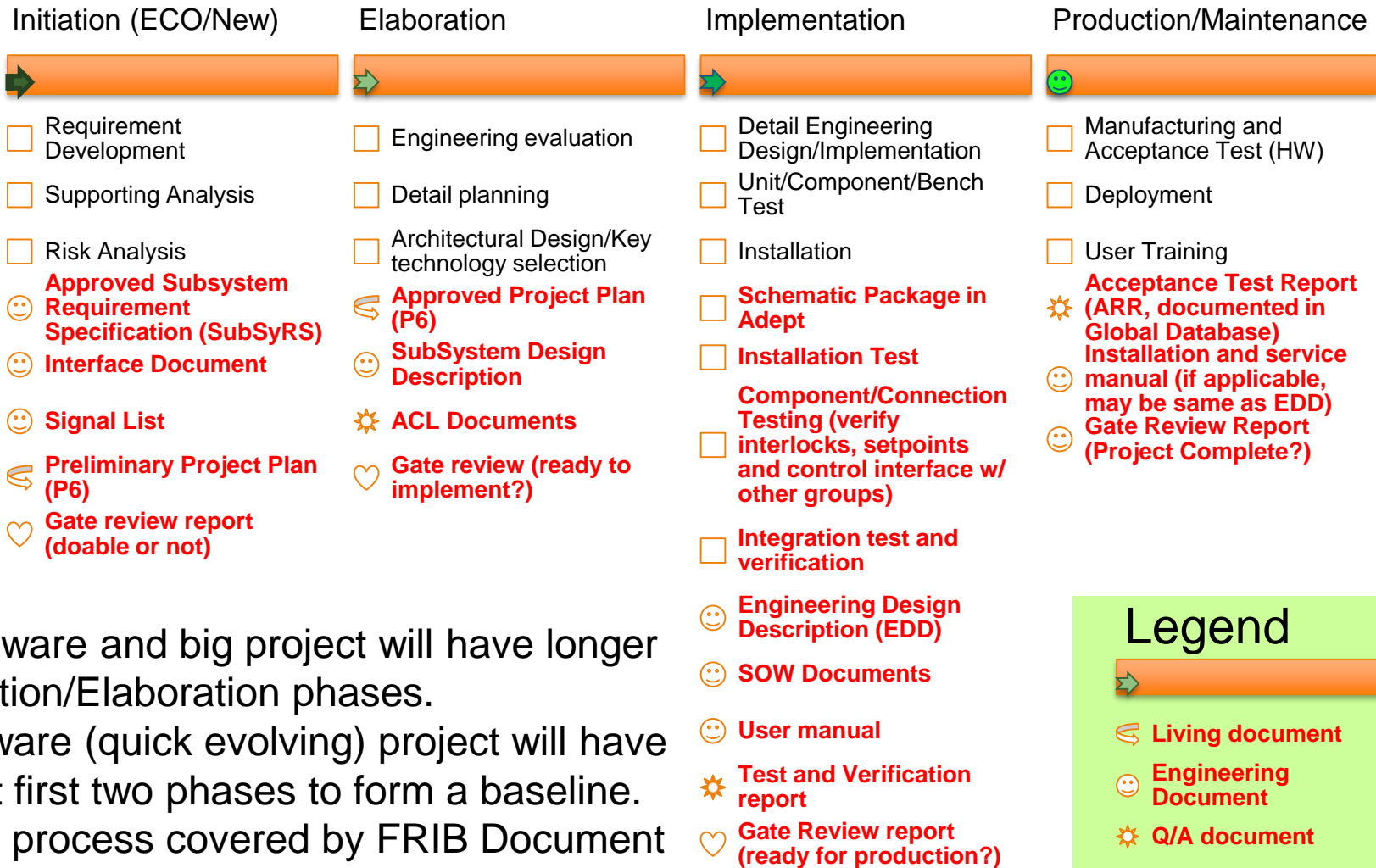
- Baseline I/O Modules
 - Analog Input – 1756-IF16
 - Analog Output – 1756-OF8
 - Digital Input – 1756-IB16IF
 - Digital Output – 1756-OB16IEF
 - RTD Input – 1756-IRT8I
- Using High Speed Digital I/O modules
 - Lower Cost than diagnostic modules
 - Modules can place timestamps on data
 - Ability for higher speed reaction to inputs with bypassing CPU ($\leq 150 \mu S$, Tested at $40-60 \mu S$)

PLC Architecture Defined [2]

- Interface Modules (IFMs) will be used as the connection point between the 1756 ControlLogix PLC Input and Output modules and signals from devices in the field
 - Designed to interface with specific Allen Bradley I/O modules and pre-wired terminal blocks
 - » Reduces cabinet installation time from ~40 hours to 10 hours
 - » Reduces wiring errors
 - » Reduces cost vs commercial option
 - » Provides 24 V or Com for every signal (Module Type Dependent)
 - » Provides extra 24 V, Com Connections
 - One module for each IO type
 - » Analog Input
 - » Analog Output
 - » Digital Input
 - » Digital Output
 - Uniform design across PLC systems for simplified spare inventory
 - Uses commercial pre-wired cables from I/O module to terminal blocks




FRIB Controls Engineering Workflow, SOP



Hardware and big project will have longer Initiation/Elaboration phases.
 Software (quick evolving) project will have short first two phases to form a baseline.
 ECO process covered by FRIB Document

Legend

- 
- 🔄 **Living document**
- ☺ **Engineering Document**
- ☀️ **Q/A document**
- ♡ **Gate review**



Controls Engineering Deliverables

Final designs include:

- Interface, requirements, design description documents
- Signal lists, interlock, and testing validation documents, support manual
- Unique schematics
- BOM
- Rack layouts

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FRIB Cryoprotect Control Design Description

FRIB-T30203-TD-000657-RD01 Page 1 of 10
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FRIB Cryoprotect Controls Design Description

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Prepared by: 9/23/2014
Reviewed by: 9/23/2014

Created by: 9/23/2014
Signed by: 9/23/2014

Reviewed by: 9/23/2014
Signed by: 9/23/2014

Concurred by: 9/23/2014
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Facility for Rare Isotope Beams
FRIB Cryogenic Distribution Control Design Description

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FRIB Cryogenic Distribution Controls Design Description

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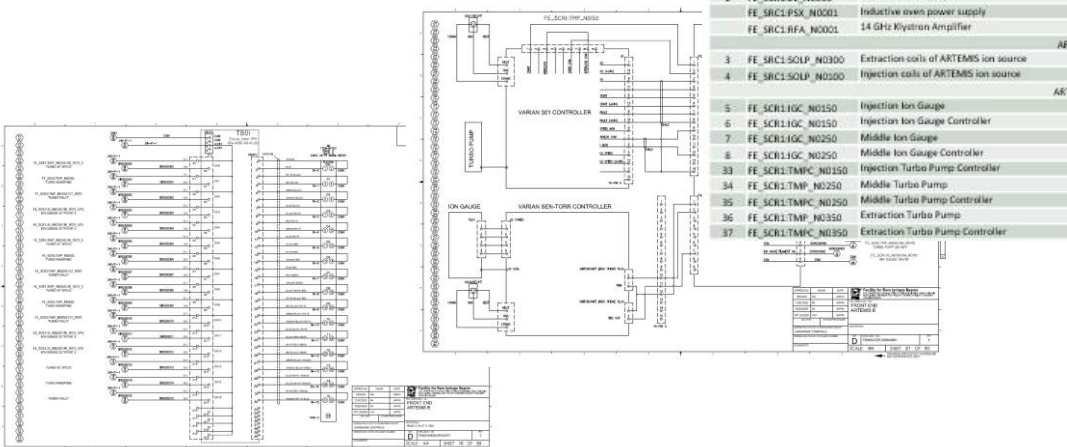
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ARTEMIS Ion Source and High Voltage Platform Signal List

P SID	FRIB Name	Device Description	Device Type	AI	AD	BI	DO
		ARTEMIS High Voltage (Earth Ground)					
	FE_SCR1.EL_N0000	Platform HV	P100W/20mA	2	1	1	2
		ARTEMIS High Voltage (1000V Platform)					
1	FE_SCR1.EL_N0100	High voltage power supply	H40W/125mA Glassman - EW40P15	2	1	1	2
2	FE_SCR1.EL_N0300	Puller Power Supply	N10W/7.0mA Glassman - FC10R12	2	1	1	2
	FE_SCR1.PSX_N0001	Inductive oven power supply					
	FE_SCR1.RFA_N0001	14 GHz Klystron Amplifier					



Component	Part Description	Part Number	Manufacturer	Vendor	Quantity
PLC Controller and I/O	1608-XLE24EN	1608-XLE24EN	Allen-Bradley	Kendall Electric	6
	85-265 VAC Power Supply (5W @ 10 Amps)	1756-PA27	Allen-Bradley	Kendall Electric	11
	Ethernet Module	1756-EN2TR	Allen-Bradley	Kendall Electric	12
	Logix5572 Processor With 4 Mbytes Memory	1756-L72	Allen-Bradley	Kendall Electric	1
	10 Slot Controller Chassis	1756A10	Allen-Bradley	Kendall Electric	11
	Analog Input - Current/Voltage 16 PTS (+30 Pin TB)	1756-F16	Allen-Bradley	Kendall Electric	5
	Analog Output - Current/Voltage 16 PTS (+20 Pin TB)	1756-OF8	Allen-Bradley	Kendall Electric	0
	Analog Input RTD Module	1756-RT81	Allen-Bradley	Kendall Electric	8
	10-30 VDC Diagnostic Input 16 PTS (+30 Pin TB)	1756-816D	Allen-Bradley	Kendall Electric	24
	19-30 VDC Diagnostic Output 16 PTS (+30 Pin TB)	1756-0B16D	Allen-Bradley	Kendall Electric	24
Rack & Panel Hardware	Pre-wired cable for Analog Input w/ RTB and 25-Pin DB Connector	1492-ACAB1E0298A	Allen-Bradley	Kendall Electric	5
	Pre-wired cable for Analog Output w/ RTB and 25-Pin DB Connector	1492-ACAB1E0298B	Allen-Bradley	Kendall Electric	0
	Pre-wired cable for Digital output w/ RTB and 40-pin connector	1492-CAB1E0297	Allen-Bradley	Kendall Electric	48
	Analog Input Interface Module	054-0033-01-01-00	MSU Design	Advanced Circuits	5
	Analog Output Interface Module	054-0034-01-01-00	MSU Design	Advanced Circuits	0
	Digital Input Interface Module	054-0035-01-01-00	MSU Design	Advanced Circuits	24
	Digital Output Interface Module	054-0036-01-01-00	MSU Design	Advanced Circuits	24
	AC Breakers - 6A	1492SP1C080	Allen-Bradley	Kendall Electric	12
	Breakers (Non Motor applications) 3A	1492SP1C030	Allen-Bradley	Kendall Electric	43
	SU Brackets for 10-slot PLC Backplane	054-0031-02-01-00	MSU Design	Electrolab	5



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FRIB Work Control Procedures

- Work control procedures for control changes are documented
 - Changes are to follow both software configuration management plan, or Engineering Change order process, FRIB/NSCL PLC Change Control Procedure and work control plan
 - Communication with System Owner defined and required
 - » Role of System Owner defined
 - » Area Manager identifies System Owner for the work
 - » Discussion on the risk with System Owner required
 - » Ownership handover procedure with System Owner defined
 - Peer review required before installation
 - Adequate validation after the work
- Developing online change control request system to get system/area managers approval ahead of work planning
- Access restrictions at various level are in place
 - Physical access granted only to those with appropriate site training
 - Work after hours/weekends on FRIB site must be pre-approved
 - Access to FRIB Controls and Test Network limited to personnel with business need and appropriate qualifications
 - Access to PLC and GIT repositories are also restricted to appropriate personnel



Summary

- PLCs have a large scope and application within FRIB
- PLC Architecture has been determined, made use of time saving designs and has been a very stable platform
- Control Engineering Process defined
- Management of change workflow is defined and undergoing improvements

Backup Slides



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K. Davidson, ICALEPCS 2017 Workshop: PLC Based
Control Systems, FRIB PLC Controls Introduction, Slide 11