Implementing a ReboT Server on a MicroBlaze.

G. Varghese, L. Butkowski, R. Rybaniec, M. Killenberg, N. Shehzad (DESY, Germany) A. Dworzanski, K. Czuba (Warsaw University of Technology, Poland)



Motivation

TMCB integration for ChimeraTK DeviceAccess

TMCB: Temperature Monitoring and Control Board

- > Components:
 - ADCs, DACs
 - 10/100 Base-T Ethernet interface
 - Xilinx Spartan 6 FPGA with MicroBlaze core, AXI Ethernet Lite MAC
- > ADC and board data exposed through VHDL Register Space on FPGA



DeviceAccess mapfile with register descriptions

- Write to registers can trigger actions on hardware, e.g. write to a relevant register sets DAC voltage
- End user requires remote access to VHDL Register Space
- Need support for single word read/write on Register Space for board control
- Support read of large data blocks from Register Space

ReboT Protocol: Register Based Access Over TCP

- ReboT: A custom TCP payload format
- Client with ReboT support and register description, has access to hardware Register Space, e.g. ChimeraTK DeviceAccess library with map file.



Performance Measurements on the Implemented Server



ReboT read of one word register (4bytes) $1.12 \pm .014$ msReboT write of one word register $1.15 \pm .038$ msReboT read 4096 byte long data $5.55 \pm .039$ ms

packets, hence have increased header over heads. Payload throughput for block transfers level off at 900 Kbytes/s.

Achieved numbers were sufficient for our current requirements



20th IEEE Real Time Conference Padua, Italy, 2016

Presenter: Geogin Varghese

DESY 22607 Hamburg, Germany geogin.varghese@desy.de



