

Analog Data Acquisition and Processing FPGA-based Solutions Integrated in Area Detector using FlexRIO technology

R. Herrero¹, A. Carpeño¹, S. Esquembri¹, and M. Ruiz¹

¹*Instrumentation and Applied Acoustics Research Group. Technical University of Madrid, Madrid, Spain. Email: ricardo.herrero@i2a2.upm.es*



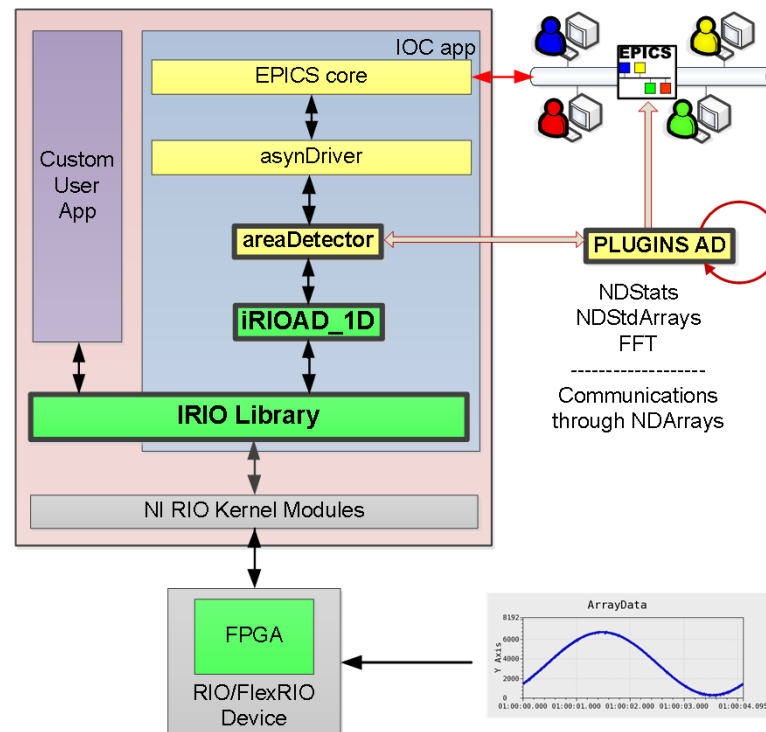
POLITÉCNICA



INSTRUMENTATION &
APPLIED ACOUSTICS
RESEARCH GROUP

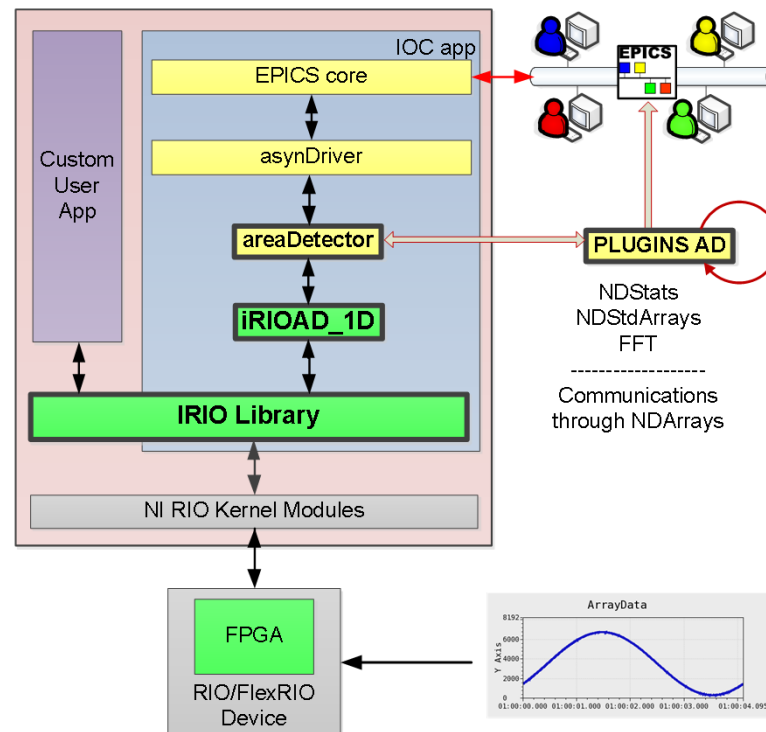
Integrate FPGA-based DAQs in EPICS using areaDetector and IRIO:

- FPGA = Preprocessing capabilities, deterministic behavior
- EPICS = Widely spread solution for distributed control systems
- areaDetector = Initiative to standardize EPICS drivers interface
- IRIO = One driver, multiple hardware implementations



Advantages:

- IRIO FPGA Templates: Ready-to-use templates. Easily customizable.
- Object-Oriented Implementation: Control your custom DAQ extending provided base classes. Core functionality already implemented.
- ADPlugins: On-the-fly reconfiguration of software processing chain



1-D areaDetector for FPGA-DAQs



POLITÉCNICA



INSTRUMENTATION &
APPLIED ACOUSTICS
RESEARCH GROUP