A new preprocessing and control board for the phase 2 electronics of AGATA experiment

J. Collado, J. M. Blasco, N. Dosme, V. González, Senior Member, IEEE, X. Grave, N. Karkour, X. Lafay, E. Legay, D. Linget, E. Sanchis, Senior Member, IEEE.
Objectives

1. Increase the number of crystals processed per electronic device with associated electronic reduction.
2. Improve the computing and readout network.
3. Design compatibility and independence for previous electronics and future developments must be kept.

PACE Process and Control over Ethernet system

STARE Serial Transfer Acquisition over Ethernet