

20th Real Time Conference

Thursday, June 9, 2016

Mini Oral 3 - Centro Congressi (2:15 PM - 3:20 PM)

-Conveners: Jinyuan Wu; Pierre-Andre Amaudruz

time	[id] title	presenter
2:15 PM	[324] 051 Design of a 10-Gbps Random Number Recorder	WANG, Xinzhe
2:17 PM	[330] 016 10-Gbps True Random Number Generator Accomplished in ASIC	WANG, Xinzhe
2:19 PM	[326] 195 20-Channel_14MeV_Neutron_Detector_Electronics_Readout_System	LIU, Shengquan
2:21 PM	[327] 097 Real-time vertical plasma position control using the heavy ion beam diagnostic	HENRIQUES, Rafael
2:23 PM	[328] 199 Data acquisition system for the J-PARC E36 experiment	Dr IGARASHI, Youichi
2:25 PM	[329] 034 A Reconfigurable Ethernet-Based Data Acquisition and Processing System for Particle Physics Experiments	Mr WANG, Zhaoqi
2:27 PM	[325] 165 A cosmic ray readout system for qualifications of small-strip Thin Gap Chambers of the ATLAS Muon Spectrometer Phase-I upgrade	WANG, Xu
2:29 PM	[331] 032 High Counting-rate Data Acquisition System for the Applications of PGNA	LIU, yuzhe
2:31 PM	[332] 001 An Energy Measurement Method of High-frequency Narrow Laser Pulse based on FPGA	WANG, Jian
2:33 PM	[333] 003 Design of ultra-low noise power system for high-precision detectors	WANG, Jian
2:35 PM	[334] 002 A DAQ Prototype for the ATLAS small-strip Thin Gap Chamber Phase-I Trigger Upgrade	HU, Kun
2:37 PM	[335] 143 Intelligent FPGA Data Acquisition Framework	LEVIT, Dmytro
2:39 PM	[336] 221 Multi-Channel FPGA-Based Data-Acquisition-System for Time-Resolved Single-Photon Counting in Synchrotron Radiation Experiments.	ZIOLKOWSKI, Michael
2:41 PM	[337] 204 Portable calibration node for LHAASO KM2A detector array	LI, hongming
2:43 PM	[338] 013 Preliminary Performance of a Continuous Crystal PET Detector with TODT Readout Scheme	XIAO, Yong
2:45 PM	[339] 260 Analog Data Acquisition and Processing FPGA-based Solutions Integrated in AreaDetector using FlexRIO technology	ESQUEMBRI MARTÍNEZ, Sergio
2:47 PM	[340] 068 Complete Parallel Readout VME DAQ System	BABA, Hidetada
2:49 PM	[341] 159 Intelligent FPGA based Event Builder and Data Acquisition System for the COMPASS experiment.	KONOROV, Igor
2:51 PM	[342] 056 mbspex and pexornet - linux device drivers for PCIe optical receiver data acquisition and contro	ADAMCZEWSKI-MUSCH, Joern
2:53 PM	[343] 206 An Ultra-fast Linear Array Detector for MHz Line Repetition Rate Spectroscopy	CASELLE, Michele

2:55 PM	[344] 047 ELECTRONICS FOR HARPO - Design, development and validation of electronics for a high performance polarized γ -ray detector.	Mr GEEREBAERT, Yannick
2:57 PM	[345] 073 The Implementation of KTX Central Control System	ZHANG, Zuchao
2:59 PM	[346] 026 A readout system for the Ground Tests of the DAMPE BGO Calorimeter	ZHANG, Junbin
3:01 PM	[347] 269 Development of a high dynamic range and wide bandwidth amplifier electronics system	LI, Cheng
3:03 PM	[348] 009 Two-dimensional encoded multiplexing readout with a 5x5cm ² THGEM	YUAN, guangyuan
3:05 PM	[349] 042 PLAS: A 32-channel, dead time-less analog memory ASIC for the TRACE detector	ALIAGA, Ramon J.
3:07 PM	[350] 249 New updates on the ATLAS ROD board implementation for Pixel Layer 1 and Layer 2	GIANGIACOMI, Nico
3:09 PM	[361] 227 The laser control system for a calibration facility of light detector	MASTROIANNI, Stefano
3:11 PM	[362] 228 The monitoring board for the calibration system of the g-2 experiment	MASTROIANNI, Stefano