

20th Real Time Conference

Friday, June 10, 2016

Poster Session 2 - Centro Congressi (10:30 AM - 12:30 PM)

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[260] Analog Data Acquisition and Processing FPGA-based Solutions Integrated in AreaDetector using FlexRIO technology	Mr HERRERO, Ricardo	
[88] Distributed and parallel real-time control system equipped FPGA-Zynq and EPICS middleware	LEE, Sangil	
[243] The data acquisition system of the DSSD detector for SIRIUS	Mr CHAMINADE, Thomas	
[165] A cosmic ray readout system for qualifications of small-strip Thin Gap Chambers of the ATLAS Muon Spectrometer Phase-I upgrade	Mr WANG, Xu	
[47] ELECTRONICS FOR HARPO - Design, development and validation of electronics for a high performance polarized gamma-ray detector	GEEREBAERT, yannick	
[49] A low noise front-end for the Belle2 forward electromagnetic calorimeter upgrade	BUDANO, Antonio TAGNANI, Diego CORRADI, Giovanni Prof. GALASSO, Matteo	
[174] Unified software architecture for the slow control system in the Belle II experiment	YAMADA, Satoru	
[118] BEAM TRANSPORT SYSTEM OF ALPI-PIAVE ACCELERATOR'S COMPLEX AT LNL EPICS BASED	Mr MONTIS, Maurizio GIACCHINI, Mauro	
[182] A general-purpose control functionality of DAQ-Middleware	Mr NAGASAKA, Yasushi	
[2] A DAQ Prototype for the ATLAS small-strip Thin Gap Chamber Phase-I Trigger Upgrade	Mr WANG, Xu	
[183] The Data Acquisition and Control System of EAST ME-SXR Diagnostic	Dr XIAO, Bingjia Dr WANG, Feng Dr LI, Shi CHEN, Ying	
[102] Current status of SPIDER CODAS and its evolution towards the ITER compliant NBI CODAS	MANDUCHI, Gabriele	
[25] Engineering Array Tests of High-Resolution Front End Electronics for Water Cherenkov Air Shower Detectors equipped with Cyclone V	SZADKOWSKI, Zbigniew	
[259] EMBEDDED IMPLEMENTATION OF A REAL-TIME SWITCHING CONTROLLER ON A ROBOTIC ARM	FERRÒ, Giuseppe	
[133] The DAQ System for a Beam Detection System Based on TPC-THGEM	Dr MA, SI	
[190] The waveform sample system based on TCP/IP transmission	Dr NING, Zhe	
[91] A Timing Synchronizer System for Beam Test Setups requiring Galvanic Isolation	MEDER, Lukas	
[194] Development of the electronics and data acquisition system for the triple-GEM detectors for the upgrade of the CMS forward muon spectrometer	DE LENTDECKER, Gilles	
[211] A new preprocessing and control board for the phase 2 electronics of AGATA experiment.	COLLADO RUIZ, Javier	

[138] Front-end Electronics for a Novel Boron-coated MWPC Thermal Neutron Detector	DI, Jiang	
[225] A Modular Data Acquisition System using the 10 GSa/s PSEC4 Waveform Recording Chip	BOGDAN, Mircea	
[26] A readout system for the Ground Tests of the DAMPE BGO Calorimeter	ZHANG, Junbin	
[68] Complete Parallel Readout VME DAQ System	Dr BABA, Hidetada	
[266] Development of the EPICS-based Monitoring and Control System for EAST Fast Control Power System	LI, SHI	
[264] The Data Acquisition Architecture for the "Dark matter Experiment using Argon Pulse-shaped discrimination - DEAP-3600 -	Mr AMAUDRUZ, Pierre-Andre	
[269] Development of a high dynamic range and wide bandwidth amplifier electronics system	Mr LI, Cheng	
[268] A Testbench Based On UVM Research For ABCstar	Prof. LIU, Zhen-An	
[58] Integration of a real-time node for magnetic perturbations signal analysis in the distributed digital control system of the TCV tokamak	GALPERTI, Cristian	
[56] mbspex and pexornet - linux device drivers for PCIe optical receiver data acquisition and control	ADAMCZEWSKI-MUSCH, Joern	
[51] Design of a 10-Gbps Random Number Recorder	WANG, Xinzhe	
	- [2] test 2	FONTAINE, Rejean
[199] Data acquisition system for the J-PARC E36 experiment	Dr IGARASHI, Youichi	
[200] The design of the readout system of two-dimensional position-sensitive GEM detector	YI, Liang	
[195] 20-Channel 14MeV Neutron Detector Electronics Readout System	Mr LIU, Shengquan	
[115] NEW DATA ARCHIVE SYSTEM FOR SPES PROJECT BASED ON EPICS ARCHIVER APPLAINACE	Mr MONTIS, Maurizio GIACCHINI, Mauro	
[117] Data transfer methods in Real Time controller of Ion Cyclotron High Voltage Power Supply	Mr DHOLA, Hitesh	
[89] A Small Data Aquisition System for the KOALA Experiment in Juelich	WÜSTNER, Peter	
[111] Error Field Measurement and Processing with FPGA in KTX Active Control System	Mr GUO, Xiaotian	
[113] Data Processing for EAST Remote Participation	Dr SUN, Xiaoyang	
[112] Trigger System for BaF2 Detector Array Readout Electronics at CSNS-WNS	JIANG, Di	
[82] The Least Mean Squares Adaptive FIR Filter for RFI Suppression in Radio Detectors of Cosmic Rays	SZADKOWSKI, Zbigniew	
[83] The Trigger- Time-Event-System for Wendelstein 7-X: Overview and first Operational Experiences	SCHACHT, Jörg	
[80] Evaluation Software for BaF2 Detector Array Electronics at CSNS-WNS	Mr WANG, Qi	
[141] FPGA baseline restorer based on high-speed ADC in particle emission rate measurement system	Mr TU, Zheng	
[3] Design of ultra-low noise power system for high-precision detectors	WANG, Jian	
[231] Development and Application of a general-purpose VME module using FMC	Dr ABE, Toshinori	
[245] Upgrade of End-cap TOF Trigger system on BESIII	Dr ZHAO, Jingzhou	

[244] Universal high-performance LO and CLK generation module for LLRF system receivers	Mr ZUKOCINSKI, Mateusz	
[247] Mechanism of gamma-induced absorption bands formation at 665 nm in KS-4V and KI type quartz glasses.	Dr FAYZULLAEV, B.S.	
[103] Distributed continuous event – based data acquisition using FlexRIO FPGA	TALIERCIO, Cesare	
[13] Preliminary Performance of a Continuous Crystal PET Detector with TODT Readout Scheme	Mr XIAO, Yong Prof. WANG, Yonggang	
[101] Controlling DAQ Electronics using a SCADA Framework	GRANADO CARDOSO, Luis	
[32] High Counting-rate Data Acquisition System for the Applications of PGNA	Mr LIU, Yuzhe	
[34] A Reconfigurable Ethernet-Based Data Acquisition and Processing System for Particle Physics Experiments	Mr WANG, Zhaoqi	
[158] Web-based parameter control and real-time waveform display for the GRIFFIN experiment	Mr SHAW, Bryerton	
[64] Test-driven software upgrade of the LHC beam-based feedback systems	LOURO ALVES, Diogo Miguel	
[177] Real Time controller for R&D on ITER Ion Cyclotron Heating & Current Drive Source	VERMA, Sriprakash	
[172] Data intercommunication layer and control system of ATLAS High Level Trigger	STOCKTON, Mark	
[249] New updates on the ATLAS ROD board implementation for Pixel Layer 1 and Layer 2	Mr GIANGIACOMI, Nico	
[96] Real Time Control of Electron Density on RFX-mod Tokamak Discharges.	CAVAZZANA, roberto	
[6] Data Chain Reconstructing Technology for the front-end electronics of the BESIII muon identification system	Mr ZHANG, xiaoguang	
[201] The series of high performance multichannel digitizers ADC4x250	KOTOV, Evgeniy	
[185] Study of Fast Data Access Based on Hierarchical Storage for EAST Tokamak	WANG, Feng	
[220] Multichannel DAQ IC with zero deadtime and extended input range for current pulse sensors	MAZUR, Dmytro	
[97] Real-time vertical plasma position control using the heavy ion beam diagnostic	Mr HENRIQUES, Rafael	
[142] Prototype for Low-cost FPGA TDC with High Precision and Density	Mr JIANG, Di	
[98] \$CLOUD^{CLOUD}\$: general-purpose instrument monitoring and data managing software	DIAS, Antonio	
[228] The monitoring board for the calibration system of the g-2 experiment	MASTROIANNI, Stefano	
[227] The laser control system for a calibration facility of light detector	MASTROIANNI, Stefano	
[93] A Monitoring System for the LHCb Dataflow	VIANA BARBOSA, Joao Vitor	
[223] Phase and Amplitude Drift Calibration of the RF Detectors in FELs LLRF Systems	CZUBA, Krzysztof	
[221] Multi-Channel FPGA-Based Data-Acquisition-System for Time-Resolved Single-Photon Counting in Synchrotron Radiation Experiments	Dr ZIOLKOWSKI, Michael	
[17] A FPGA-based Pulse Pile-up Rejection Technique for the Spectrum Measurement in PGNA	Dr CHEN, Lian	
[143] Intelligent FPGA Data Acquisition Framework	LEVIT, Dmytro	
[116] IFMIF EVEDA RFQ LOCAL CONTROL SYSTEM	GIACCHINI, Mauro	

[153] Front-end Electronics for BaF2 Detector Array at CSNS-WNS	Mr QI, Xincheng	
[159] Intelligent FPGA based Event Builder and Data Acquisition System for the COMPASS experiment.	KONOROV, Igor	
[234] Trigger system for a large area RPC TOFtracker	Prof. CLEMÊNCIO, Filomena	
[16] 10-Gbps True Random Number Generator Accomplished in ASIC	WANG, Xinzhe	
[224] Development of theCBM RICH readout and DAQ	OVCHARENKO, Egor	
[42] PLAS: A 32-channel, dead time-less analog memory ASIC fpr the TRACE detector	ALIAGA, Ramon J.	
[43] Detection of Neutrino-Induced Air Showers by the Artificial Neural Network FPGA Trigger	SZADKOWSKI, Zbigniew	
[9] Two-dimensional encoded multiplexing readout with a 5x5cm2 THGEM	Mr YUAN, guangyuan	
[204] Portable calibration node for LHAASO KM2A detector array	Dr GONG, Guanghua	
[140] The study of strip Readout Prototype for ATLAS Phase-I muon Trigger upgrade	LI, Feng	
[206] An Ultra-fast Linear Array Detector for MHz Line Repetition Rate Spectroscopy	Dr CASELLE, Michele	
[149] The BGO system for Real Time Beam Background Monitoring in BEAST II	Dr SHIU, Jing-Ge	
[77] FPGA based RF and piezo controllers for SRF cavities in CW mode	Dr CICHALEWSKI, Wojciech	
[73] The Implementation of KTX Central Control System	ZHANG, Zuchao	
[263] Experience with a Slow Control system based on industrial process control hardware and software for the Xenon1T Dark Matter Search	CARDOSO, Joao	
[41] Readout System with 2-Channel 8-Bit 1GHz FADC and Gigabits Ethernet Based on RAIN1000Z1 ZYNQ Module for Crystal Detector	Dr XUE, Tao	
[258] An Agile Quality Assurance Framework for the Development of Fusion Real-time Applications	Dr CABRITA NETO, Andre	