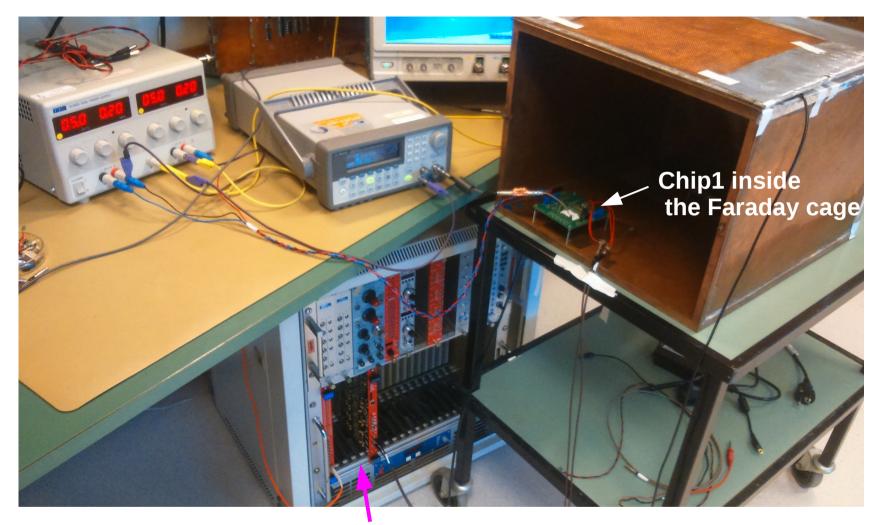
Pulse shape stability - Chip1

SAMPA test meeting - 6th May 2015

Ganesh Tambave

University of Bergen, Bergen, Norway On Behalf of Norwegian group

Test setup - Chip1

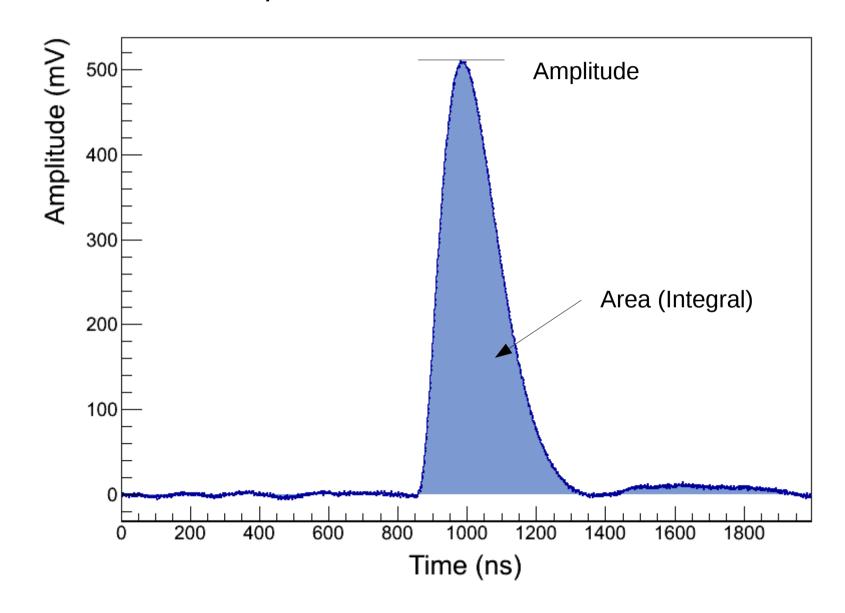


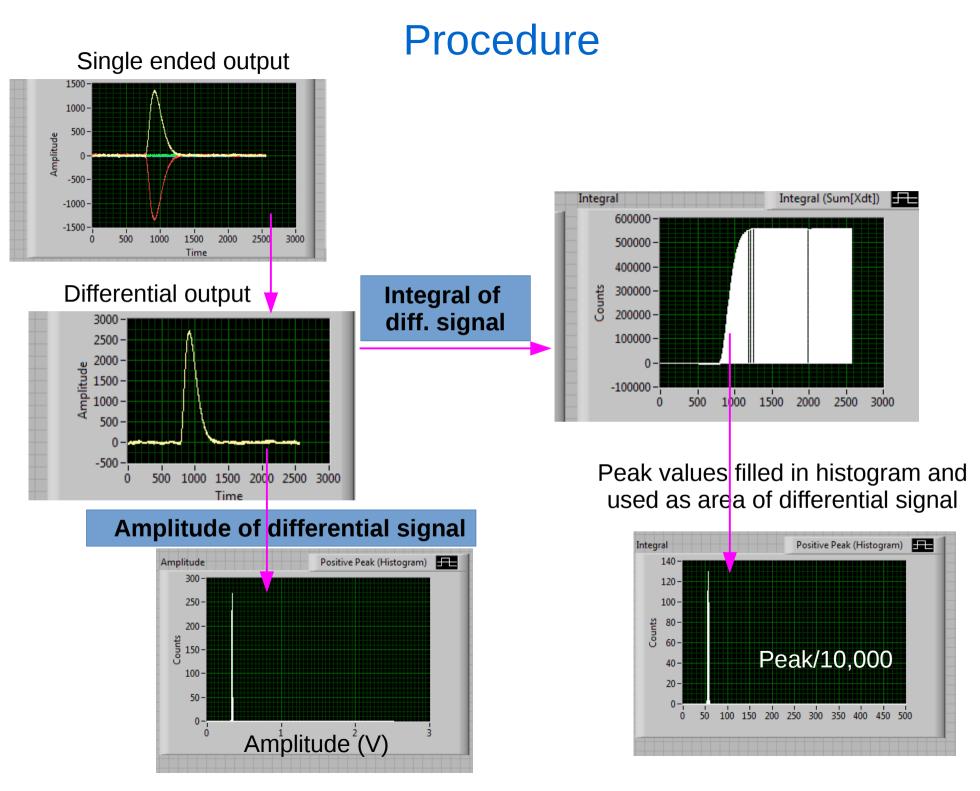
VEM based DAQ system – ADC 14 bit, 1 GS/s

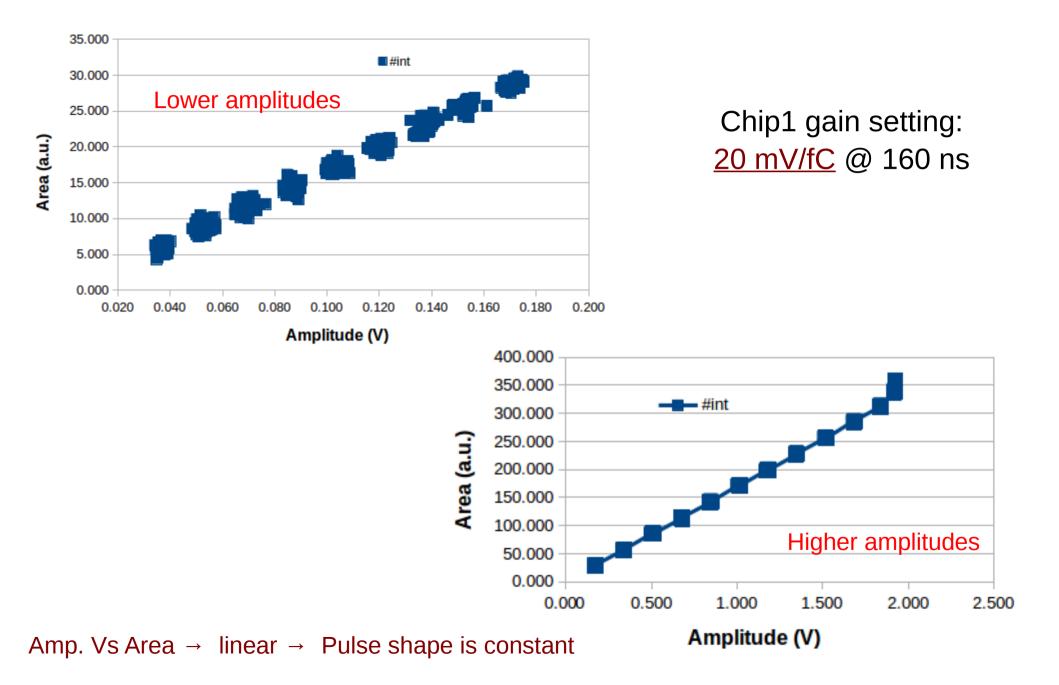
- Pulse generator settings:
 - Ramp shaped waveforms with 10 kHz frequency
 - Amplitude varied from 2 mV to 120 mV (2 fC to 120 fC)
- Chip1-ch#5: gain 20 mV & 30 mV/fC @160 mV and negative polarity

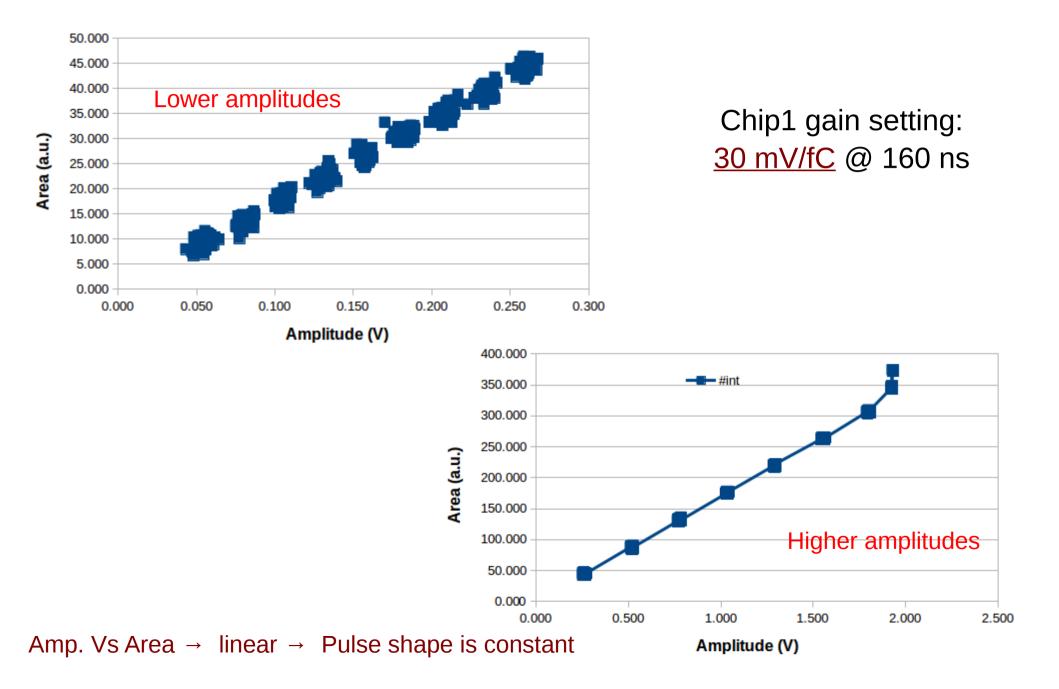
Procedure

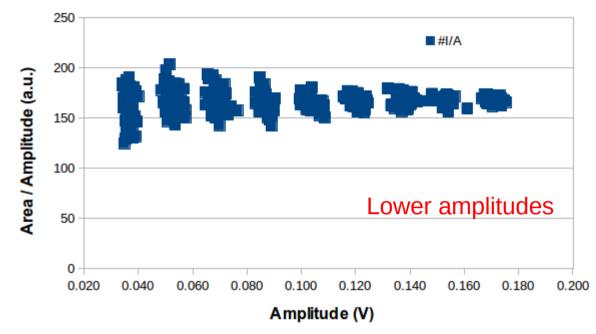
For stable pulse shape the ratio of Area/Amplitude should be constant



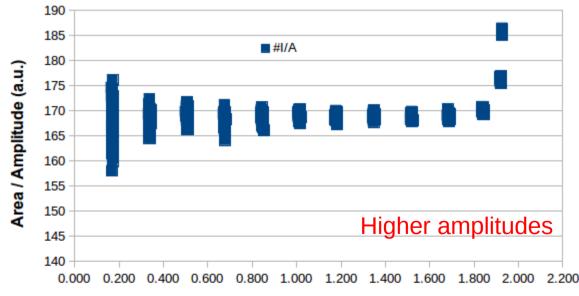






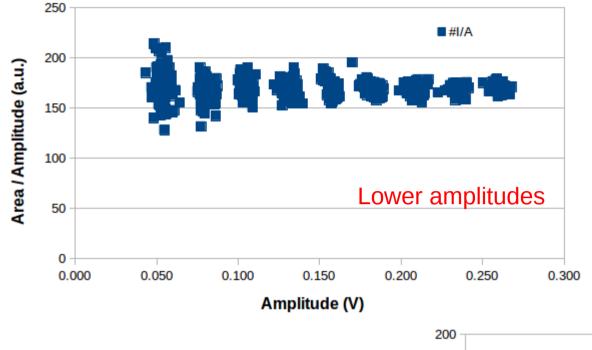


Chip1 gain setting: <u>20 mV/fC</u>@ 160 ns

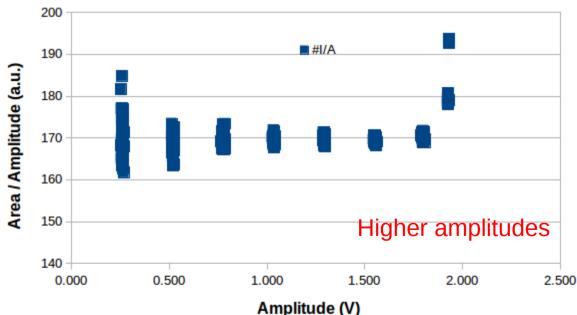


- Pulse shape is stable
- The fluctuations are more for low pulse amplitudes
- SNR is poor for low pulse amplitudes

Amplitude (V)



Chip1 gain setting: <u>30 mV/fC</u>@ 160 ns



- Pulse shape is constant
- The fluctuations are more for low pulse amplitudes
- SNR is poor for low pulse amplitudes