

Detecting low energetic X-rays with GridPix Detectors

Jochen Kaminski

University of Bonn

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From Micromegas to GridPix

MM invented by Y. Giomataris, et al. (NIMA 376, p. 29-35, 1995)

Two stage parallel plate detector:

- Ionization in drift volume
- Gas amplification in thin gap with high electric field

Standard charge collection:

- Pads of several mm²
- \cdot Long strips (\sim 10 cm, pitch \sim 200 µm)

Could the spatial resolution of single electrons be improved?

Ar:CO $_{_2}$ 70:30 $\,\rightarrow\,$ D $_{_{\rm t}}$ = 187 μ m/ $\sqrt{$ cm $\,\rightarrow\,$ σ = 21 μ m

Ar:CH₄ 90:10 \rightarrow D_t = 208 µm/ \sqrt{cm} \rightarrow σ = 24 µm Ar:iButan 95:5 → D_{t} = 211 µm/ $\sqrt{\text{cm}}$ → σ = 24 µm

Smaller pads/pixels could result in better resolution!

Timepix Chip

Available for tests since Nov. 2006 Number of pixels: 256×256 pixels Pixel pitch: $55 \times 55 \text{ }\mu\text{m}^2$ Chip dimensions: 1.4×1.4 cm² $ENC:$ \sim 90 e^-

Limitations: no multi-hit capability, charge and time measurement not possible for one pixel. Each pixel can be set to one of these modes: $TOT = time over threshold (charge)$ Time between hit and shutter end

Timepix readout

There are several readout systems optimized for different applications available for purchase. We have built one based on the Scalable Readout System of RD51, because it is easy to scale \bigcirc and cheap, if you have the main components already. Idea of SRS: produce a flexible readout electronics, which can handle different chips (new FPGA code, chip carrier), which many groups can use. New C-Card, intermediate board, and chip carriers were designed for Timepix. Now up to 32 Timepix chips can be used per FEC/C-card.

A small-size system using the same FPGA code and most of the hardware can be based on a virtex evaluation board. This is used in CAST.

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Protection Layer

Discharge triggered for example by highly ionizing particles could easily destroy the chip. The charge collected by one pixel was too high.

A protection layer is placed on the chip to disperse the charge on many pixels and thus lower the input current per pixels. Besides, the charge is removed slowly and thus quenches the discharge.

high resistive material 15 μm aSi:H (~10¹¹ Ω·cm) 8 μm Si $_{\rm \chi}$ N $_{\rm \gamma}$ (~10 $^{\rm 14}$ Ω·cm)

Chips survives several thousand discharges triggered by αs.

Optimization of InGrids

Detailed studies have been performed to optimize the layout of the structure. (NIMA 591, pp. 147, 2008, PhD. Thesis of M. Chefdeville, NIKHEF)

Also the layout of the supporting structures (pillars and dykes) was optimized to give the highest mechanical strength.

The influence of the gap size and hole diameters on gain, energy resolution, ion feedback and collection efficiency were measured.

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Production at Twente was based on 1 - 9 chips process. This could not satisfy the increasing demands of R&D projects. A new production was set up at the Fraunhofer Institut IZM at Berlin. This process is wafer-based \rightarrow 1 wafer (107 chips) is processed at a time.

Wafer-based Production **F** Fraunhofer

1. Formation of $\text{Si}_{\mathsf{x}}\text{N}_{\mathsf{y}}$ protection layer

- 2. Deposition of SU-8
- 3. Pillar structure formation
- 4. Formation of Al grid
- 5. Dicing of Wafer

6. Development of SU-8

Main challenges: - Formation of layers, in particular protection layer

- Deposition of Al
- Final development of SU-8 \rightarrow still chip-based

Institute for Nanotechnology

SiRN should not cover bond pads

First tests: mechanical mask \rightarrow failed due to thermal stress **Better: poyimide mask chem.** removed

After development of pillars, the grid is too fragile for dicing

Time consuming

SEM Pictures **Example**

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Building an X-ray detector

Timepix with protection layer and InGrid on a chip carrier

Building an X-ray detector

Readout module

Field shaping electrode (anode)

Calculation of field distortions from nominal 500 V/cm E-field.

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 -53.9062 -52.8125 -51.7188 -50.625 -49.5312 -48.4375 -47.3438

Pressure on outer side of cathode: 10-5 mbar Pressure inside detector: 1050 mbar Window: 2 μ m aluminized Mylar (with strongback)

Entrance Window

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In principle photons of energy ~26 eV could be detected. But single pixels could be mistaken for noise \rightarrow three pixels close by are probably enough. But photons of 78 eV do not pass through the window. They have to be produced internally.

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Tests with variable X-ray generator

CAST Detector Lab has an X-ray tube with exchangeable targets and filter wheels. => monochromatic X-ray lines down to few hundred eV. Vacuum system allowing for differential pumping

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Energy calibration

Pixel counting starts failing, if diffusion is not large enough and more than 1 electron ends up on a pixel.

Energy measurement based on collected charge still works fine.

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Energy resolution

Different gas mixtures, electrical fields and gas gains were studied and good setting for the energy resolutions were found.

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Application CAST

Decommissioned LHC-magnet is pointed to the sun. Axions produced in the sun convert into

axions

Detect X-rays with high efficiency.

- \rightarrow Ar-mixture, 1 bar, 3 cm conversion volume Suppress background as much as possible
- \rightarrow radiopure material lead absorber, distinguish tracks from γs

Application CAST

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Application LCTPC

The ILC or CLIC are possible successors of the LHC colliding e⁺ and e⁻ at 300 GeV – 3 TeV. One of the two detector concepts foresees a large Time Projection Chamber as a central tracking device.

Micropattern gas amplification stages are needed to fulfill requirements.

size of endcaps \sim 10 m² 8 rows of MPGD detector modules; module size \sim 17×22 cm² 240 modules per endcap

To readout TPC with InGrids, one needs ~100 chips per module \rightarrow 2000-2500 per endcap

Test with 160 GridPix detectors

- 3 module with 20 Octoboards (160 chips) were constructed,
- central module with 96 chips
- 2 outer modules with 32 chips each

Some of the challenges are:

- LV distribution possibly up to 85 A $@$ 2.2 V
- Cooling
- InGrid production
- Bonding on boards
- Synchronized readout

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Test Beam Results

 $\sim 10^6$ frames recorded at 5 Hz in Ar: CF_4 : IC_4H_{10} 95:3:2, B = 0/1 T Test beam program: – Voltage scans (gas gain) – z-scan, momentum scan – Different angles The analysis has started Previous test beam results have shown, that the spatial resolution is dominated by diffusion only

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Outlook

Timepix3 is available and is expected to give much better results than Timepix:

- Better time resolution (1.7 ns)
- Multi-hit capable
- Can measure time and charge for each pixel
- Much higher rate
- Ready for Through-silicon-vias

NIKHEF has developed a readout system for single Timepix3 chips (SPIDR). First tests with standard Micromegas on top were successful. Implementation in SRS is planned within AIDA2020.

Further R&D:

- Pillars made of ceramics (e.g. SiO) for sturdier structure.
- Grids made of resistive materials for higher spark protection
- Through-silicon-via technology to reduce dead area between chips.

Summary

InGrids have shown excellent performance:

Energy resolution of $\sigma_{\rm g}^{\,}/E$ ~ 3.85 % (at 5.9 keV)

Spatial resolution only limited by diffusion.

High efficiency for single electron detection.

Production techniques are well advanced, a few details have to be improved. Several readout systems optimized for compactness, speed or

large systems have been developed and are commercially available.

Large systems (~160 chips) have been operated with a new readout system in a test beam earlier this year.

Further R&D on InGrids are planned (resistive grid, all ceramic,...) New Timepix-3 is available and shows good results.

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