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Neuromorphic silicon chips

Wednesday 11 November 2015 11:30 (45 minutes)

Neuromorphic silicon chips have been developed over the last 30 years, inspired by the design of biological nervous systems and offering an alternative paradigm for computation, with real-time massively parallel operation and potentially large power savings with respect to conventional computing architectures. I will present the general principles with a brief investigation of the design choices that have been explored, and I'll discuss how such hardware has been applied to problems such as classification.

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