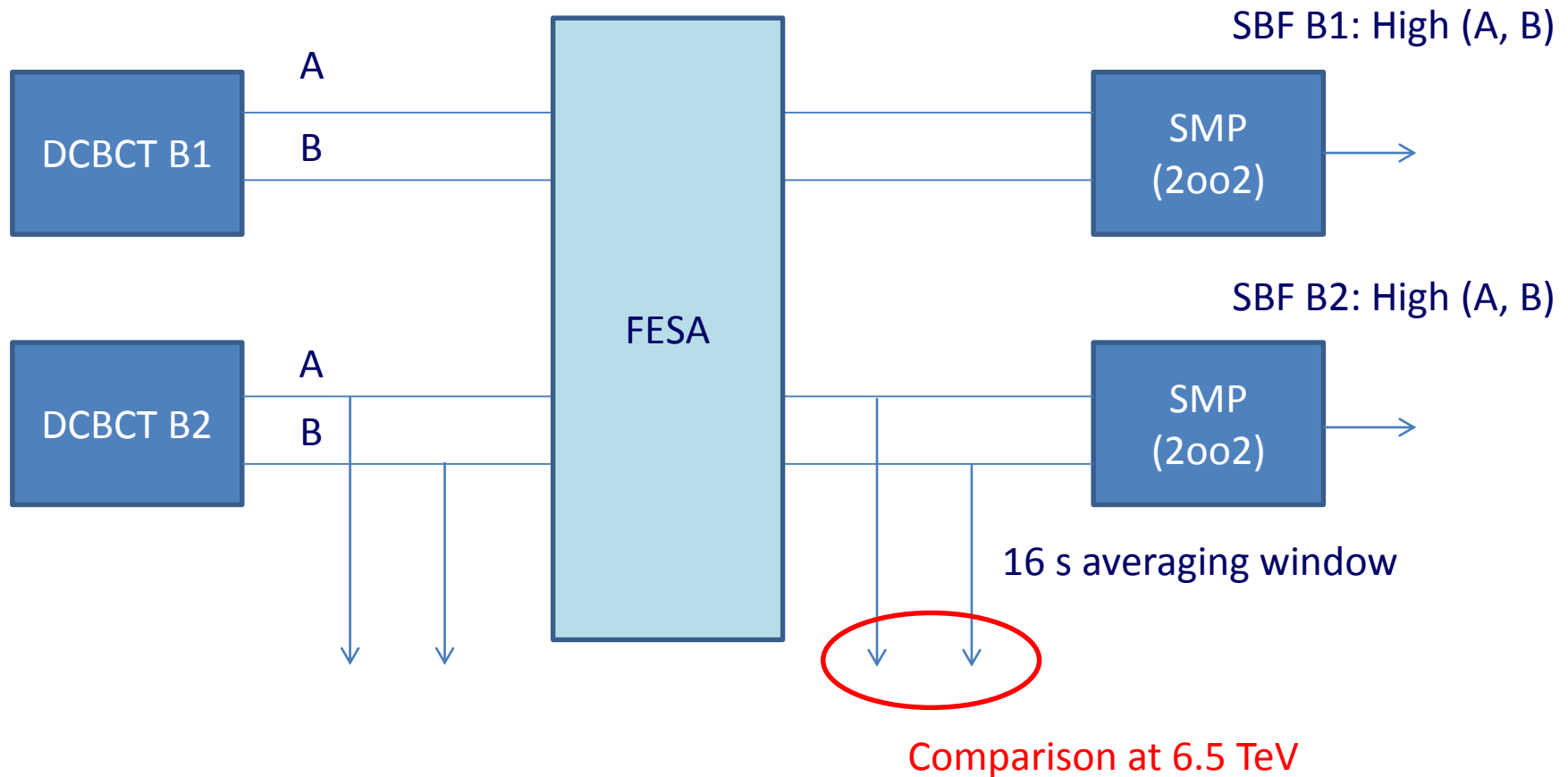


# **DCBCT signals from the SMP before and after logic change**

**A. Apollonio, S. Gabourin**

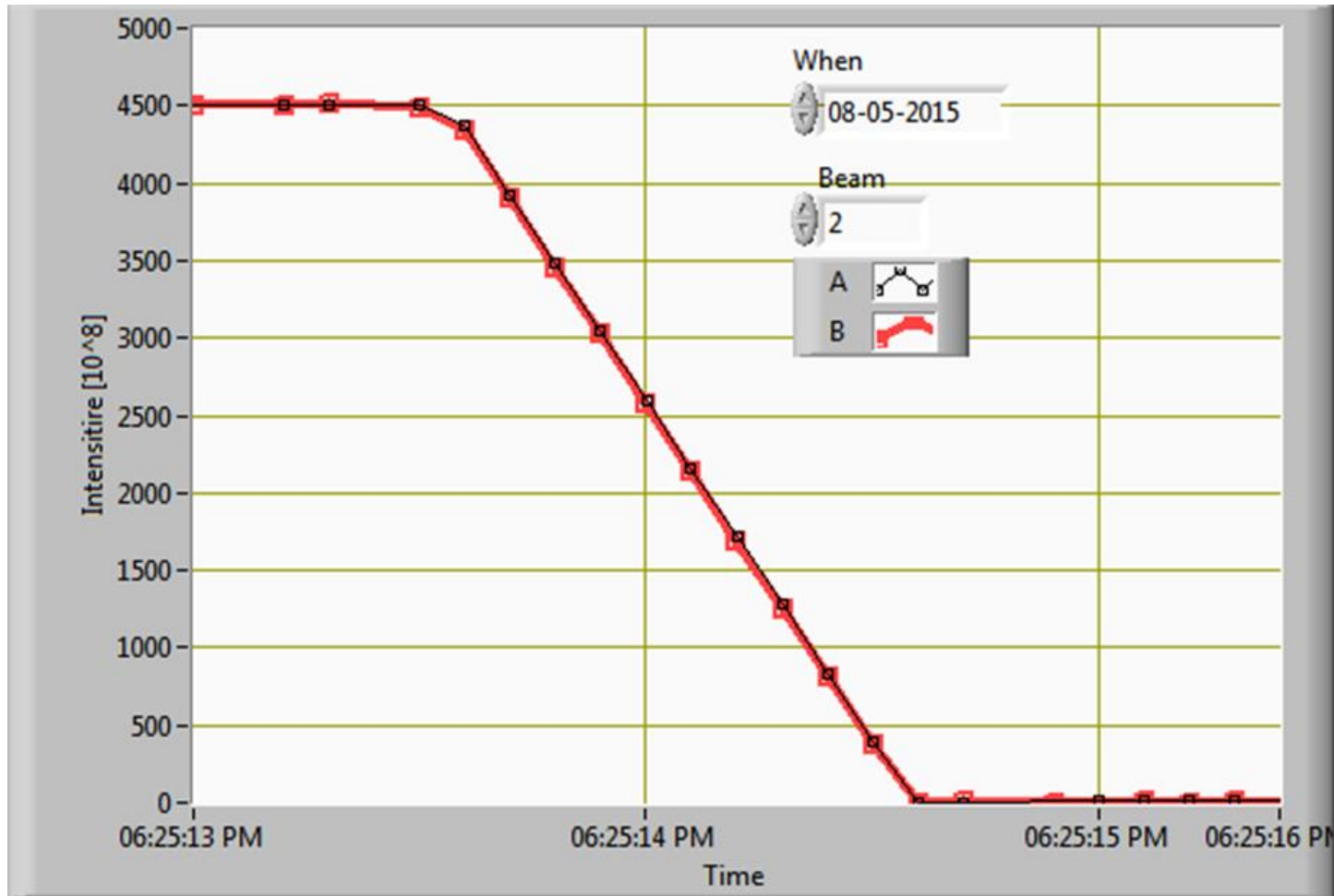
**29/05/2015**

# SMP Input Signals

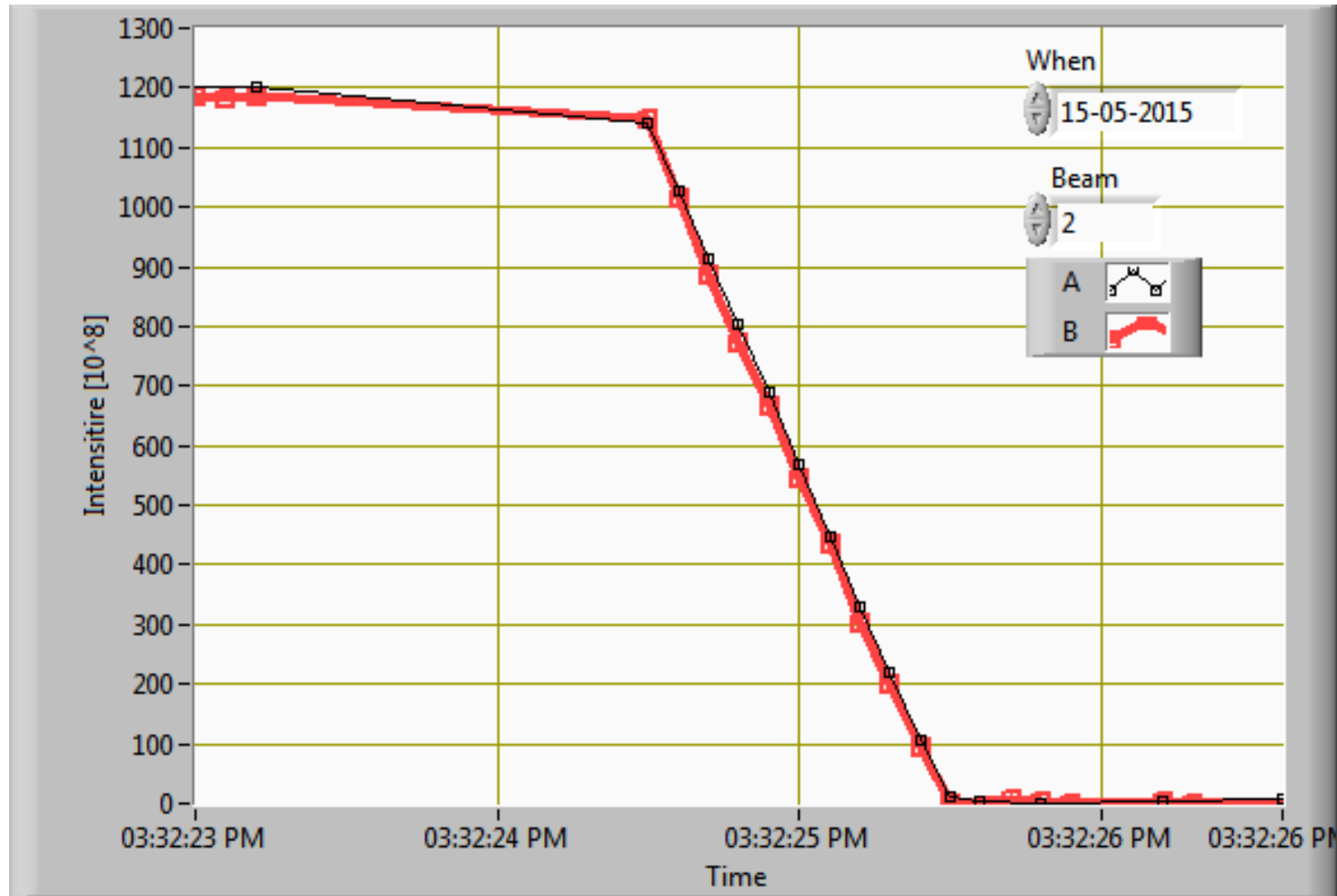


- No significant difference expected before the change
- Visible difference after the change (e.g. after a beam dump)

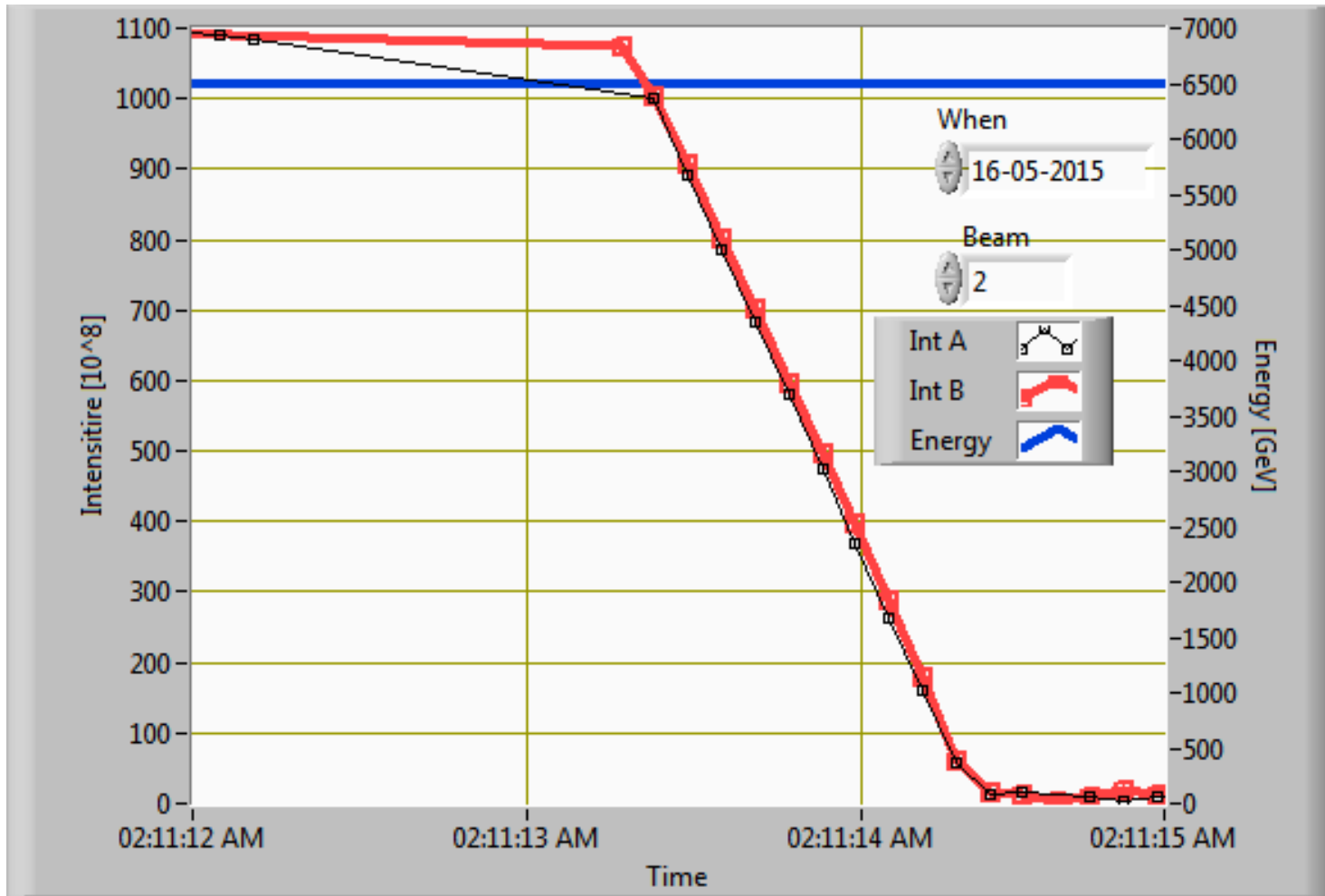
- Individual SMP variables not readable from the logging database
- **Raw history buffer data** (to be decoded: Labview + Matlab)
- Selection of events **before/after** the change on the DCBCT side
- **SBF data** for comparison (to be done)



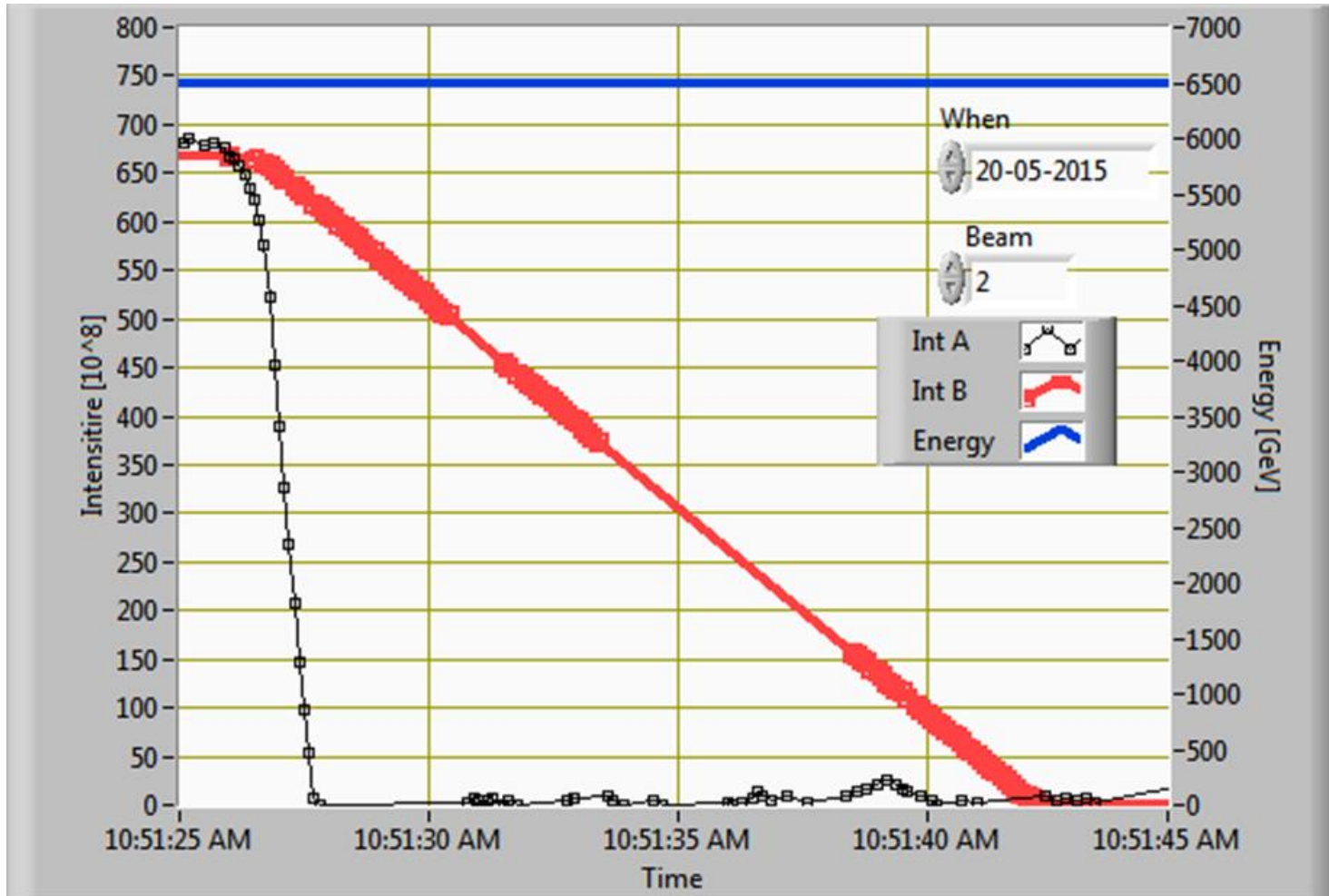
- No evidence of 16 s average window



- No evidence of 16 s average window



- No evidence of 16 s average window



- Clear evidence of 16 s average window on B2 B

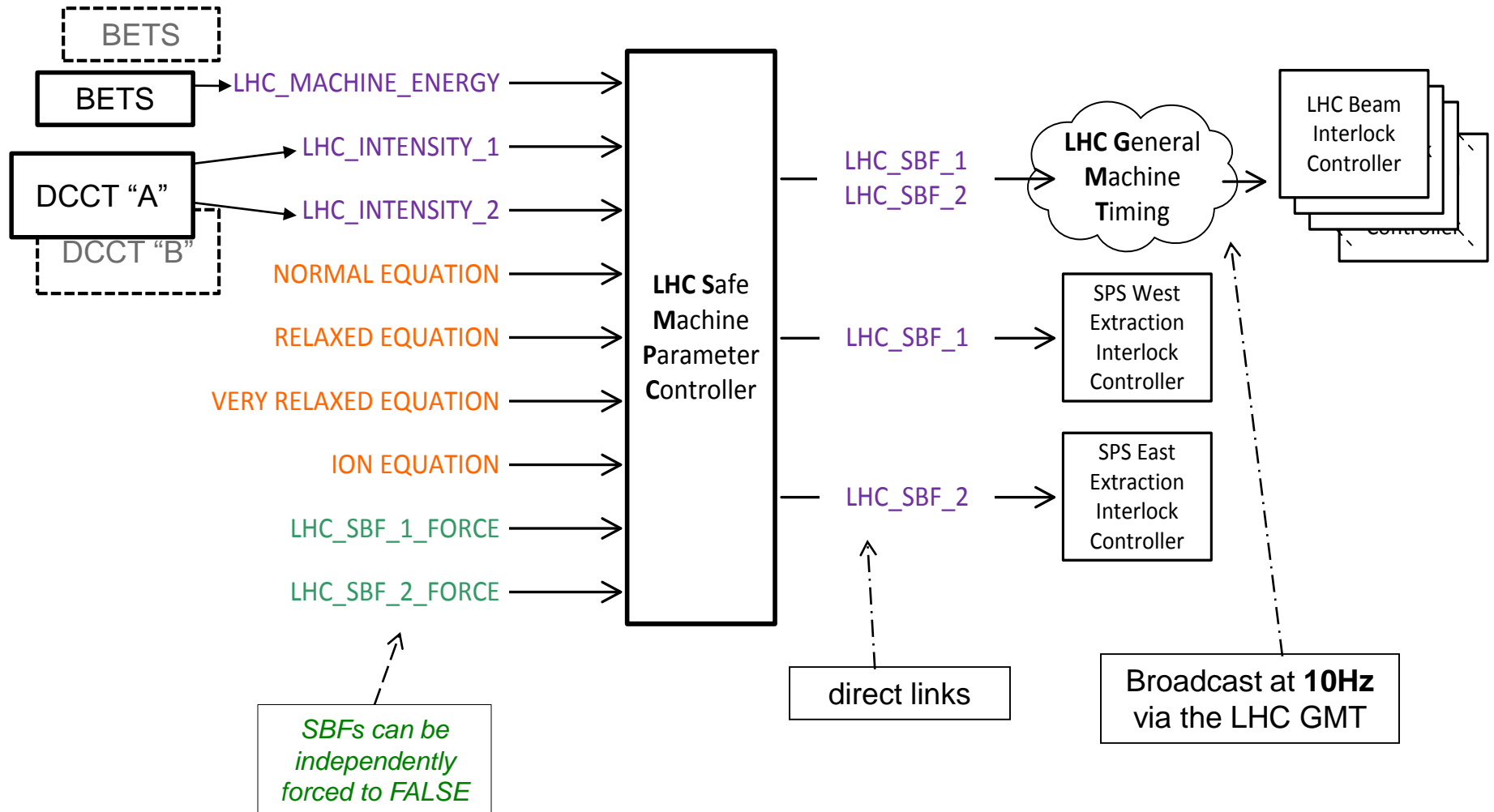
- ❑ Cross-check from the SMP side confirms the 16 s averaging window
- ❑ SBF: 2oo2 logic + highest value taken → no implications for MPS
- ❑ No expected dumps in the future due to noise on the DCBCT
- ❑ Normal settings of the SMP should now be enforced



**THANKS A LOT FOR  
YOUR ATTENTION!**



# LHC Setup Beam Flag: implementation



B. Todd