

openlab-Intel software tools workshop 2015

Report of Contributions

Contribution ID: 0

Type: **not specified**

Introduction and technicalities

Presenters: RADEMAKERS, Fons (CERN); SZOSTEK, Pawel (CERN)

Contribution ID: 1

Type: **not specified**

Introduction and technicalities

Thursday 9 July 2015 09:00 (15 minutes)

Presenter: SZOSTEK, Pawel (CERN)

Contribution ID: 2

Type: **not specified**

Intel architectures roadmap - what software developers need to know about advancements in hardware

Thursday 9 July 2015 09:15 (30 minutes)

Presenter: Mr ZITZLSBERGER, Georg (Intel)

Contribution ID: 3

Type: **not specified**

Parallel Programming concepts

Thursday 9 July 2015 09:45 (1h 30m)

Presenter: Mr MATVEEV, Zakhar (Intel)

Contribution ID: 4

Type: **not specified**

Effective compilation with Intel C++ Compiler

Thursday 9 July 2015 11:30 (30 minutes)

Presenter: Mr ZITZLSBERGER, Georg (Intel)

Contribution ID: 5

Type: **not specified**

Code Modernization - use cases

Thursday 9 July 2015 13:30 (30 minutes)

Presenter: Mr PABST, Hans (Intel)

Contribution ID: 6

Type: **not specified**

Intel Performance libraries (MKL, TBB, OpenMP4 and extensions)

Thursday 9 July 2015 14:00 (1 hour)

Presenter: Mr PABST, Hans (Intel)

Contribution ID: 7

Type: **not specified**

Hands-on lab: MKL, TBB, OpenMP and Compiler

Thursday 9 July 2015 15:00 (2 hours)

Presenter: Mr PABST, Hans (Intel)

Contribution ID: 8

Type: **not specified**

Introduction to vectorization in preparation of day 2

Contribution ID: 9

Type: **not specified**

Wrap-up of day 1 and Q&A

Thursday 9 July 2015 17:00 (40 minutes)

Contribution ID: **10**

Type: **not specified**

Intel VTune Amplifier training

Friday 10 July 2015 09:00 (1h 30m)

Presenter: Mr ZITZSLBERGER, Georg (Intel)

Contribution ID: 11

Type: **not specified**

Code vectorization using Intel Compiler and Intel Advisor

Friday 10 July 2015 10:45 (1h 15m)

Presenters: Mr ZITZLSBERGER, Georg (Intel); Mr MATVEEV, Zakhar (Intel)

Contribution ID: 12

Type: **not specified**

Hands-on lab: improving vectorization techniques with Intel Compiler and Intel Advisor - Part I

Friday 10 July 2015 13:30 (1 hour)

Presenters: Mr ZITZLSBERGER, Georg (Intel Corporation); Mr MATVEEV, Zakhar (Intel Corporation)

Contribution ID: 13

Type: **not specified**

Hands-on lab: improving vectorization techniques with Intel Compiler and Intel Advisor - Part II

Friday 10 July 2015 14:30 (2 hours)

Presenters: Mr ZITZLSBERGER, Georg (Intel Corporation); Mr MATVEEV, Zakhar (Intel)

Contribution ID: 14

Type: **not specified**

Wrap-up, Q&A and follow-ups

Friday 10 July 2015 16:30 (30 minutes)