

**Session Program**

**12-16 Sept 2005**

**11th Workshop on Electronics for LHC and  
future Experiments**

***Parallel session A2***

Heidelberg  
Germany

Tuesday 13 September

14:15

Parallel session A2

Session | Location: Heidelberg, Germany

14:15-14:40

Evaluation of the Radiation Tolerance of SiGe Heterojunction Bipolar Transistors Under 24GeV Proton Exposure

Speaker

Mr N. Spencer

14:40-15:05

Irradiation tests of the complete ALICE TPC Front-End Electronics chain

Speaker

Mr Ketil Røed

15:05-15:30

A radiation-tolerant LDO voltage regulator for HEP applications

Speaker

Dr Federico Faccio

15:30-15:55

The TRAcklet Processor chip for the ALICE TRD

Speaker

Dr Venelin Angelov

15:55