

**Session Program**

**12-16 Sept 2005**

**11th Workshop on Electronics for LHC and  
future Experiments**

***Plenary session P8***

Heidelberg  
Germany

# Friday 16 September

09:00

## Plenary session P8

Session | Location: Heidelberg, Germany

09:00-09:45

### Implementing Artificial Neural Networks in Mixed-Mode VLSI

**Speaker**

Prof. Joannes Schemmel

09:45-10:10

### Recent Progress in Field Programmable Gate Arrays

**Speaker**

Dr Peter ALFKE

10:10-10:35

### FPGA Dynamic Reconfiguration in ALICE and beyond

**Speaker**

Mr Gerd Troeger

10:35